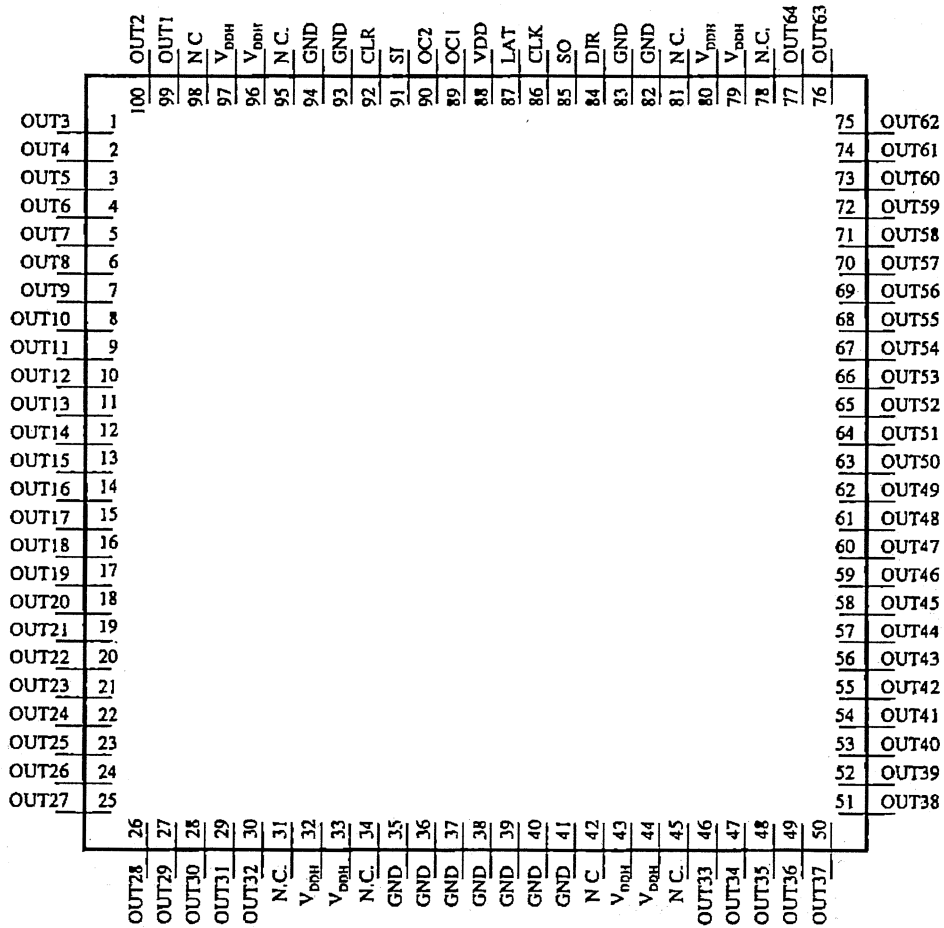


AN16021AA-K

A • Mod Ucom

• Pin Arrangement (Top view)



B

C

D

E

F

● Pin Function (1/2)

Pin No.	Pin Name	Type	Discription
1	OUT3	Output	High-voltage push-pull output pin
2	OUT4		
3	OUT5		
4	OUT6		
5	OUT7		
6	OUT8		
7	OUT9		
8	OUT10		
9	OUT11		
10	OUT12		
11	OUT13		
12	OUT14		
13	OUT15		
14	OUT16		
15	OUT17		
16	OUT18		
17	OUT19		
18	OUT20		
19	OUT21		
20	OUT22		
21	OUT23		
22	OUT24		
23	OUT25		
24	OUT26		
25	OUT27		
26	OUT28		
27	OUT29		
28	OUT30		
29	OUT31		
30	OUT32		
31	NC	-	Not connected
32	VDDH	Supply	High-voltage circuit supply pin
33	VDDH	Supply	High-voltage circuit supply pin
34	NC	-	Not connected
35	GND	Ground	GND pin
36	GND		
37	GND		
38	GND		
39	GND		
40	GND		
41	GND		
42	NC	-	Not connected
43	VDDH	Supply	High-voltage circuit supply pin
44	VDDH	Supply	High-voltage circuit supply pin
45	NC	-	Not connected
46	OUT33	Output	High-voltage push-pull output pin
47	OUT34		
48	OUT35		
49	OUT36		
50	OUT37		
51	OUT38		
52	OUT39		
53	OUT40		
54	OUT41		
55	OUT42		
56	OUT43		
57	OUT44		
58	OUT45		

● Pin Function (2/2)

Pin No.	Pin Name	Type	Discription															
59	OUT46	Output	High-voltage push-pull output pin															
60	OUT47																	
61	OUT48																	
62	OUT49																	
63	OUT50																	
64	OUT51																	
65	OUT52																	
66	OUT53																	
67	OUT54																	
68	OUT55																	
69	OUT56																	
70	OUT57																	
71	OUT58																	
72	OUT59																	
73	OUT60																	
74	OUT61																	
75	OUT62																	
76	OUT63																	
77	OUT64																	
78	NC	-	Not connected															
79	DDDH	Supply	High-voltage circuit supply pin															
80	DDDH	Supply	High-voltage circuit supply pin															
81	NC	-	Not connected															
82	GND	Ground	GND pin															
83	GND	Ground	GND pin															
84	DIR	Input	Setup pin of sift register sift direction L: Shift into reverse (SO → SI) H: Shift forward (SI → SO)															
85	SO	Input	Serial data input/output pin															
86	CLK	Input	Serial clock input pin Fetch SI or SO data to sift register by CLK rise edge															
87	LAT	Input	LAT data input pin L: Transfer shft register data to output latch H: Hold data to output latch															
88	VDD	Supply	Logic supply pin															
89	OC1	Input	Output control pin Control output according to the right truth value table															
90	OC2																	
			<table border="1"> <thead> <tr> <th>OC1</th> <th>OC2</th> <th>OUT</th> </tr> </thead> <tbody> <tr> <td>L</td> <td>L</td> <td>ALL Hi-Z</td> </tr> <tr> <td>L</td> <td>H</td> <td>DATA</td> </tr> <tr> <td>H</td> <td>L</td> <td>ALL L</td> </tr> <tr> <td>H</td> <td>H</td> <td>ALL H</td> </tr> </tbody> </table>	OC1	OC2	OUT	L	L	ALL Hi-Z	L	H	DATA	H	L	ALL L	H	H	ALL H
OC1	OC2	OUT																
L	L	ALL Hi-Z																
L	H	DATA																
H	L	ALL L																
H	H	ALL H																
91	SI	SI	Input/OutputSerial data input/output pin															
92	CLR		All output reset pin CLR pin: L → Normal operation CLR pin: H → All output High															
93	GND	Ground	GND pin															
94	GND	Ground	GND pin															
95	NC	-	Not connected															
96	VDDH	Supply	High-voltage circuit supply pin															
97	VDDH	Supply	High-voltage circuit supply pin															
98	NC	-	Not connected															
99	OUT1	Output	High-voltage push-pull output pin															
100	OUT2	Output	High-voltage push-pull output pin															