

# DATA SHEET

Part No.	AN26261A
Package Code No.	ULGA031-W-3525

Maintenance/Discontinued  
(planned maintenance type, maintenance type, planned discontinued type, discontinued type)  
Maintenance/Discontinued includes following four Product lifecycle stage.

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# AN26261A

## Transmit RF IC for WCDMA (Dual Band)

### ■ Overview

- AN26261A is WCDMA transmit RFIC that is planned to use for dual band WCDMA in Japan.  
AN26261A is consist of direct conversion modulator, voltage gain control amplifiers, VCOs and synthesizer.  
There is able to build the WCDMA transmit RF block with external PA, pre-driver with gain control amplifier and RF-filter.
- This IC is expecting to use with the WCDMA receive IC; AN26260A.

### ■ Features

- Direct modulation transmit RFIC for dual band WCDMA with the on-chip VCOs.
- Transmit frequencies : 1920 MHz to 1980 MHz, 830 MHz to 855 MHz.
- Current consumption : 45 mA (Typ.) .

### ■ Applications

- WCDMA single, dual band terminals.

### ■ Package

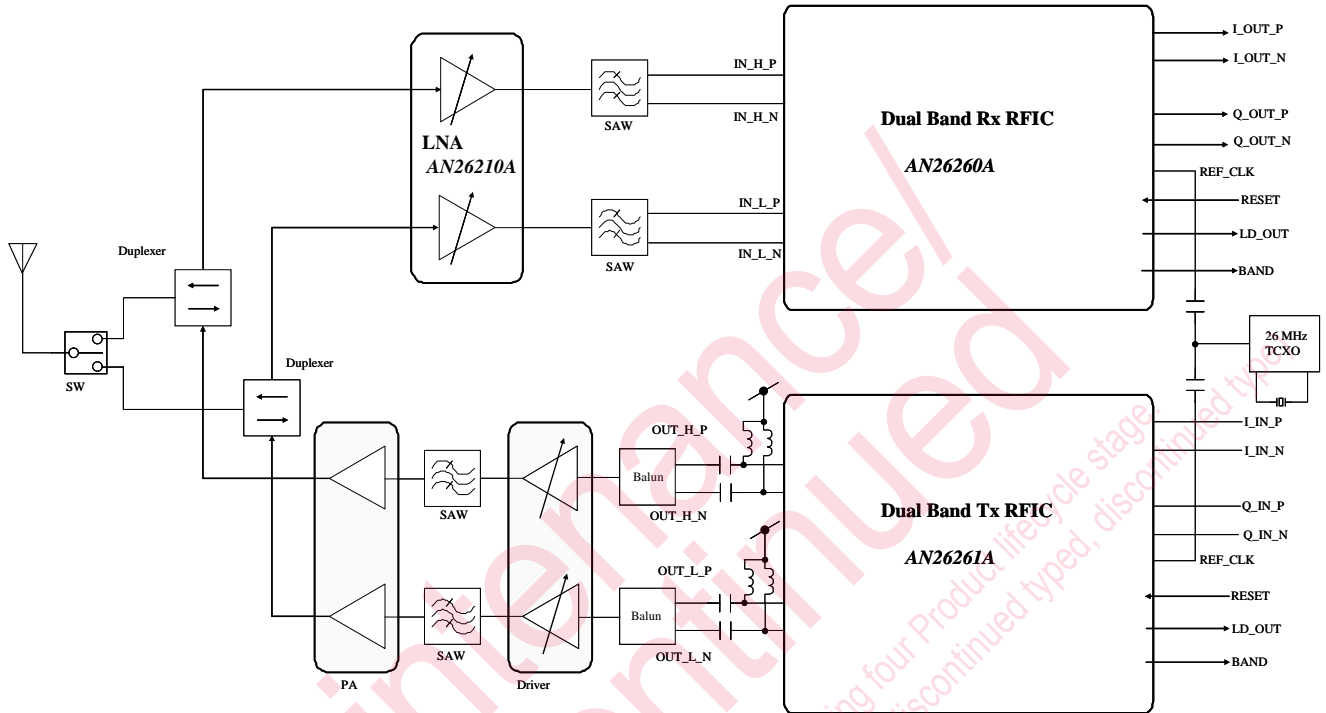
- Wafer level chip size package (WLCSP).  
Size :  $2.47 \times 3.47 \times 0.8 \text{ mm}^3$ .

### ■ Type

- SiGe monolithic Bi-CMOS IC.

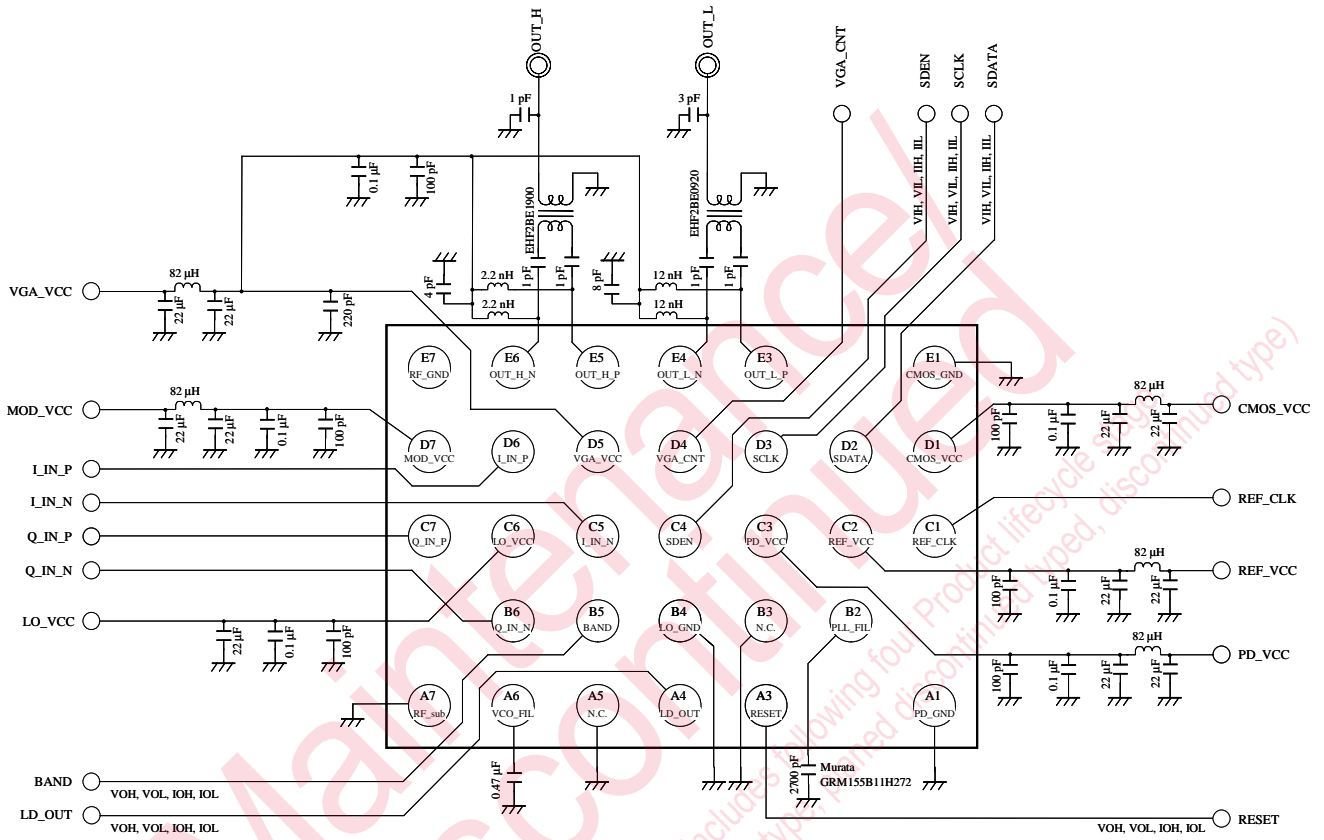
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■ Application Circuit Example



Main Engineering  
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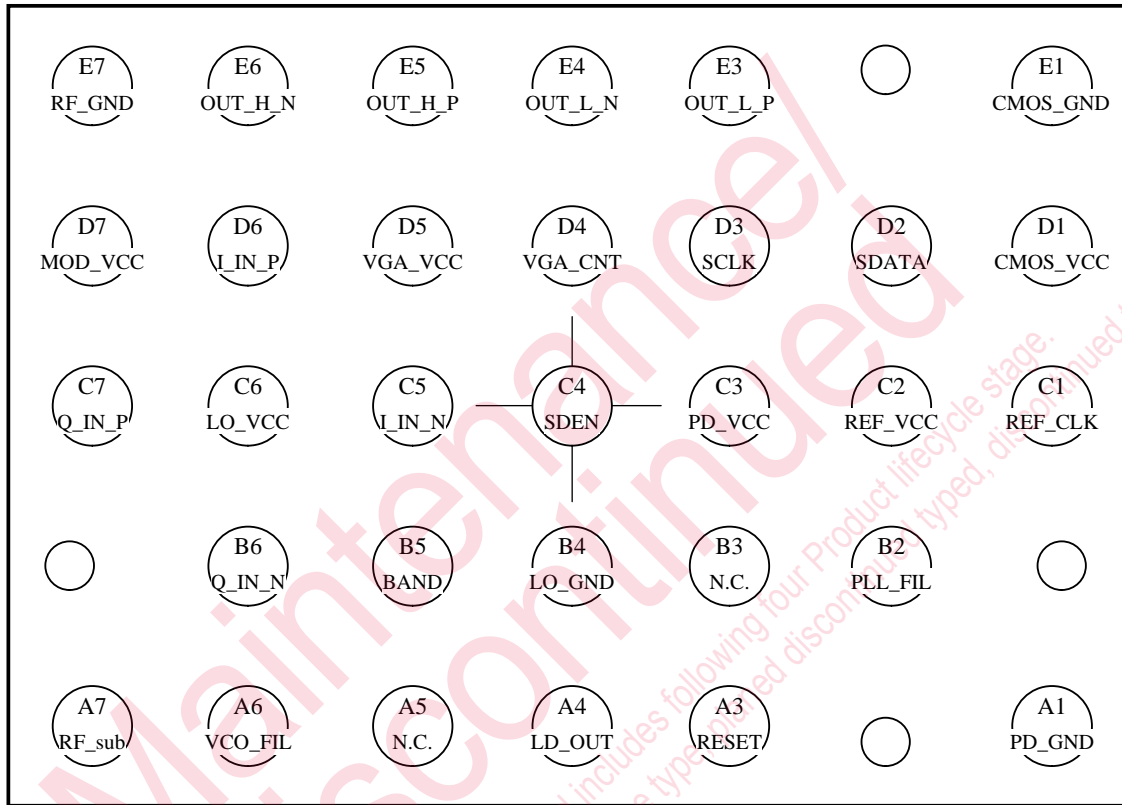
■ Test Circuit Diagram (Top View)



■ Pin Out

The figure below shows pin layout is ' top view '.

Top view



## ■ Pin Descriptions

Pin No.	Pin name	Type	Description
A1	PD_GND	Ground	Phase detector ground
A3	RESET	In	Reset signal input
A4	LD_OUT	Out	Synthesizer lock detector output
A5	N.C.	—	No connection or ground
A6	VCO_FIL	InOut	VCO ripple filter
A7	RF_sub	Ground	RF substrate
B2	PLL_FIL	InOut	PLL loop filter
B3	N.C.	—	No connection or ground
B4	LO_GND	Ground	Local ground
B5	BAND	Out	Band selector output
B6	Q_IN_N	In	Q channel baseband negative input
C1	REF_CLK	In	Reference clock input
C2	REF_VCC	Supply	Reference clock amplifier supply
C3	PD_VCC	Supply	Phase detector supply
C4	SDEN	In	Serial enable
C5	I_IN_N	In	I channel baseband negative input
C6	LO_VCC	Supply	Local supply
C7	Q_IN_P	In	Q channel baseband positive input
D1	CMOS_VCC	Supply	CMOS logic supply
D2	SDATA	In	Serial data input
D3	SCLK	In	Serial clock input
D4	VGA_CNT	In	Variable gain control amplifier control voltage input
D5	VGA_VCC	Supply	Variable gain control amplifier supply
D6	I_IN_P	In	I channel baseband positive input
D7	MOD_VCC	Supply	Modulator supply
E1	CMOS_GND	Ground	CMOS logic ground
E3	OUT_L_P	Out	800 MHz band positive output
E4	OUT_L_N	Out	800 MHz band negative output
E5	OUT_H_P	Out	2 GHz band positive output
E6	OUT_H_N	Out	2 GHz band negative output
E7	RF_GND	Ground	RF ground

### ■ Absolute Maximum Ratings

A No.	Parameter	Symbol	Rating	Unit	Notes
1	Supply voltage	$V_{CC}$	0 to 3.6	V	*1
2	Supply current	$I_{CC}$	64.5	mA	—
3	Power dissipation	$P_D$	59.8	mW	*2
4	Operating ambient temperature	$T_{opr}$	-25 to +85	°C	*3
5	Storage temperature	$T_{stg}$	-55 to +125	°C	*3
6	DC input voltage	VI	0 to $V_{CC} + 0.3$ and less than 3.6	V	*1,*4
7	I,Q DC differential input voltage	VID	-2 to 2	V	*1,*5

Note) \*1: The supply voltage is shown the value under the condition which not exceeds the absolute maximum ratings and the power dissipation.

\*2: The power dissipation is shown the value at  $T_a = 85^\circ\text{C}$  for the independent (non-mounted) IC package without a heat sink.

In case of use this IC, please refer to the  $P_D$ - $T_a$  diagram of the package standard and use under the condition not exceeding the allowable value.

\*3: Except for the power dissipation, operating ambient temperature, and storage temperature, all ratings are for  $T_a = 25^\circ\text{C}$ .

\*4: SCLK, SDATA, SDEN, RESET, REF\_CLK, I\_IN\_P, I\_IN\_N, Q\_IN\_P, Q\_IN\_N, VGA\_CNT pins.

\*5: I\_IN\_P to I\_IN\_N or Q\_IN\_P to Q\_IN\_N differential input voltage.

### ■ Operating supply voltage range

Parameter	Symbol	Range	Unit	Notes
Supply voltage range	$V_{CC}$	2.7 to 3.0	V	—



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