

AN6530, AN6531

4-pin Positive Adjustable Voltage Regulator

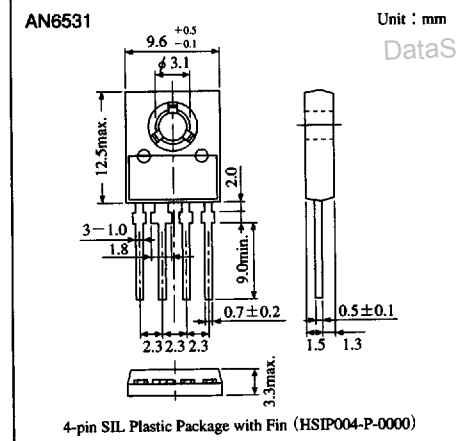
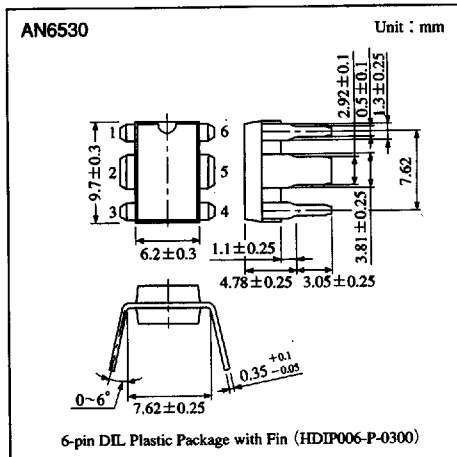
Overview

The AN6530 and the AN6531 are the monolithic 4-pin positive adjustable voltage regulators. They provide any stabilized output voltage between 5V and 30V with external resistor, and are ideal for power circuits with current capacitance up to 0.5A. Moreover, these voltage regulators are highly reliable with various internal protection circuits. The AN6530 is in a 6-pin DIL plastic package, and the AN6531 is in a 4-pin SIL plastic package.

Features

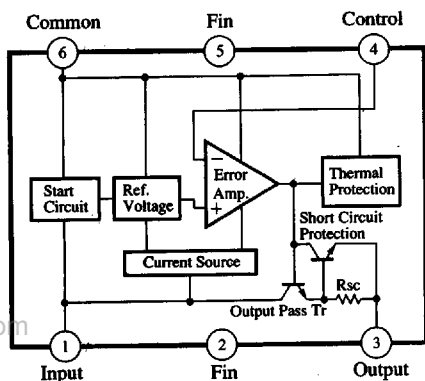
- Wide range of output voltage : $V_O = 5$ to 30V
- Internal thermal overload protection
- Internal short-circuit protection
- Output transistor safe area compensation

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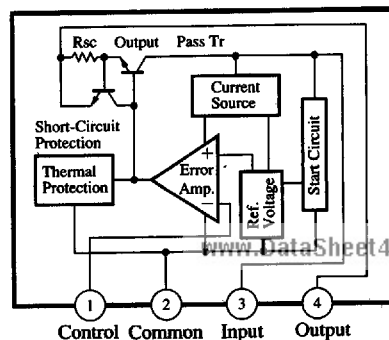
Block Diagram

AN6530



Note) Pin② and ⑤ are electrically in common, and can be connected as Common like Pin⑥. Always use Pin⑥ as Common.

AN6531



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■ Absolute Maximum Ratings ($T_a=25^\circ\text{C}$)

Parameter	Symbol	Rating	Unit
Supply voltage	V_{CC}	40	V
Supply current	I_{CC}^{*1}	1.5	A
Power dissipation	AN6530	1.5 ^{*2}	W
	AN6531		
Operating ambient temperature	T_{opr}	-20 to +75	$^\circ\text{C}$
Storage temperature	T_{stg}	-55 to +150	$^\circ\text{C}$

*1 The internal circuit is provided with a current limiting circuit.

*2 Maximum power dissipation value in the case where there is no heat sink (The value varies with the external heat dissipation state.)

■ Electrical Characteristics ($T_a=25^\circ\text{C}$)

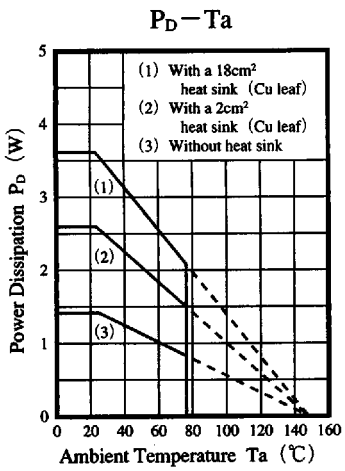
Parameter	Symbol	Condition	min	typ	max	Unit	
Output voltage tolerance	V_O	$V_I=V_O+3\text{V to }V_O+15\text{V}$, $I_O=5\text{ to }350\text{mA}$, $T_j=25^\circ\text{C}$	—	—	4	%	
Line regulation	REG_{IN}	$V_O=5\text{V}$, $I_O=200\text{mA}$, $V_I=7.5\text{ to }25\text{V}$, $T_j=25^\circ\text{C}$	—	—	1	%	
		$V_O=18\text{V}$, $I_O=5\text{mA}$, $V_I=21\text{ to }33\text{V}$, $T_j=25^\circ\text{C}$	—	—	0.75	%	
		$V_O=18\text{V}$, $I_O=200\text{mA}$, $V_I=21\text{ to }25\text{V}$, $T_j=25^\circ\text{C}$	—	—	0.67	%	
Load regulation	REG_L	$V_O=5\text{V}$, $V_I=12\text{V}$, $I_O=5\text{ to }500\text{mA}$, $T_j=25^\circ\text{C}$	—	—	1	%	
Bias current	I_{bias}	$T_j=25^\circ\text{C}$	—	3	5	mA	
Control pin current	I_{cont}	$T_j=25^\circ\text{C}$	—	1	8	μA	
Ripple rejection ratio	RR	$V_I=8\text{ to }18\text{V}$, $V_O=5\text{V}$, $f=120\text{Hz}$	62	80	—	dB	
Output noise voltage	V_{no}	$V_O=5\text{V}$, $f=10\text{Hz to }100\text{kHz}$	—	40	—	μV	
Minimum input/output voltage difference	$V_{DIF(min.)}$	$I_O=500\text{mA}$, $T_j=25^\circ\text{C}$	—	2	—	V	
Short circuit current	I_{OS}	$V_I=35\text{V}$, $V_O=5\text{V}$, $T_j=25^\circ\text{C}$	—	50	600	mA	
Peak output current	I_{OP}	$V_O=5\text{V}$, $T_j=25^\circ\text{C}$	0.4	1	1.4	A	
Output voltage temperature coefficient	$\Delta V_O/T_a$	$V_O=5\text{V}$	$T_j=-55\text{ to }+25^\circ\text{C}$	—	0.5	—	mV/ $^\circ\text{C}$
		$I_O=5\text{mA}$	$T_j=25\text{ to }150^\circ\text{C}$	—	-0.5	—	mV/ $^\circ\text{C}$
Control pin voltage	V_{cont}	$T_j=25^\circ\text{C}$	4.8	5	5.2	V	

Note1) The specified condition $T_j=25^\circ\text{C}$ means that the test should be carried out with the test time so short (within 10ms) that the drift in characteristic value due to the rise in chip junction temperature can be ignored.

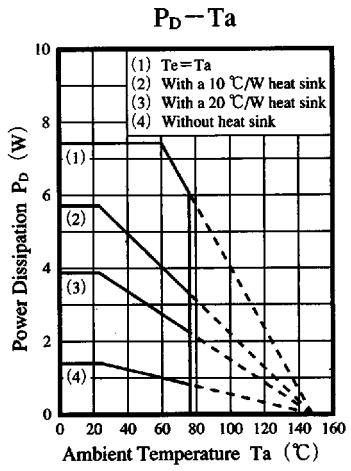
Note2) When not specified, $V_I=10\text{V}$, $V_O=5\text{V}$, $I_O=350\text{mA}$, $C_I=0.33\ \mu\text{F}$ and $C_O=0.1\ \mu\text{F}$.

■ Characteristics Curve

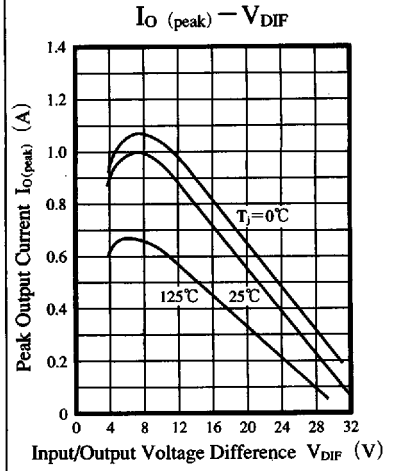
AN6530 Characteristics



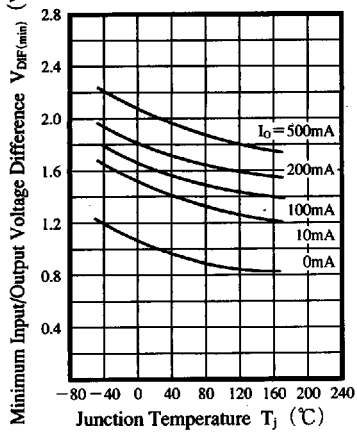
AN6531 Characteristics



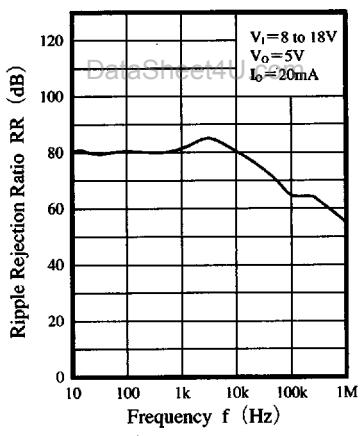
Common Characteristics



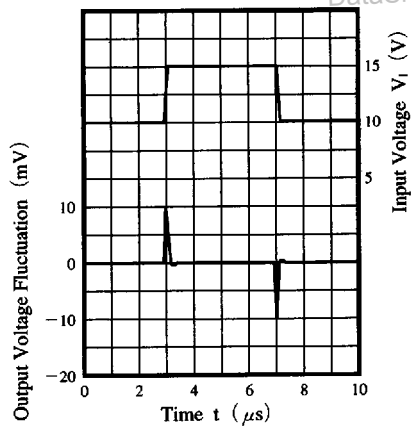
$V_{DIF(\text{min})} - T_j$



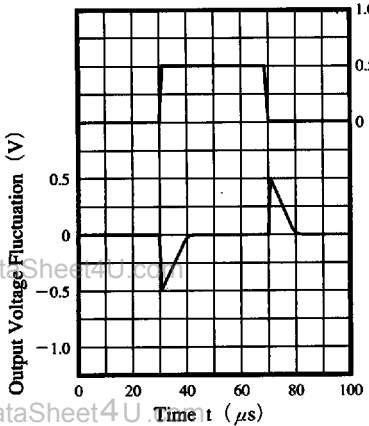
RR - f



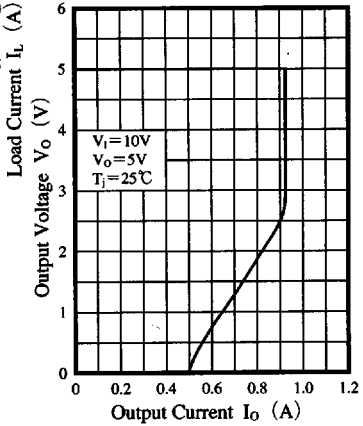
Input Transient Response



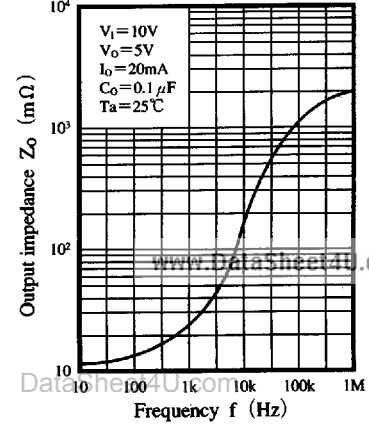
Load Transient Response



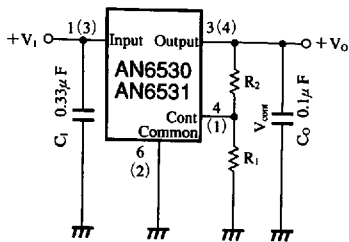
Current Limiting Characteristic



$Z_o - f$



Basic Regulator Circuit



$$\bullet V_O = V_{\text{cont}} \left(\frac{R_1 + R_2}{R_1} \right)$$

$$(V_{\text{cont}} \approx 5V, R_1 = 5k\Omega)$$

● C_1 is necessary when the line of V_I is long.

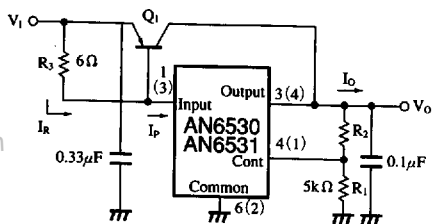
● C_0 improves the transient response.

() Pin No. : AN6531

Application Circuits

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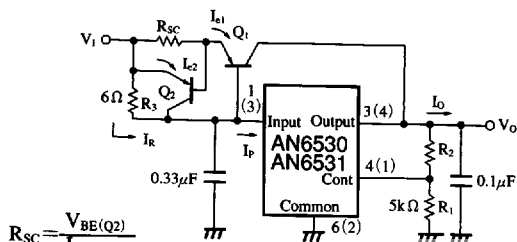
(1) Current Boost Circuit



$$R_3 = \frac{V_{BE(Q1)} \cdot \beta}{(\beta + 1) I_P - I_O}$$

() Pin No. : AN6531

(2) Current Boost Circuit (With Current Limiting Circuit)



$$R_{SC} = \frac{V_{BE(Q2)}}{I_{e1(\text{max})}}$$

$$R_3 = \frac{V_{BE(Q1)} + I_{e1} R_{SC}}{I_O - I_{e1}}$$

$$I_{e2(\text{max})} = I_{P(\text{max})} - \frac{V_{BE(Q1)} + V_{BE(Q2)}}{R_3}$$

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