AO3418, AO3418L (Green Product)

KERSEMI ELECTRONIC CO.,LTD.

N-Channel Enhancement Mode Field Effect Transistor

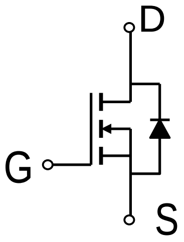
TO-236 (SOT-23) Top View

Features

V_{DS} (V) = 30V
I_D = 3.8 A
$R_{DS(ON)} < 60m\Omega$ (V_{GS} = 10V)
$R_{DS(ON)} < 70m\Omega$ (V_{GS} = 4.5V)
$R_{DS(ON)} < 155m\Omega$ (V_{GS} = 2.5V)

General Description

The AO3418 uses advanced trench technology to provide excellent $R_{DS(ON)}$, very low gate charge and operation with gate voltages as low as 2.5V. This device is suitable for use as a load switch or in PWM applications. AO3418L (Green Product) is offered in a lead-free package.



Absolute Maximum Ratings T_A=25°C unless otherwise noted

Parameter		Symbol	Maximum	Units
Drain-Source Voltage		V_{DS}	30	V
Gate-Source Voltage		V_{GS}	±12	V
Continuous Drain Current [A]	T_A=25°C	I_D	3.8	A
	T_A=70°C		3.1	
Pulsed Drain Current [B]		I_{DM}	15	
Power Dissipation [A]	T_A=25°C	P_D	1.4	W
	T_A=70°C		0.9	
Junction and Storage Temperature Range		T_J, T_{STG}	-55 to 150	°C

Thermal Characteristics

Parameter		Symbol	Typ	Max	Units
Maximum Junction-to-Ambient [A]	t ≤ 10s	$R_{\theta JA}$	70	90	°C/W
Maximum Junction-to-Ambient [A]	Steady-State		100	125	°C/W
Maximum Junction-to-Lead [C]	Steady-State	$R_{\theta JL}$	63	80	°C/W

Electrical Characteristics (T_J=25°C unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ	Max	Units
STATIC PARAMETERS						
BV_{DSS}	Drain-Source Breakdown Voltage	I_D=250µA, V_{GS}=0V	30			V
I_{DSS}	Zero Gate Voltage Drain Current	V_{DS}=24V, V_{GS}=0V		0.001	1	µA
		T_J=55°C			5	
I_{GSS}	Gate-Body leakage current	V_{DS}=0V, V_{GS}=±12V			100	nA
$V_{GS(th)}$	Gate Threshold Voltage	V_{DS}=V_{GS} I_D=250µA	1	1.4	1.8	V
$I_{D(ON)}$	On state drain current	V_{GS}=4.5V, V_{DS}=5V	15			A
$R_{DS(ON)}$	Static Drain-Source On-Resistance	V_{GS}=10V, I_D=3.8A		43	60	mΩ
		T_J=125°C		64	85	
		V_{GS}=4.5V, I_D=3.5A		52	70	mΩ
		V_{GS}=2.5V, I_D=1A		101	155	mΩ
g_{FS}	Forward Transconductance	V_{DS}=5V, I_D=3.8A		11.7		S
V_{SD}	Diode Forward Voltage	I_S=1A,V_{GS}=0V		0.81	1	V
I_S	Maximum Body-Diode Continuous Current				2.5	A
DYNAMIC PARAMETERS						
C_{iss}	Input Capacitance	V_{GS}=0V, V_{DS}=15V, f=1MHz		226	270	pF
C_{oss}	Output Capacitance			39		pF
C_{rss}	Reverse Transfer Capacitance			29		pF
R_g	Gate resistance	V_{GS}=0V, V_{DS}=0V, f=1MHz		1.4	1.7	Ω
SWITCHING PARAMETERS						
Q_g	Total Gate Charge	V_{GS}=4.5V, V_{DS}=15V, I_D=3.8A		3	3.6	nC
Q_{gs}	Gate Source Charge			1.4		nC
Q_{gd}	Gate Drain Charge			0.55		nC
$t_{D(on)}$	Turn-On DelayTime	V_{GS}=10V, V_{DS}=15V, R_L=3.9Ω, R_{GEN}=6Ω		2.6	4	ns
t_r	Turn-On Rise Time			3.2	5	ns
$t_{D(off)}$	Turn-Off DelayTime			14.5	22	ns
t_f	Turn-Off Fall Time			2.1	3	ns
t_{rr}	Body Diode Reverse Recovery Time	I_F=3.8A, dI/dt=100A/µs		10.2	13	ns
Q_{rr}	Body Diode Reverse Recovery Charge	I_F=3.8A, dI/dt=100A/µs		3.8	5	nC

A: The value of $R_{\theta JA}$ is measured with the device mounted on 1in^2 FR-4 board with 2oz. Copper, in a still air environment with T_A =25°C. The value in any a given application depends on the user's specific board design. The current rating is based on the t≤ 10s thermal resistance rating.

B: Repetitive rating, pulse width limited by junction temperature.

C. The $R_{\theta JA}$ is the sum of the thermal impedence from junction to lead $R_{\theta JL}$ and lead to ambient.

D. The static characteristics in Figures 1 to 6,12,14 are obtained using 80µs pulses, duty cycle 0.5% max.

E. These tests are performed with the device mounted on 1 in^2 FR-4 board with 2oz. Copper, in a still air environment with T_A=25°C.

TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS

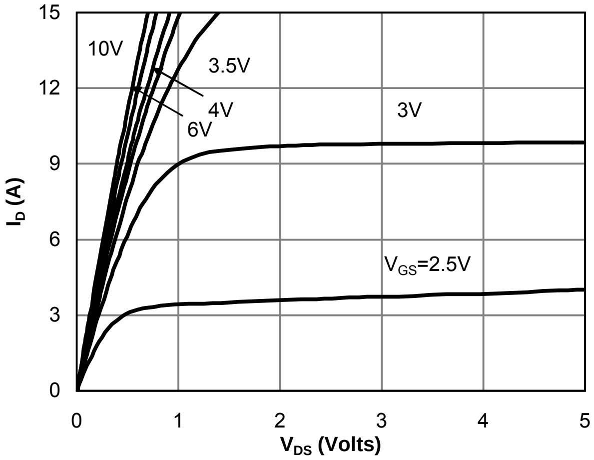


Fig 1: On-Region Characteristics

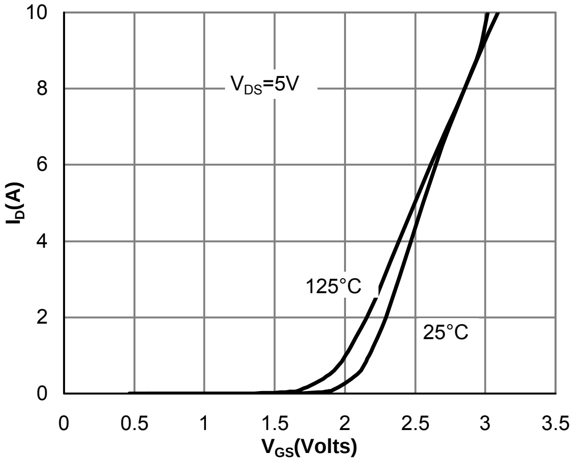


Figure 2: Transfer Characteristics

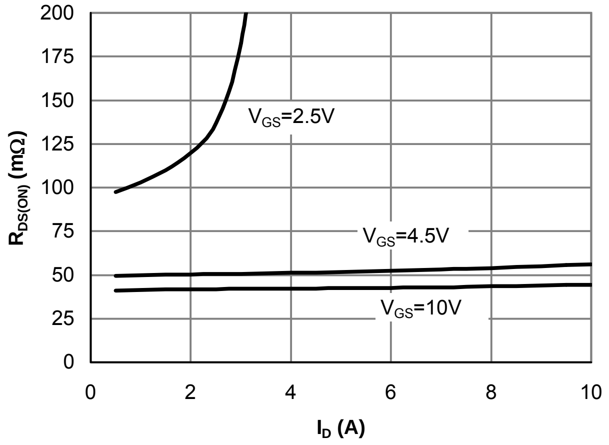


Figure 3: On-Resistance vs. Drain Current and Gate Voltage

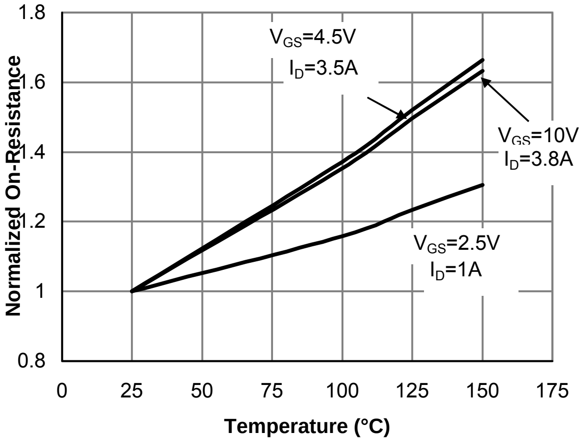


Figure 4: On-Resistance vs. Junction Temperature

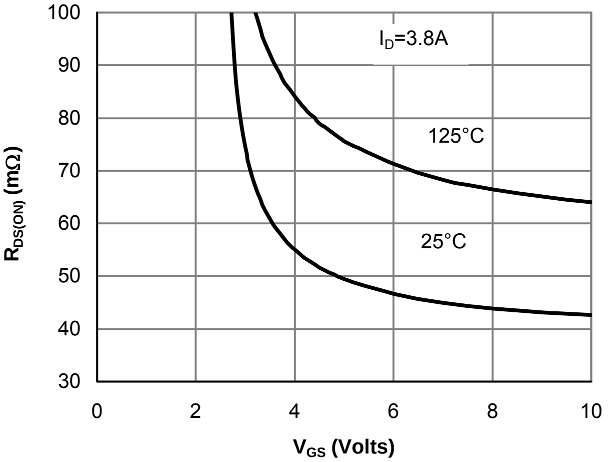


Figure 5: On-Resistance vs. Gate-Source Voltage

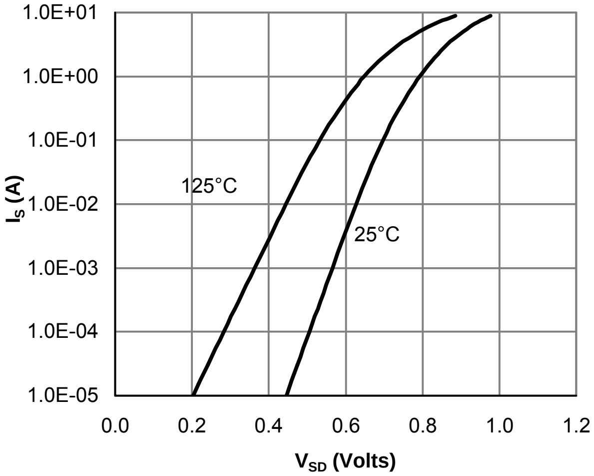


Figure 6: Body-Diode Characteristics

TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS

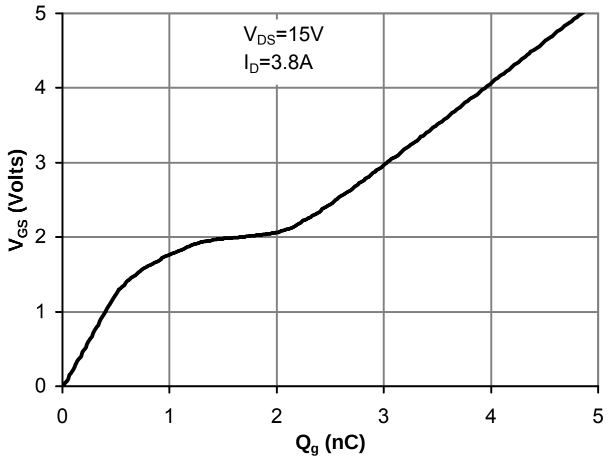


Figure 7: Gate-Charge Characteristics

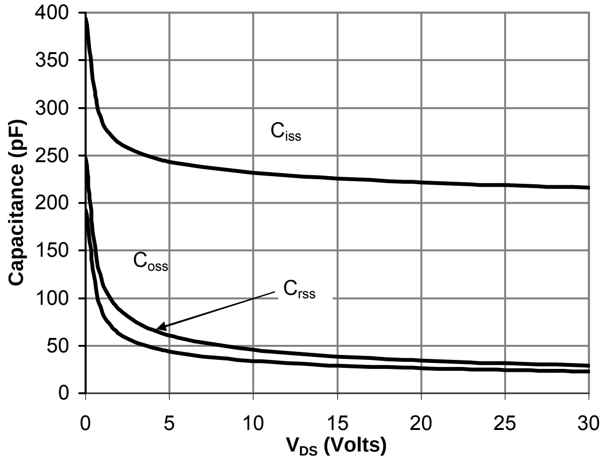


Figure 8: Capacitance Characteristics

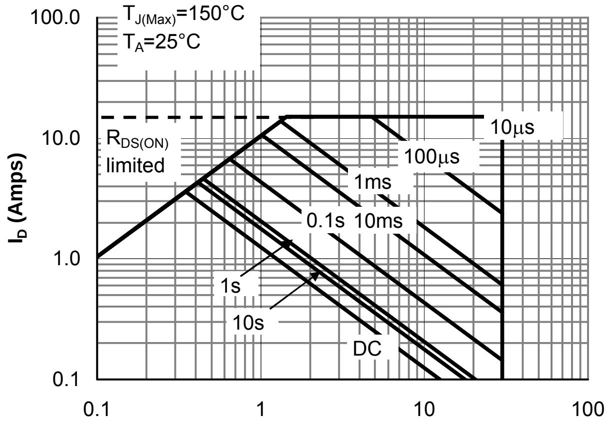


Figure 9: Maximum Forward Biased Safe Operating Area (Note E)

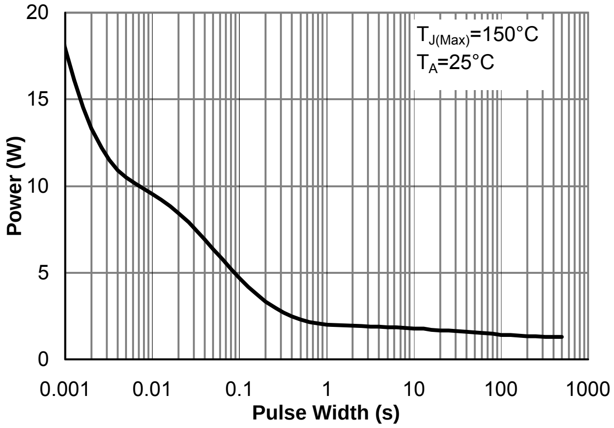


Figure 10: Single Pulse Power Rating Junction-to-Ambient (Note E)

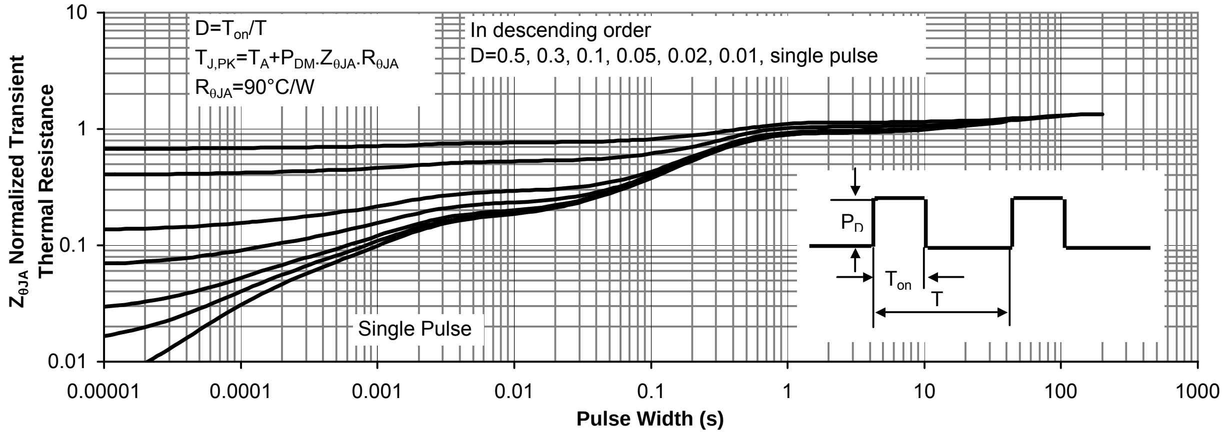


Figure 11: Normalized Maximum Transient Thermal Impedance