
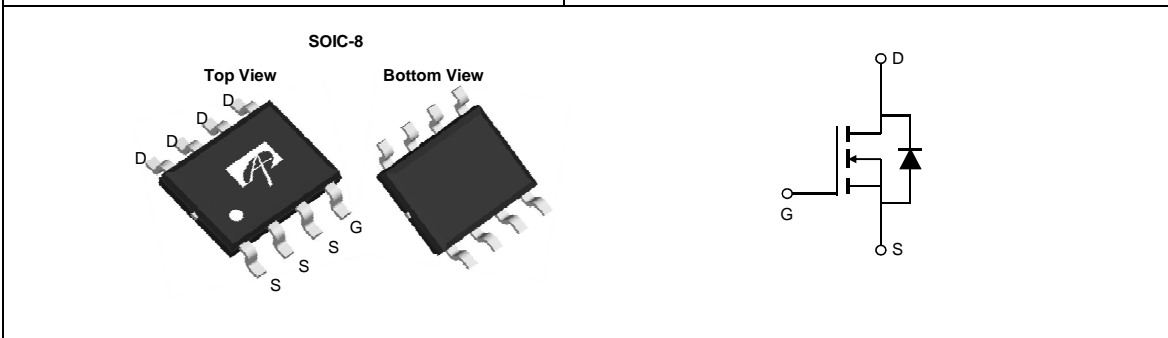


<p>General Description</p> <ul style="list-style-type: none"> Latest Trench Power AlphaMOS (αMOS LV) technology Very Low RDS(on) at 4.5V_{GS} Low Gate Charge High Current Capability RoHS and Halogen-Free Compliant <p>Application</p> <ul style="list-style-type: none"> DC/DC Converters in Computing, Servers, and POL Isolated DC/DC Converters in Telecom and Industrial 	<p>Product Summary</p> <table style="width: 100%; border: none;"> <tr> <td style="padding: 2px 10px 2px 0;">V_{DS}</td> <td style="padding: 2px 10px 2px 0;">30V</td> </tr> <tr> <td style="padding: 2px 10px 2px 0;">I_D (at $V_{GS}=10V$)</td> <td style="padding: 2px 10px 2px 0;">23A</td> </tr> <tr> <td style="padding: 2px 10px 2px 0;">$R_{DS(ON)}$ (at $V_{GS}=10V$)</td> <td style="padding: 2px 10px 2px 0;">< 3.7mΩ</td> </tr> <tr> <td style="padding: 2px 10px 2px 0;">$R_{DS(ON)}$ (at $V_{GS} = 4.5V$)</td> <td style="padding: 2px 10px 2px 0;">< 5.3mΩ</td> </tr> </table> <p>100% UIS Tested 100% R_g Tested</p> <div style="text-align: right;">  </div>	V_{DS}	30V	I_D (at $V_{GS}=10V$)	23A	$R_{DS(ON)}$ (at $V_{GS}=10V$)	< 3.7mΩ	$R_{DS(ON)}$ (at $V_{GS} = 4.5V$)	< 5.3mΩ
V_{DS}	30V								
I_D (at $V_{GS}=10V$)	23A								
$R_{DS(ON)}$ (at $V_{GS}=10V$)	< 3.7mΩ								
$R_{DS(ON)}$ (at $V_{GS} = 4.5V$)	< 5.3mΩ								



Absolute Maximum Ratings $T_A=25^\circ\text{C}$ unless otherwise noted			
Parameter	Symbol	Maximum	Units
Drain-Source Voltage	V_{DS}	30	V
Gate-Source Voltage	V_{GS}	±20	V
Continuous Drain Current	I_D	$T_A=25^\circ\text{C}$	23
		$T_A=100^\circ\text{C}$	14
Pulsed Drain Current ^C	I_{DM}	174	A
Avalanche Current ^C	I_{AS}	37	A
Avalanche energy $L=0.1\text{mH}$ ^C	E_{AS}	68	mJ
V_{DS} Spike	100ns	V_{SPIKE}	36
Power Dissipation ^B	P_D	$T_A=25^\circ\text{C}$	3.1
		$T_A=100^\circ\text{C}$	1.2
Junction and Storage Temperature Range	T_J, T_{STG}	-55 to 150	°C

Thermal Characteristics					
Parameter		Symbol	Typ	Max	Units
Maximum Junction-to-Ambient ^A	$t \leq 10\text{s}$	$R_{\theta JA}$	31	40	°C/W
	Steady-State		59	75	°C/W
Maximum Junction-to-Lead	Steady-State	$R_{\theta JL}$	16	24	°C/W

Electrical Characteristics (T_J=25°C unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ	Max	Units
STATIC PARAMETERS						
BV _{DSS}	Drain-Source Breakdown Voltage	I _D =250μA, V _{GS} =0V	30			V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} =30V, V _{GS} =0V T _J =55°C			1 5	μA
I _{GSS}	Gate-Body leakage current	V _{DS} =0V, V _{GS} = ±20V			100	nA
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _D =250μA	1.2	1.8	2.2	V
R _{DS(ON)}	Static Drain-Source On-Resistance	V _{GS} =10V, I _D =20A T _J =125°C		3 4.1	3.7 5	mΩ
		V _{GS} =4.5V, I _D =20A		4.1	5.3	mΩ
g _{FS}	Forward Transconductance	V _{DS} =5V, I _D =20A		105		S
V _{SD}	Diode Forward Voltage	I _S =1A, V _{GS} =0V		0.7	1	V
I _S	Maximum Body-Diode Continuous Current				4	A
DYNAMIC PARAMETERS						
C _{iss}	Input Capacitance			2010		pF
C _{oss}	Output Capacitance	V _{GS} =0V, V _{DS} =15V, f=1MHz		898		pF
C _{rss}	Reverse Transfer Capacitance			124		pF
R _g	Gate resistance	V _{GS} =0V, V _{DS} =0V, f=1MHz	0.9	1.8	2.7	Ω
SWITCHING PARAMETERS						
Q _{g(10V)}	Total Gate Charge	V _{GS} =10V, V _{DS} =15V, I _D =20A		36	49	nC
Q _{g(4.5V)}	Total Gate Charge			17	23	nC
Q _{gs}	Gate Source Charge			6		nC
Q _{gd}	Gate Drain Charge			8		nC
t _{D(on)}	Turn-On DelayTime	V _{GS} =10V, V _{DS} =15V, R _L =0.75Ω, R _{GEN} =3Ω		7.5		ns
t _r	Turn-On Rise Time			4.0		ns
t _{D(off)}	Turn-Off DelayTime			37.0		ns
t _f	Turn-Off Fall Time			7.5		ns
t _{rr}	Body Diode Reverse Recovery Time	I _F =20A, dI/dt=500A/μs		14		ns
Q _{rr}	Body Diode Reverse Recovery Charge	I _F =20A, dI/dt=500A/μs		20.3		nC

A. The value of R_{θJA} is measured with the device mounted on 1in² FR-4 board with 2oz. Copper, in a still air environment with T_A=25° C. The value in any given application depends on the user's specific board design.

B. The power dissipation P_D is based on T_{J(MAX)}=150° C, using ≤ 10s junction-to-ambient thermal resistance.

C. Repetitive rating, pulse width limited by junction temperature T_{J(MAX)}=150° C. Ratings are based on low frequency and duty cycles to keep initial T_J=25° C.

D. The R_{θJA} is the sum of the thermal impedance from junction to lead R_{θJL} and lead to ambient.

E. The static characteristics in Figures 1 to 6 are obtained using <300μs pulses, duty cycle 0.5% max.

F. These curves are based on the junction-to-ambient thermal impedance which is measured with the device mounted on 1in² FR-4 board with 2oz. Copper, assuming a maximum junction temperature of T_{J(MAX)}=150° C. The SOA curve provides a single pulse rating.

THIS PRODUCT HAS BEEN DESIGNED AND QUALIFIED FOR THE CONSUMER MARKET. APPLICATIONS OR USES AS CRITICAL COMPONENTS IN LIFE SUPPORT DEVICES OR SYSTEMS ARE NOT AUTHORIZED. AOS DOES NOT ASSUME ANY LIABILITY ARISING OUT OF SUCH APPLICATIONS OR USES OF ITS PRODUCTS. AOS RESERVES THE RIGHT TO IMPROVE PRODUCT DESIGN, FUNCTIONS AND RELIABILITY WITHOUT NOTICE.

TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS

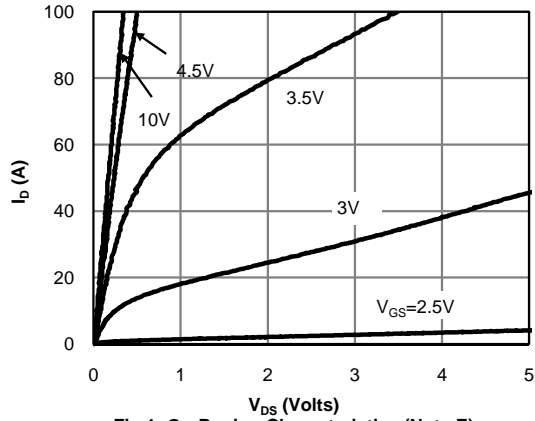


Fig 1: On-Region Characteristics (Note E)

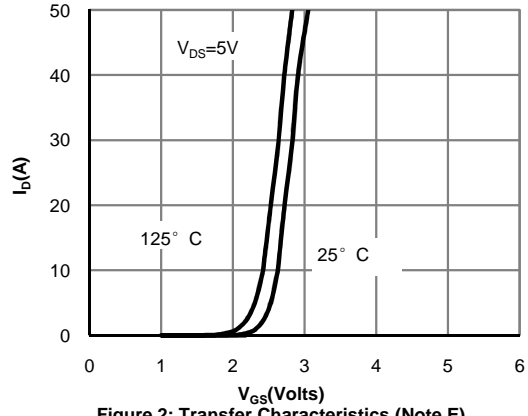


Figure 2: Transfer Characteristics (Note E)

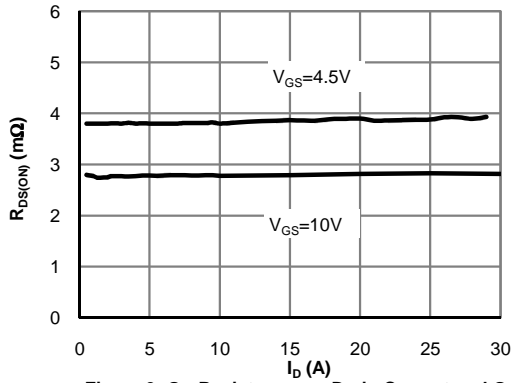


Figure 3: On-Resistance vs. Drain Current and Gate Voltage (Note E)

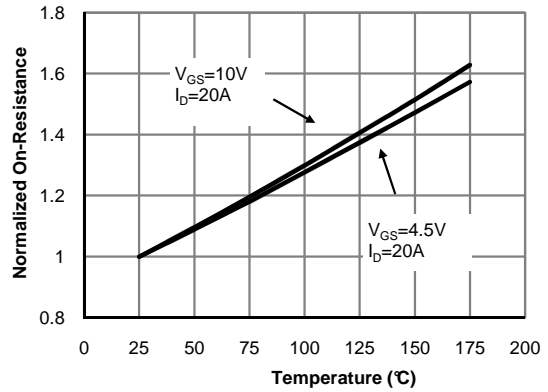


Figure 4: On-Resistance vs. Junction Temperature (Note E)

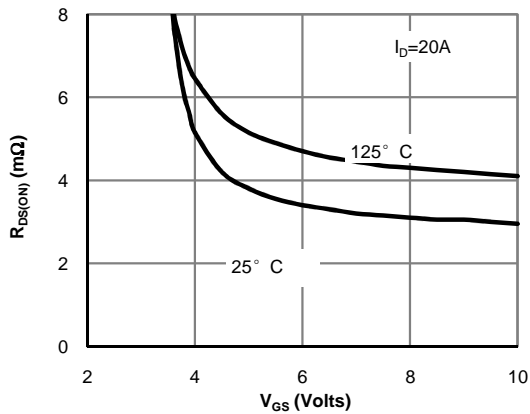


Figure 5: On-Resistance vs. Gate-Source Voltage (Note E)

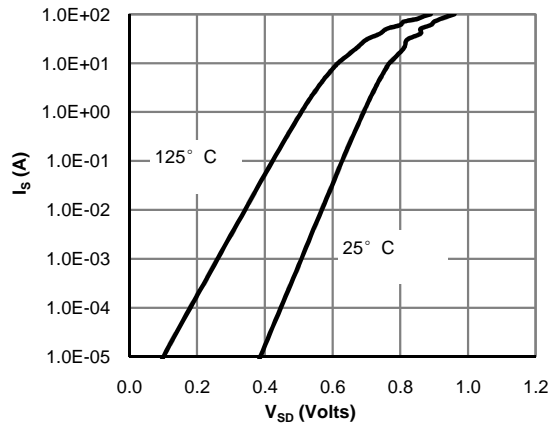


Figure 6: Body-Diode Characteristics (Note E)

TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS

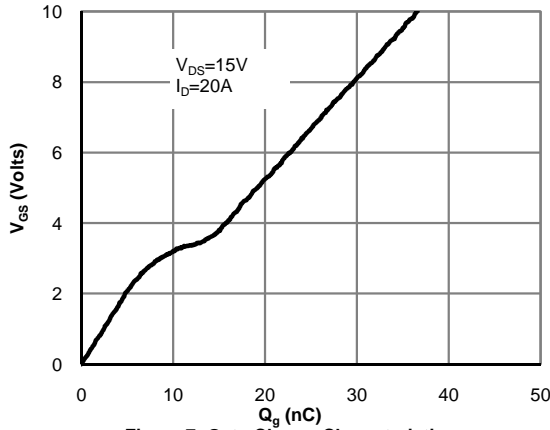


Figure 7: Gate-Charge Characteristics

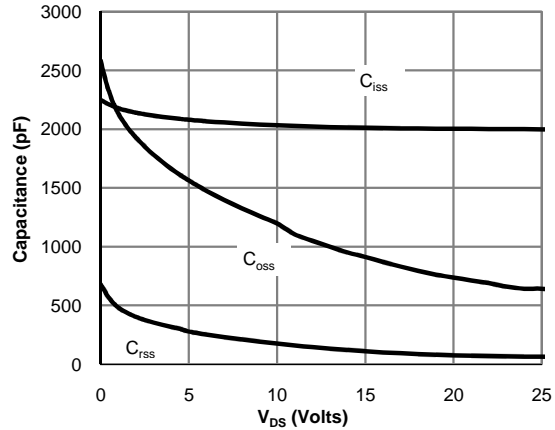


Figure 8: Capacitance Characteristics

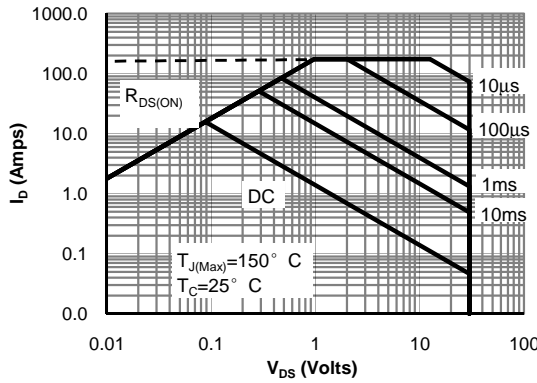


Figure 9: Maximum Forward Biased Safe Operating Area (Note F)

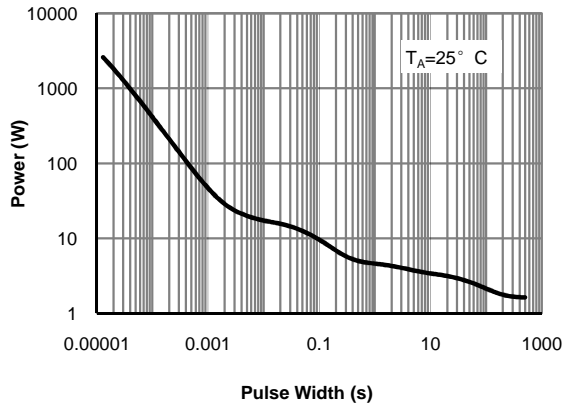


Figure 14: Single Pulse Power Rating Junction-to-Ambient (Note F)

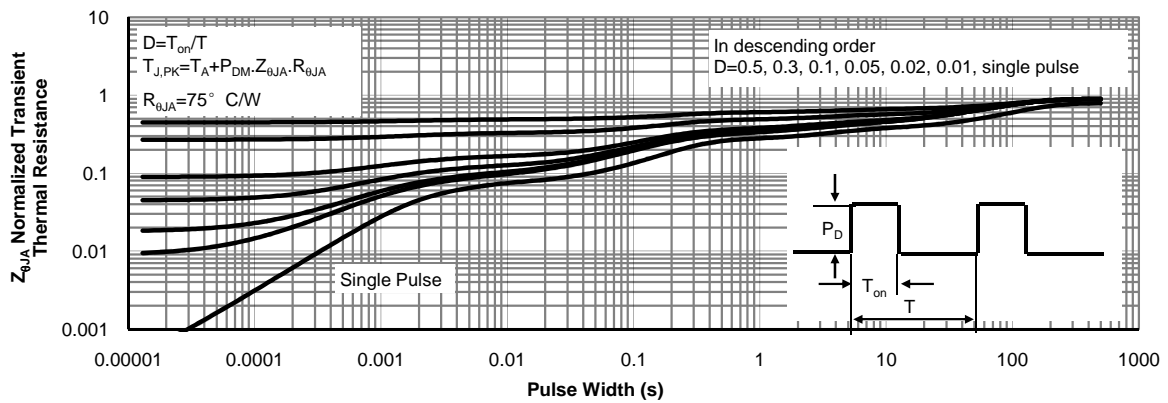
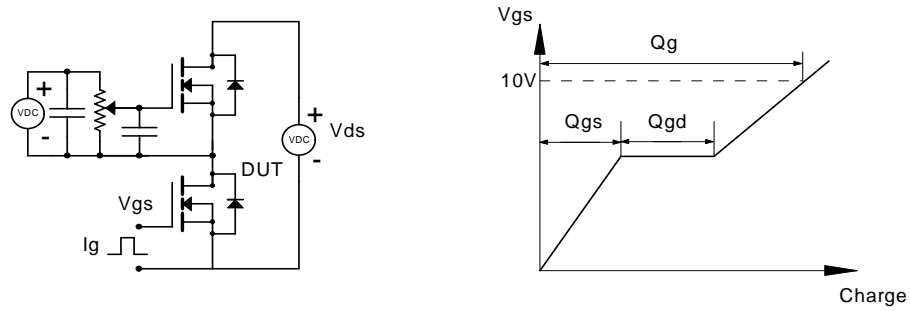
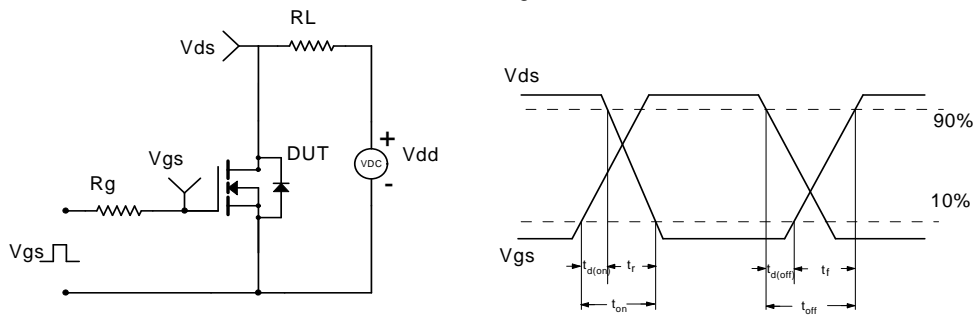


Figure 15: Normalized Maximum Transient Thermal Impedance (Note F)

Gate Charge Test Circuit & Waveform



Resistive Switching Test Circuit & Waveforms



Unclamped Inductive Switching (UIS) Test Circuit & Waveforms



Diode Recovery Test Circuit & Waveforms

