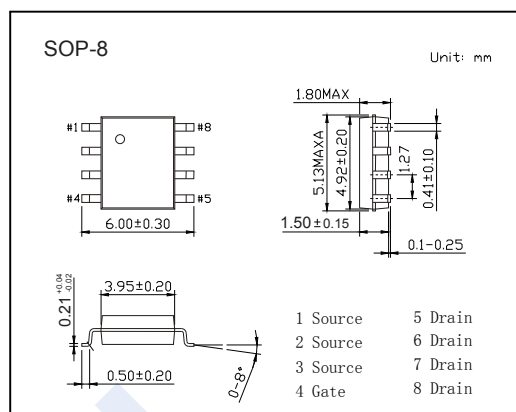
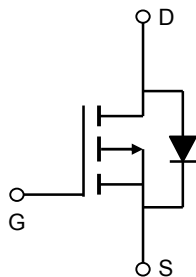


P-Channel MOSFET

AO4415 (KO4415)

■ Features

- $V_{DS} (V) = -30V$
- $I_D = -8 A (V_{GS} = -20V)$
- $R_{DS(ON)} < 26m\Omega (V_{GS} = -20V)$
- $R_{DS(ON)} < 35m\Omega (V_{GS} = -10V)$



■ Absolute Maximum Ratings $T_a = 25^\circ C$

Parameter		Symbol	Rating	Unit
Drain-Source Voltage		V_{DS}	-30	V
Gate-Source Voltage		V_{GS}	± 25	
Continuous Drain Current	$T_A = 25^\circ C$	I_D	-8	A
	$T_A = 70^\circ C$		-6.6	
Pulsed Drain Current		I_{DM}	-40	
Power Dissipation	$T_A = 25^\circ C$	P_D	3	W
	$T_A = 70^\circ C$		2.1	
Thermal Resistance.Junction- to-Ambient	$t \leq 10s$	R_{thJA}	40	$^\circ C/W$
	Steady-State		75	
Thermal Resistance.Junction- to-Lead		R_{thJL}	30	
Junction Temperature		T_J	150	$^\circ C$
Junction Storage Temperature Range		T_{stg}	-55 to 150	

P-Channel MOSFET

AO4415 (KO4415)

■ Electrical Characteristics Ta = 25°C

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Drain-Source Breakdown Voltage	V _{DSS}	I _D =-250 μA, V _{GS} =0V	-30			V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =-24V, V _{GS} =0V			-1	μA
		V _{DS} =-24V, V _{GS} =0V, T _J =55°C			-5	
Gate-Body leakage current	I _{GSS}	V _{DS} =0V, V _{GS} =±25V			±100	nA
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} , I _D =-250 μA	-1.7		-3.5	V
Static Drain-Source On-Resistance	R _{DS(on)}	V _{GS} =-20V, I _D =-8A			26	mΩ
		V _{GS} =-20V, I _D =-8A T _J =125°C			35	
		V _{GS} =-10V, I _D =-8A			35	
		V _{GS} =-6V, I _D =-5A		41		
On state drain current	I _{D(ON)}	V _{GS} =-10V, V _{DS} =-5V	-40			A
Forward Transconductance	g _{FS}	V _{DS} =-5V, I _D =-8A		11.5		S
Input Capacitance	C _{iss}	V _{GS} =0V, V _{DS} =-15V, f=1MHz		893	1100	pF
Output Capacitance	C _{oss}			204		
Reverse Transfer Capacitance	C _{rss}			151		
Gate resistance	R _g	V _{GS} =0V, V _{DS} =0V, f=1MHz		4	6	Ω
Total Gate Charge	Q _g	V _{GS} =-10V, V _{DS} =-15V, I _D =-8A		16.6	21	nC
Gate Source Charge	Q _{gs}			3.2		
Gate Drain Charge	Q _{gd}			5.2		
Turn-On DelayTime	t _{d(on)}	V _{GS} =-10V, V _{DS} =-15V, R _L =1.8Ω, R _{GEN} =3Ω		10.5		ns
Turn-On Rise Time	t _r			7.3		
Turn-Off DelayTime	t _{d(off)}			15.1		
Turn-Off Fall Time	t _f			8.6		
Body Diode Reverse Recovery Time	t _{rr}	I _F =-8A, dI/dt=100A/μs		21	26	nC
Body Diode Reverse Recovery Charge	Q _{rr}			10.7		
Maximum Body-Diode Continuous Current	I _S				-4.2	A
Diode Forward Voltage	V _{SD}	I _S =-1A, V _{GS} =0V			-1	V

Note : The static characteristics in Figures 1 to 6 are obtained using <300 μs pulses, duty cycle 0.5% max.

■ Marking

Marking	4415 KC****
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P-Channel MOSFET

AO4415 (KO4415)

■ Typical Characteristics

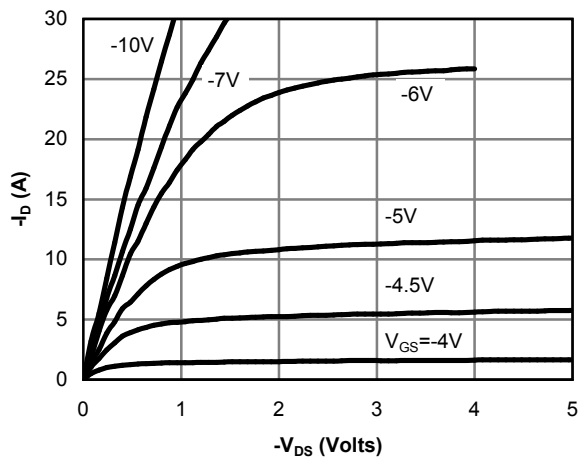


Fig 1: On-Region Characteristics

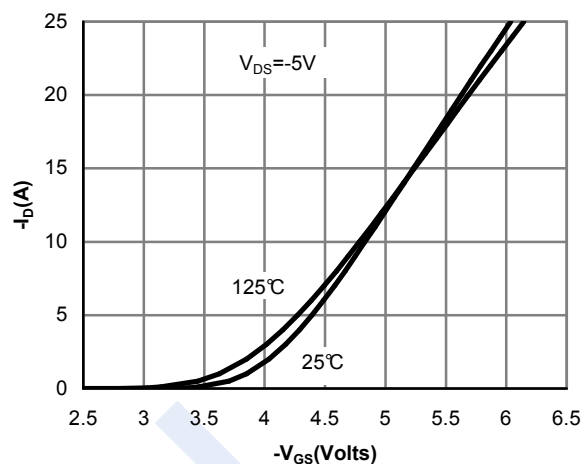


Figure 2: Transfer Characteristics

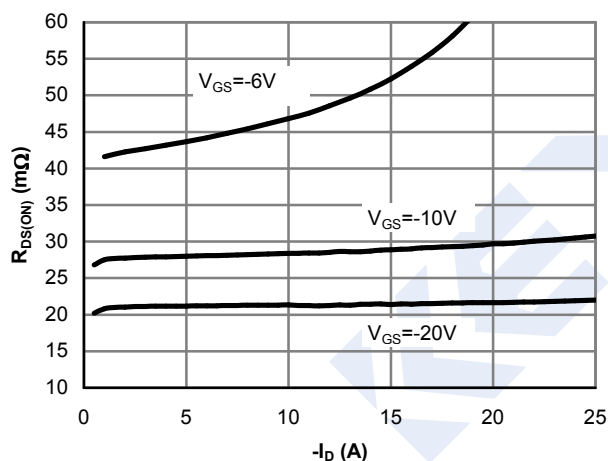


Figure 3: On-Resistance vs. Drain Current and Gate Voltage

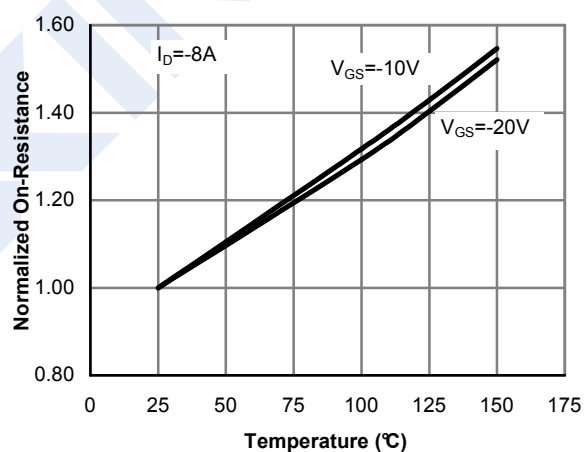


Figure 4: On-Resistance vs. Junction Temperature

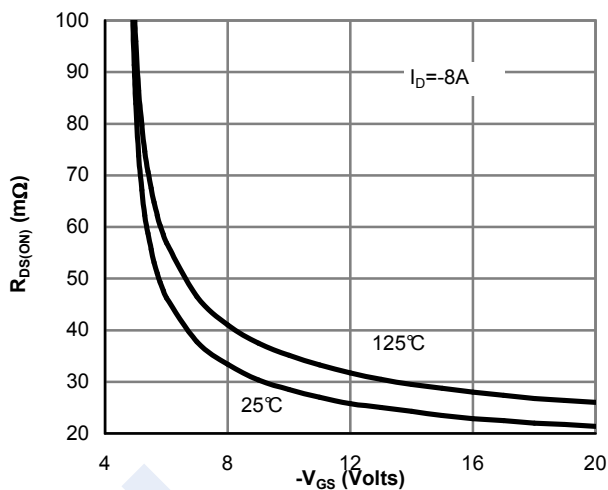


Figure 5: On-Resistance vs. Gate-Source Voltage

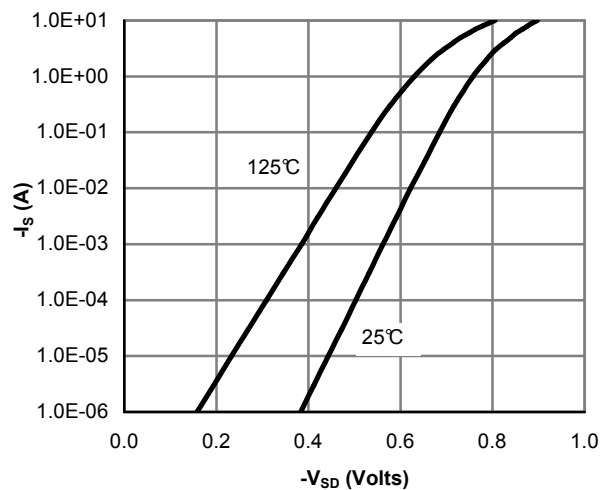


Figure 6: Body-Diode Characteristics

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AO4415 (KO4415)

■ Typical Characteristics

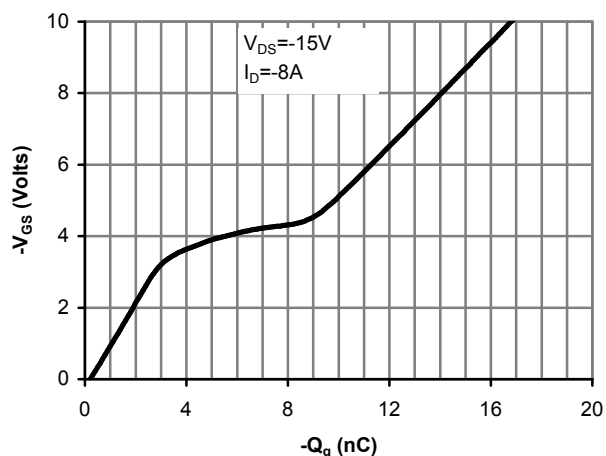


Figure 7: Gate-Charge Characteristics

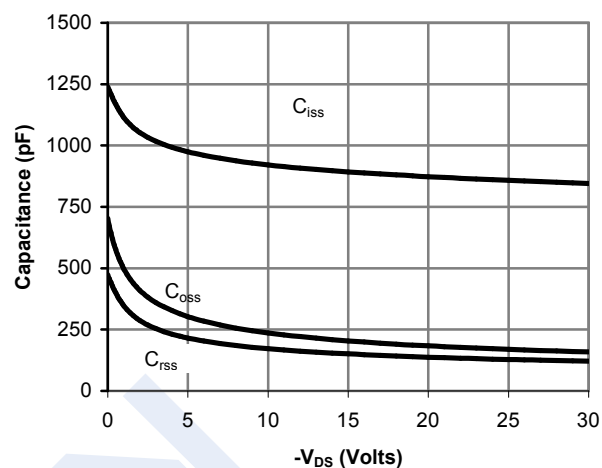


Figure 8: Capacitance Characteristics

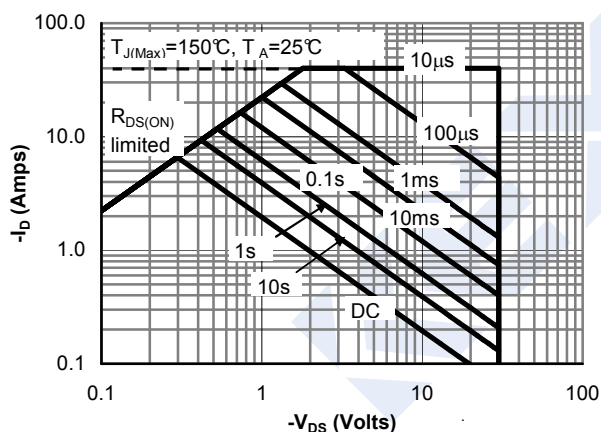


Figure 9: Maximum Forward Biased Safe Operating Area (Note E)

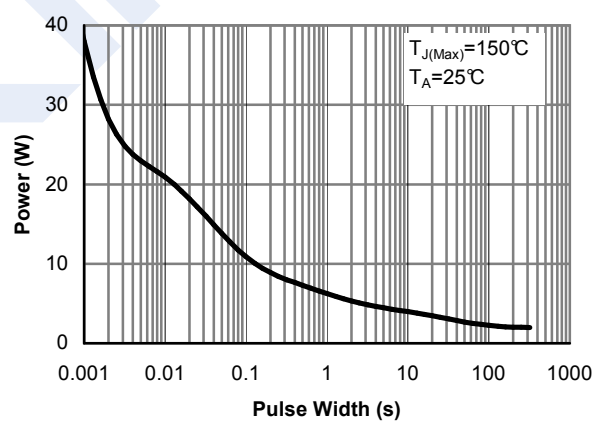


Figure 10: Single Pulse Power Rating Junction-to-Ambient (Note E)

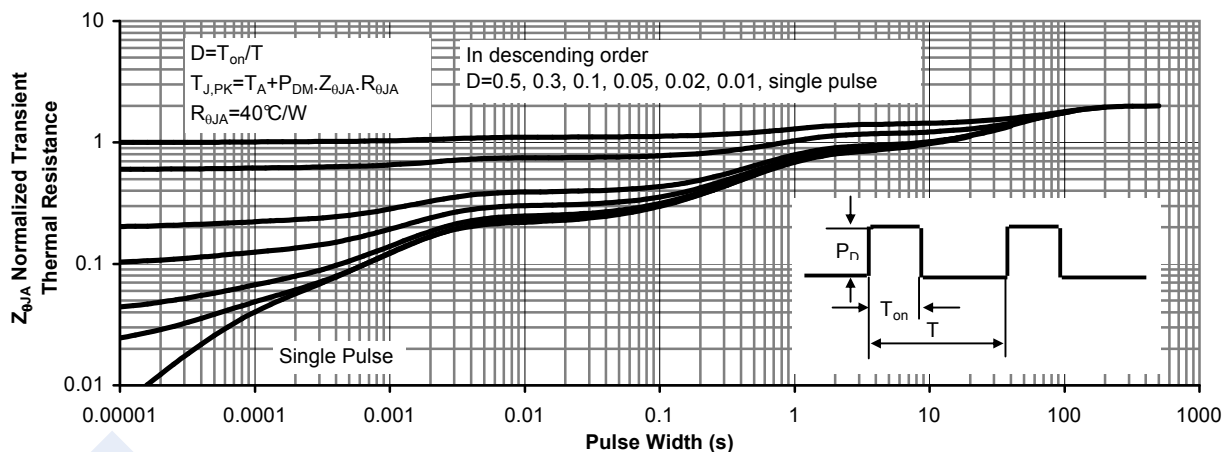


Figure 11: Normalized Maximum Transient Thermal Impedance