



General Description

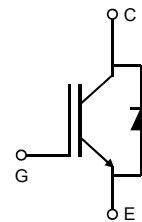
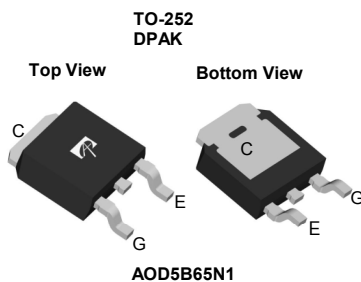
- Latest Alpha IGBT (α IGBT) technology
- 650V breakdown voltage
- Very low turn-off switching loss with softness
- Fast and soft recovery freewheeling diode
- High efficient turn-on di/dt controllability
- Very good EMI behavior
- Short-circuit ruggedness

Applications

- Motor Drives
- Home Appliance and Fan Motor Applications
- Other Hard Switching Applications

Product Summary

V_{CE}	650V
I_C ($T_C=100^\circ\text{C}$)	5A
$V_{CE(sat)}$ ($T_J=25^\circ\text{C}$)	2V



Orderable Part Number	Package Type	Form	Minimum Order Quantity
AOD5B65N1	TO252	Tape & Reel	2500

Absolute Maximum Ratings $T_A=25^\circ\text{C}$ unless otherwise noted

Parameter	Symbol	AOD5B65N1	Units
Collector-Emitter Voltage	V_{CE}	650	V
Gate-Emitter Voltage	V_{GE}	± 30	V
Continuous Collector Current	I_C	$T_C=25^\circ\text{C}$	A
		$T_C=100^\circ\text{C}$	
Pulsed Collector Current, Limited by T_{Jmax}	I_{CM}	15	A
Turn off SOA, $V_{CE} \leq 650\text{V}$, Limited by T_{Jmax}	I_{LM}	15	A
Continuous Diode Forward Current	I_F	$T_C=25^\circ\text{C}$	A
		$T_C=100^\circ\text{C}$	
Diode Pulsed Current, Limited by T_{Jmax}	I_{FM}	15	A
Short circuit withstanding time t_1 $V_{GE}=15\text{V}$, $V_{CC} \leq 300\text{V}$, $T_J \leq 150^\circ\text{C}$	t_{SC}	5	μs
Power Dissipation	P_D	$T_C=25^\circ\text{C}$	W
		$T_C=100^\circ\text{C}$	
Junction and Storage Temperature Range	T_J, T_{STG}	-55 to 150	$^\circ\text{C}$
Maximum lead temperature for soldering purpose, 1/8" from case for 5 seconds	T_L	300	$^\circ\text{C}$

Thermal Characteristics

Parameter	Symbol	AOD5B65N1	Units
Maximum Junction-to-Ambient	$R_{\theta JA}$	55	$^\circ\text{C/W}$
Maximum IGBT Junction-to-Case	$R_{\theta JC}$	2.4	$^\circ\text{C/W}$
Maximum Diode Junction-to-Case	$R_{\theta JC}$	6.8	$^\circ\text{C/W}$

1) Allowed number of short circuits: <1000; time between short circuits: >1s.

Electrical Characteristics (T_J=25°C unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ	Max	Units	
STATIC PARAMETERS							
BV_{CES}	Collector-Emitter Breakdown Voltage	$I_C=1mA, V_{GE}=0V, T_J=25^{\circ}C$	650	-	-	V	
$V_{CE(sat)}$	Collector-Emitter Saturation Voltage	$V_{GE}=15V, I_C=5A$	$T_J=25^{\circ}C$	-	2	2.5	V
			$T_J=125^{\circ}C$	-	2.5	-	
			$T_J=150^{\circ}C$	-	2.64	-	
V_F	Diode Forward Voltage	$V_{GE}=0V, I_C=5A$	$T_J=25^{\circ}C$	-	2.13	2.7	V
			$T_J=125^{\circ}C$	-	2.21	-	
			$T_J=150^{\circ}C$	-	2.17	-	
$V_{GE(th)}$	Gate-Emitter Threshold Voltage	$V_{CE}=5V, I_C=1mA$	-	5.2	-	V	
I_{CES}	Zero Gate Voltage Collector Current	$V_{CE}=650V, V_{GE}=0V$	$T_J=25^{\circ}C$	-	-	10	μA
			$T_J=125^{\circ}C$	-	-	100	
			$T_J=150^{\circ}C$	-	-	500	
I_{GES}	Gate-Emitter leakage current	$V_{CE}=0V, V_{GE}=\pm 30V$	-	-	± 100	nA	
g_{FS}	Forward Transconductance	$V_{CE}=20V, I_C=5A$	-	3.1	-	S	
DYNAMIC PARAMETERS							
C_{ies}	Input Capacitance	$V_{GE}=0V, V_{CC}=25V, f=1MHz$	-	230	-	pF	
C_{oes}	Output Capacitance		-	20	-	pF	
C_{res}	Reverse Transfer Capacitance		-	6.6	-	pF	
Q_g	Total Gate Charge	$V_{GE}=15V, V_{CC}=520V, I_C=5A$	-	9.2	-	nC	
Q_{ge}	Gate to Emitter Charge		-	2.2	-	nC	
Q_{gc}	Gate to Collector Charge		-	4.3	-	nC	
$I_{C(SC)}$	Short circuit collector current	$V_{GE}=15V, V_{CC}=300V, t_{sc}\leq 5\mu s, T_J\leq 150^{\circ}C$	-	24	-	A	
R_g	Gate resistance	$V_{GE}=0V, V_{CC}=0V, f=1MHz$	-	20	-	Ω	
SWITCHING PARAMETERS, (Load Inductive, $T_J=25^{\circ}C$)							
$t_{D(on)}$	Turn-On DelayTime	$T_J=25^{\circ}C, V_{GE}=15V, V_{CC}=400V, I_C=5A, R_G=60\Omega$	-	8	-	ns	
t_r	Turn-On Rise Time		-	14	-	ns	
$t_{D(off)}$	Turn-Off Delay Time		-	73	-	ns	
t_f	Turn-Off Fall Time		-	16	-	ns	
E_{on}	Turn-On Energy		-	0.081	-	mJ	
E_{off}	Turn-Off Energy		-	0.049	-	mJ	
E_{total}	Total Switching Energy		-	0.13	-	mJ	
t_{rr}	Diode Reverse Recovery Time	$T_J=25^{\circ}C, I_F=5A, dI/dt=200A/\mu s, V_{CC}=400V$	-	170	-	ns	
Q_{rr}	Diode Reverse Recovery Charge		-	0.19	-	μC	
I_{rm}	Diode Peak Reverse Recovery Current		-	2.5	-	A	
SWITCHING PARAMETERS, (Load Inductive, $T_J=150^{\circ}C$)							
$t_{D(on)}$	Turn-On DelayTime	$T_J=150^{\circ}C, V_{GE}=15V, V_{CC}=400V, I_C=5A, R_G=60\Omega$	-	7	-	ns	
t_r	Turn-On Rise Time		-	16	-	ns	
$t_{D(off)}$	Turn-Off Delay Time		-	88	-	ns	
t_f	Turn-Off Fall Time		-	26	-	ns	
E_{on}	Turn-On Energy		-	0.09	-	mJ	
E_{off}	Turn-Off Energy		-	0.089	-	mJ	
E_{total}	Total Switching Energy		-	0.179	-	mJ	
t_{rr}	Diode Reverse Recovery Time	$T_J=150^{\circ}C, I_F=5A, dI/dt=200A/\mu s, V_{CC}=400V$	-	273	-	ns	
Q_{rr}	Diode Reverse Recovery Charge		-	0.34	-	μC	
I_{rm}	Diode Peak Reverse Recovery Current		-	3.2	-	A	

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TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS

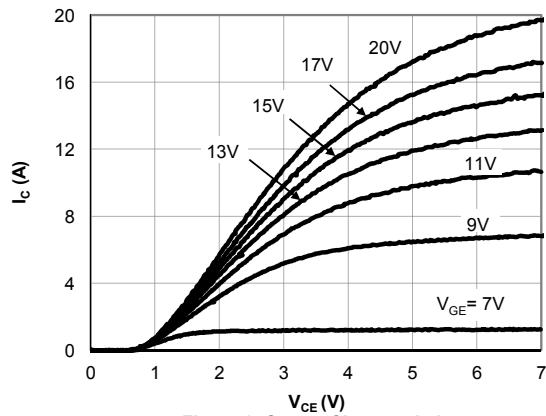


Figure 1: Output Characteristic
($T_j=25^\circ\text{C}$)

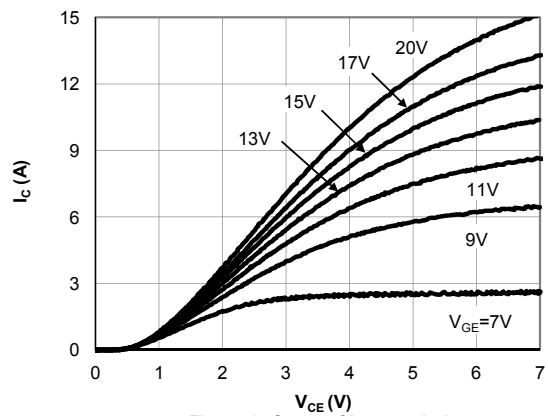


Figure 2: Output Characteristic
($T_j=150^\circ\text{C}$)

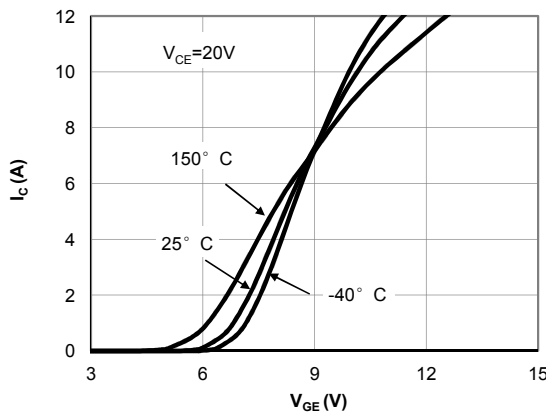


Figure 3: Transfer Characteristic

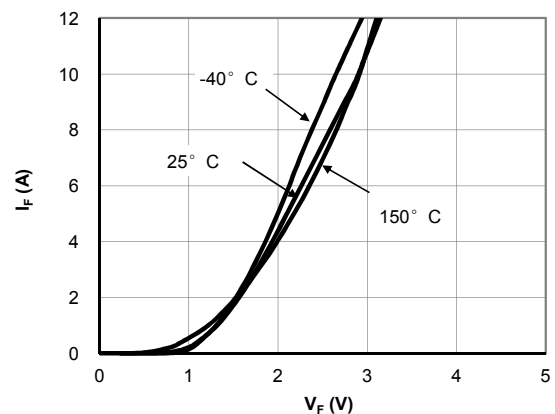


Figure 4: Diode Characteristic

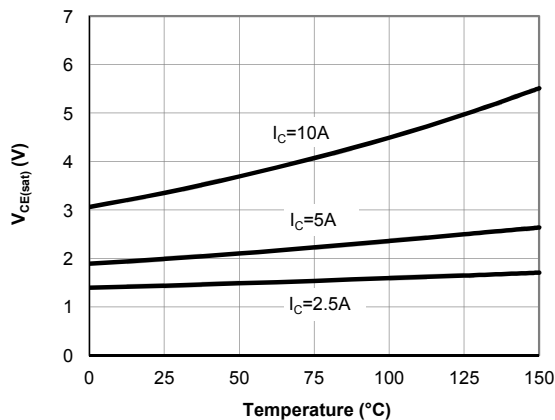


Figure 5: Collector-Emitter Saturation Voltage vs.
Junction Temperature

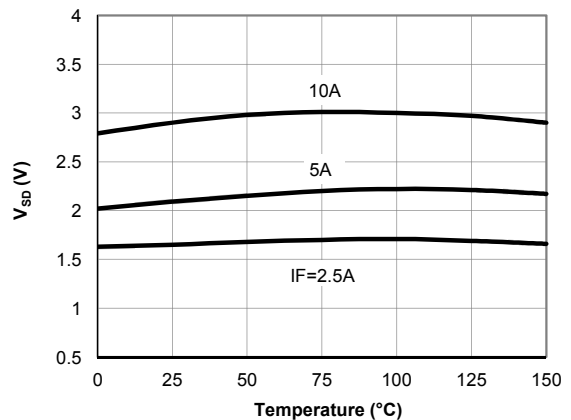
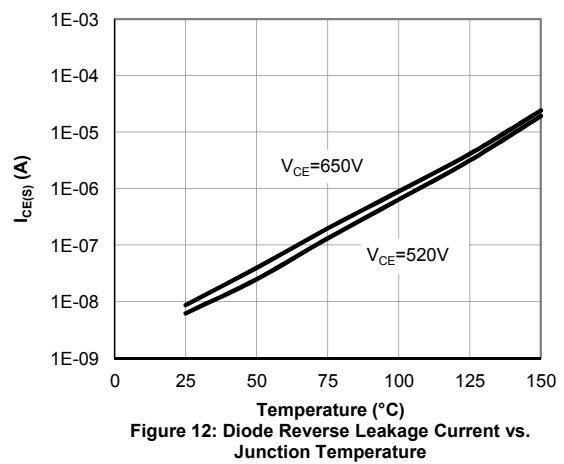
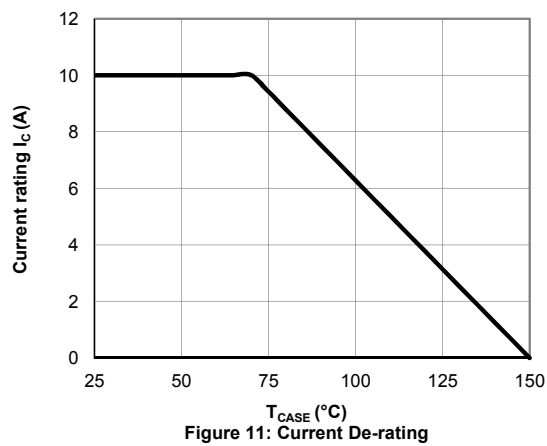
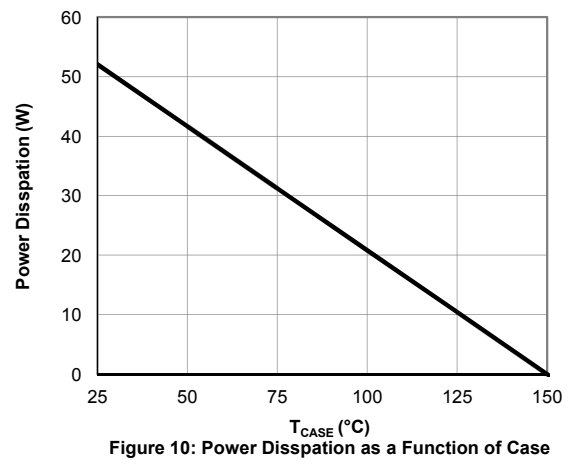
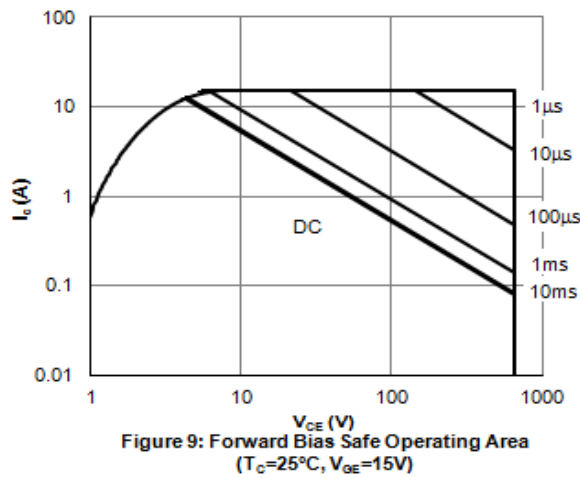
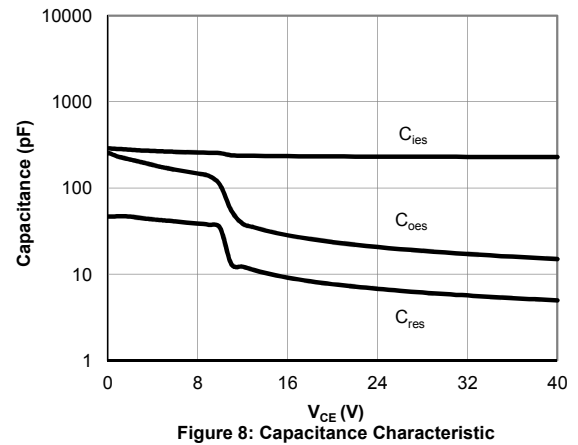
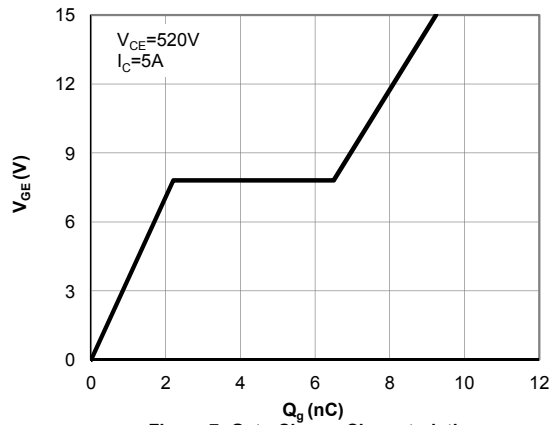
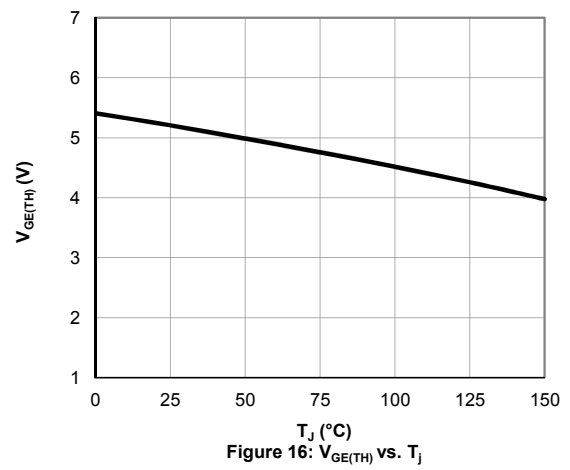
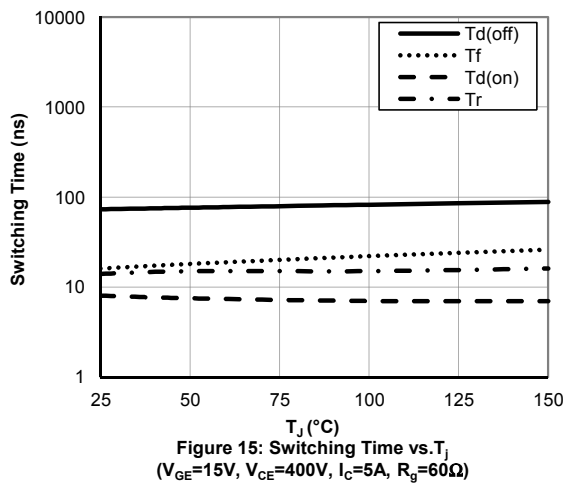
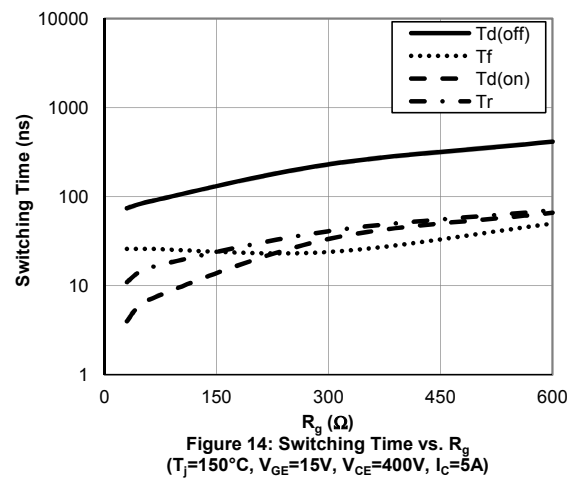
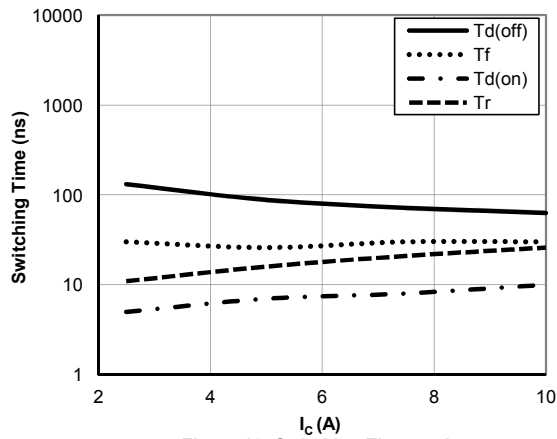


Figure 6: Diode Forward voltage vs. Junction
Temperature

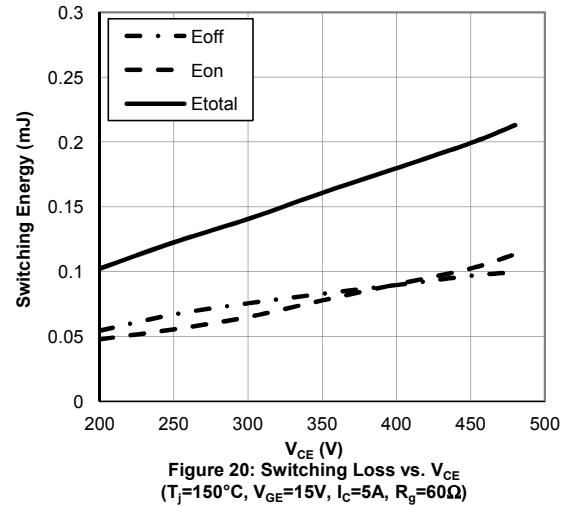
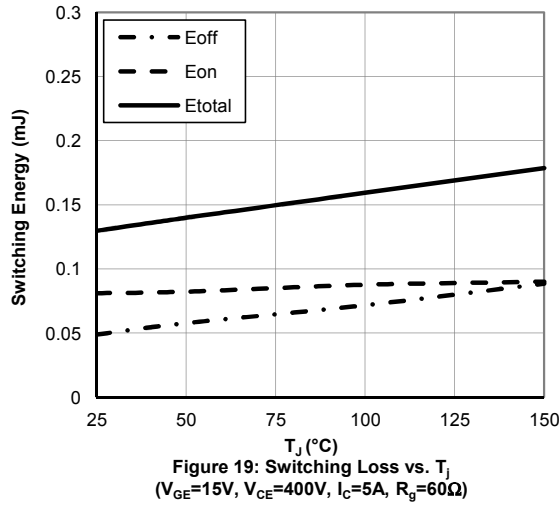
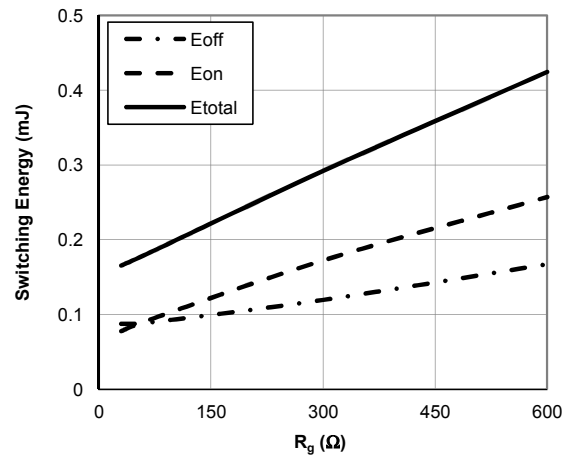
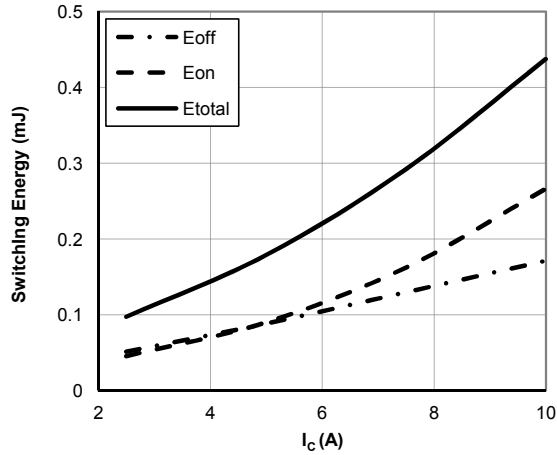
TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS



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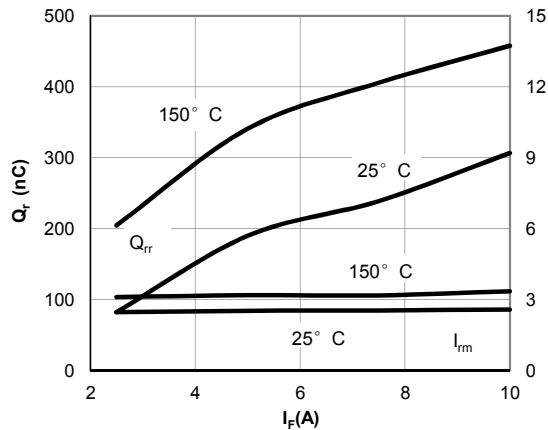


Figure 21: Diode Reverse Recovery Charge and Peak Current vs. Conduction Current
($V_{GE}=15V$, $V_{CE}=400V$, $di/dt=200A/\mu s$)

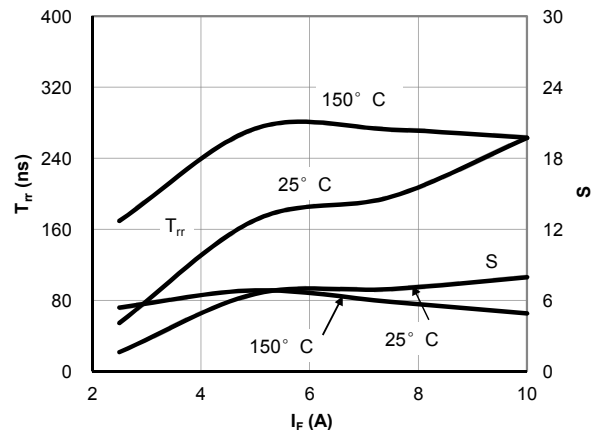


Figure 22: Diode Reverse Recovery Time and Softness Factor vs. Conduction Current
($V_{GE}=15V$, $V_{CE}=400V$, $di/dt=200A/\mu s$)

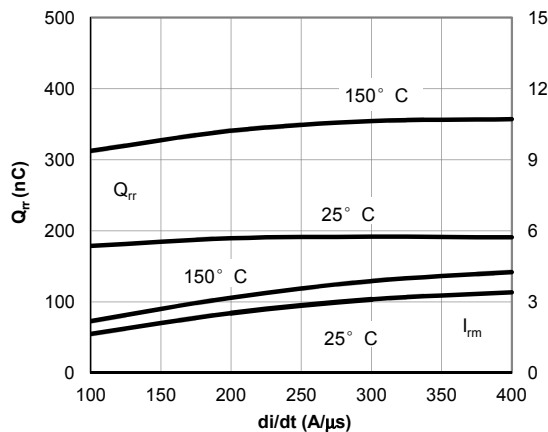


Figure 23: Diode Reverse Recovery Charge and Peak Current vs. di/dt
($V_{GE}=15V$, $V_{CE}=400V$, $I_F=5A$)

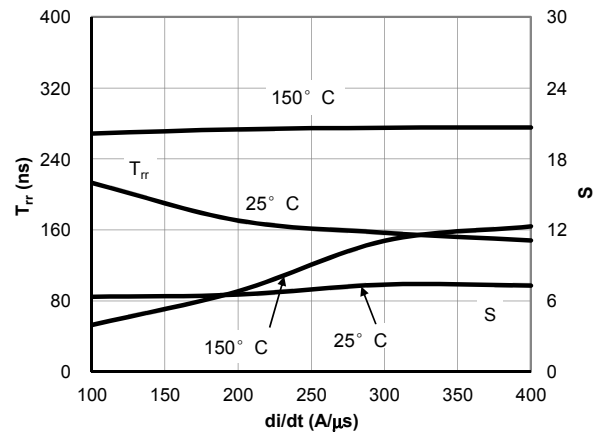


Figure 24: Diode Reverse Recovery Time and Softness Factor vs. di/dt
($V_{GE}=15V$, $V_{CE}=400V$, $I_F=5A$)

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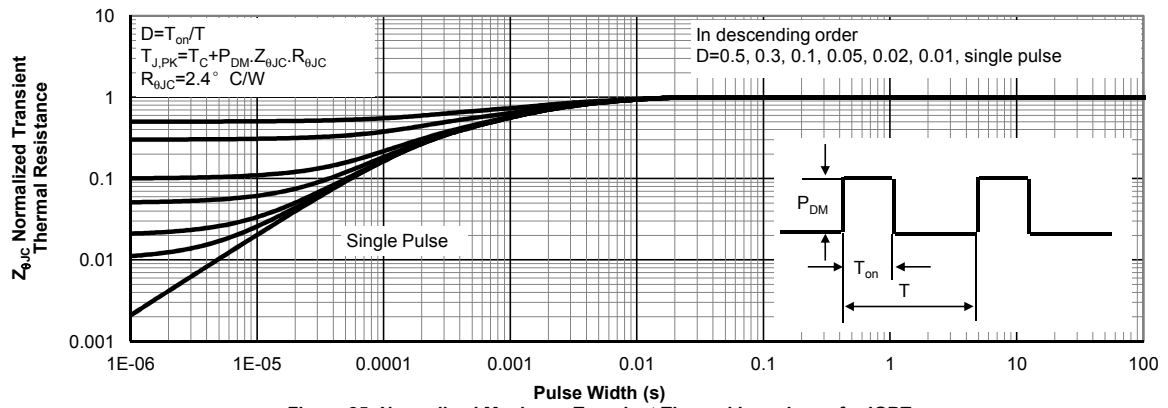


Figure 25: Normalized Maximum Transient Thermal Impedance for IGBT

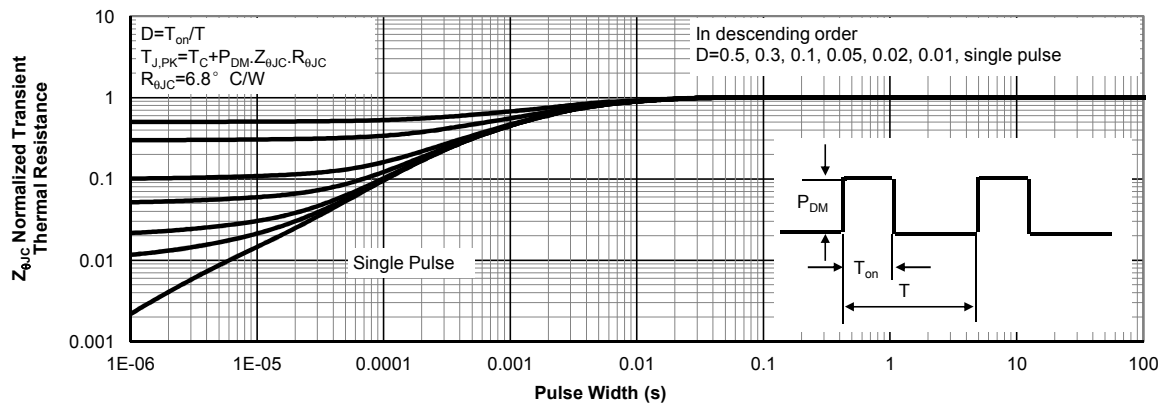


Figure 26: Normalized Maximum Transient Thermal Impedance for Diode

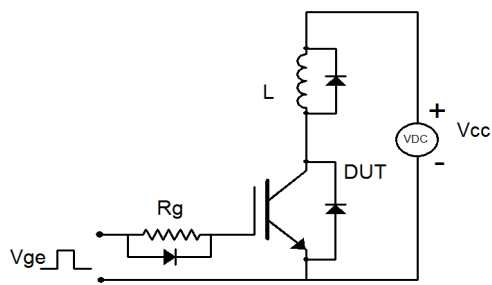


Figure A: Gate Charge Test Circuit & Waveforms

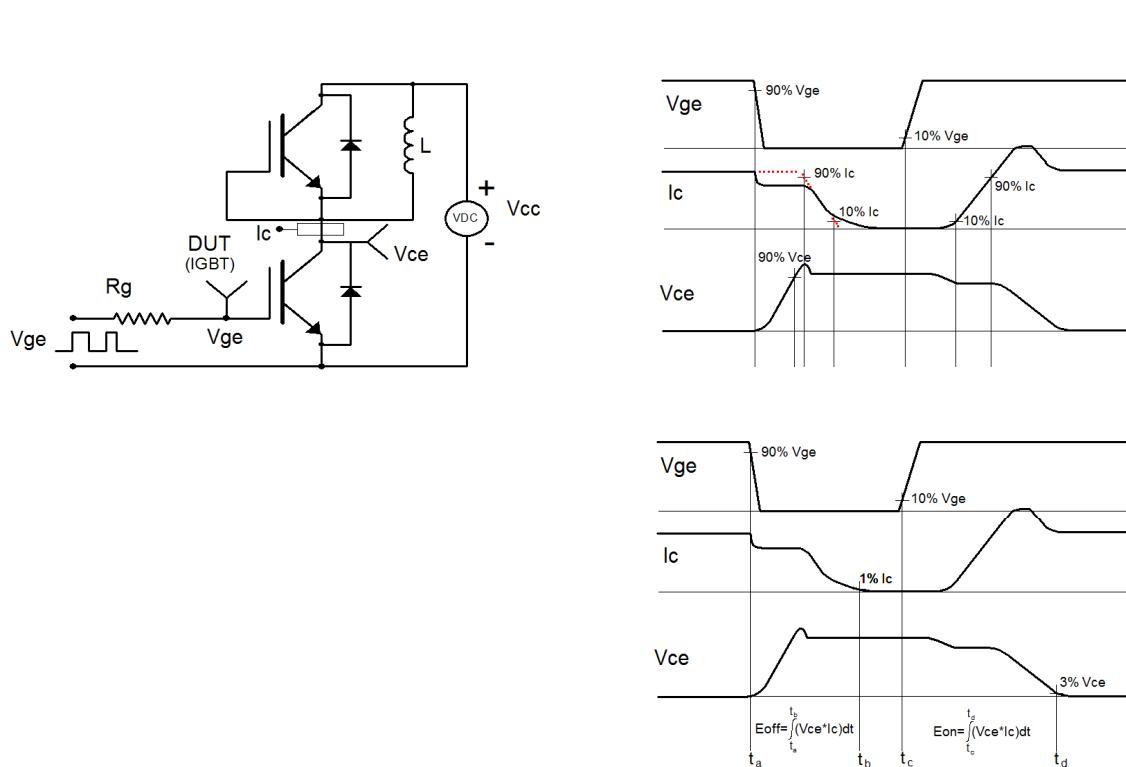


Figure B: Inductive Switching Test Circuit & Waveforms

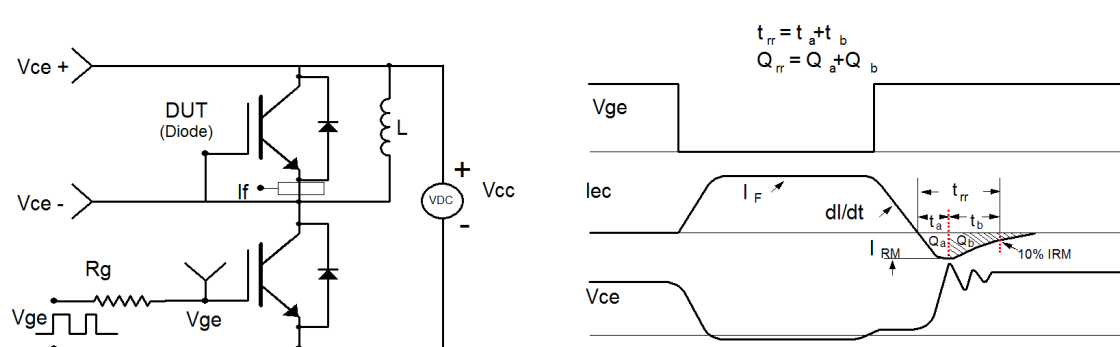


Figure C: Diode Recovery Test Circuit & Waveforms