

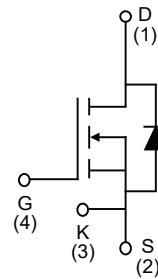
## Features

- Proprietary  $\alpha$ SiC MOSFET technology
- Low loss, with low  $R_{DS, ON}$
- Fast switching with low  $R_G$  and low capacitance
- Optimized gate drive voltage ( $V_{GS} = 15$  V)
- Low reverse recovery diode ( $Q_{rr}$ )
- AEC-Q101 Automotive Qualified

## Applications

- xEV Charger
- Electric Vehicle Supply Equipment (EVSE)
- Motor Drives
- Automotive Inverters

## Pin Configuration



Ordering Part Number	Package Type	Form	Shipping Quantity
AOM015V75X2Q	TO-247-4L	Tube	30/Tube

## Absolute Maximum Ratings

( $T_A = 25^\circ\text{C}$ , unless otherwise noted)

Symbol	Parameter		AOM015V75X2Q	Units
$V_{DS}$	Drain-Source Voltage		750	V
$V_{GS, MAX}$	Gate-Source Voltage	Maximum	-8/+18	V
$V_{GS,OP,TRANS}$		Max Transient <sup>(A)</sup>	-8/+20	
$V_{GS,OP}$		Recommended Operating <sup>(B)</sup>	-5/+15	
$I_D$	Continuous Drain Current	$T_C = 25^\circ\text{C}$	96	A
		$T_C = 100^\circ\text{C}$	67	
$I_{DM}$	Pulsed Drain Current <sup>(C)</sup>		200	
$E_{AS}$	Single Pulsed Avalanche Energy <sup>(D)</sup>		1.8	J
$P_D$	Power Dissipation <sup>(C)</sup>		312	W
$T_J, T_{STG}$	Junction and Storage Temperature Range		-55 to 175	°C
$T_L$	Maximum lead temperature for soldering purpose, 1/8" from case for 5 seconds		300	°C

## Thermal Characteristics

Symbol	Parameter	AOM015V75X2Q	Units
$R_{\theta JA}$	Maximum Junction-to-Ambient <sup>(E,F)</sup>	40	°C/W
$R_{\theta JC}$	Maximum Junction-to-Case <sup>(G)</sup>	0.48	°C/W

## Electrical Characteristics

( $T_A = 25^\circ C$ , unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ	Max	Units
<b>STATIC</b>						
$BV_{DSS}$	Drain-Source Breakdown Voltage	$I_D = 250 \mu A, V_{GS} = 0 V, T_J = 25^\circ C$	750			V
		$I_D = 250 \mu A, V_{GS} = 0 V, T_J = 150^\circ C$	750			V
$I_{DSS}$	Zero Gate Voltage Drain Current	$V_{DS} = 750 V, V_{GS} = 0 V$			100	μA
$I_{GSS}$	Gate-Body Leakage Current	$V_{DS} = 0 V, V_{GS} = +15/-5 V$			250	nA
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_D = 24 mA$	1.8	2.5	3.5	V
$R_{DS(ON)}$	Static Drain-Source On-Resistance	$V_{GS} = 15 V, I_D = 24 A$	$T_J = 25^\circ C$	15	22	mΩ
			$T_J = 175^\circ C$	23		mΩ
$g_{FS}$	Forward Transconductance	$V_{DS} = 20 V, I_D = 24 V$		23		S
$V_{SD}$	Diode Forward Voltage	$I_S = 24 A, V_{GS} = -5 V$		4	5	V
<b>DYNAMIC</b>						
$C_{iss}$	Input Capacitance	$V_{GS} = 0 V, V_{DS} = 400 V, f = 1 MHz$		4880		pF
$C_{oss}$	Output Capacitance			371		pF
$C_{rss}$	Reverse Transfer Capacitance			25		pF
$E_{oss}$	Cross Stored Energy			36		μJ
$R_G$	Gate Resistance	$f = 1 MHz$		1.1		Ω
<b>SWITCHING</b>						
$Q_g$	Total Gate Charge	$V_{GS} = -5/+15 V, V_{DS} = 520 V, I_D = 24 A$		152		nC
$Q_{gs}$	Gate Source Charge			58		nC
$Q_{gd}$	Gate Drain Charge			30		nC
$t_{D(on)}$	Turn-On Delay Time	$V_{GS} = -5 V/+15 V, V_{DS} = 400 V, I_D = 50 A, R_{G,ON} = 2 \Omega, R_{G,OFF} = 0 \Omega$		11		ns
$t_r$	Turn-On Rise Time			13		ns
$t_{D(off)}$	Turn-Off Delay Time			16		ns
$t_f$	Turn-Off Fall Time			5		ns
$E_{on}$	Turn-On Energy		$L = 30 \mu H$	180		μJ
$E_{off}$	Turn-Off Energy		FWD: AOM015V75X2Q	5		μJ
$E_{tot}$	Total Switching Energy	$I_F = 50 A, dI/dt = 2500 A/\mu s, V_{DS} = 400 V$		185		μJ
$t_{rr}$	Body Diode Reverse Recovery Time			17.5		ns
$I_{rm}$	Peak Reverse Recovery Current			20		A
$Q_{rr}$	Body Diode Reverse Recovery Charge			211		nC

### Notes:

- A.  $t_{ON} < 1\% * (\text{Duty Cycle}) / (\text{Frequency})$ ,  $t < 25$  hrs over lifetime
- B. Device can be operated at  $V_{GS} = 0/15 V$ . Actual operating  $V_{GS}$  will depend on application specifics such as parasitic inductance and dV/dt but should not exceed maximum ratings.
- C. The power dissipation  $P_D$  is based on  $T_{J(MAX)} = 175^\circ C$ , using junction-to-case thermal resistance, and is more useful in setting the upper dissipation limit for cases where additional heatsinking is used.
- D.  $L = 5 mH$ ,  $I_{AS} = 27 A$ ,  $R_G = 10 \Omega$ , Starting  $T_J = 25^\circ C$ .
- E. The value of  $R_{\theta JA}$  is measured with the device in a still air environment

with  $T_A = 25^\circ C$ .

- F. The  $R_{\theta JA}$  is the sum of the thermal impedance from junction to case  $R_{\theta JC}$  and case to ambient.
- G. The value of  $R_{\theta JC}$  is measured with the device mounted to a large heatsink, assuming a maximum junction temperature of  $T_{J(MAX)} = 175^\circ C$ .
- H. The static characteristics in Figures 1 to 8 are obtained using  $< 300 \mu s$  pulses, duty cycle 0.5% max.
- I. These curves are based on  $R_{\theta JC}$  which is measured with the device mounted to a large heatsink, assuming a maximum junction temperature of  $T_{J(MAX)} = 175^\circ C$ . The SOA curve provides a single pulse rating.

## Typical Electrical and Thermal Characteristics

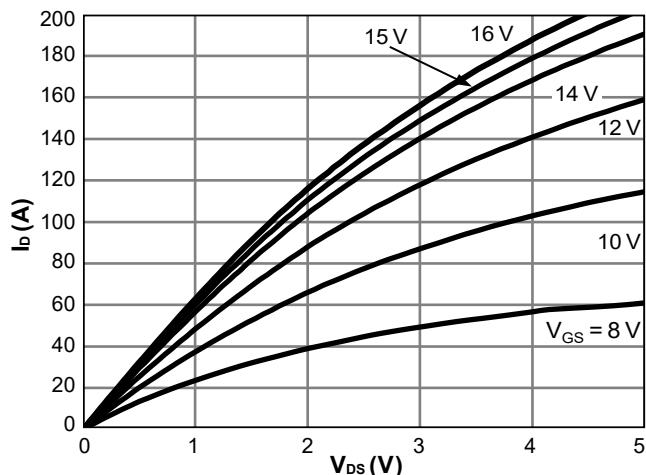


Figure 1. On-Region Characteristics  $T_J = 25^\circ\text{C}$

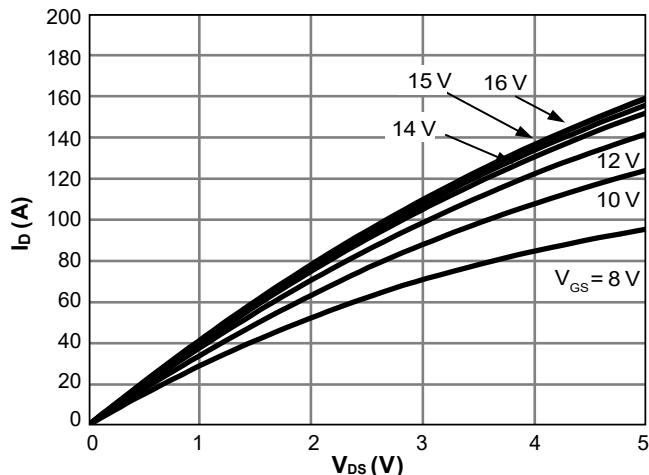


Figure 2. On-Region Characteristics  $T_J = 175^\circ\text{C}$

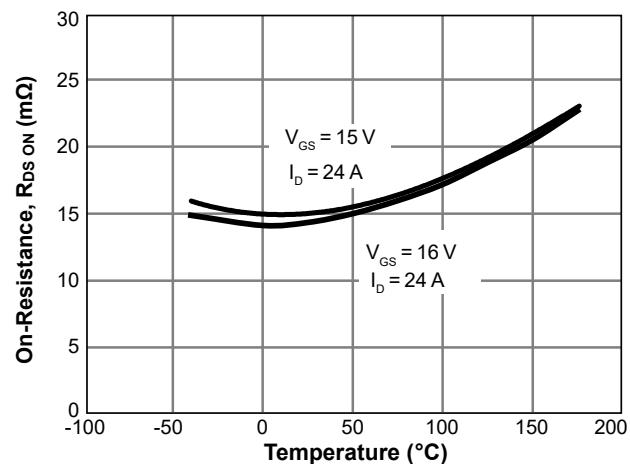


Figure 3. On-Resistance vs. Junction Temperature

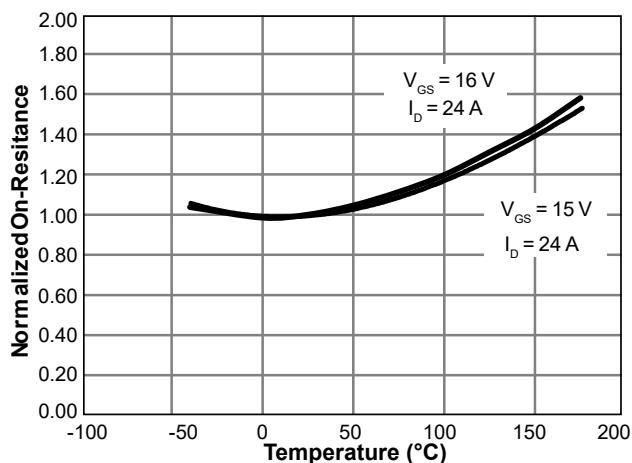


Figure 4. Normalized On-Resistance vs. Junction Temperature

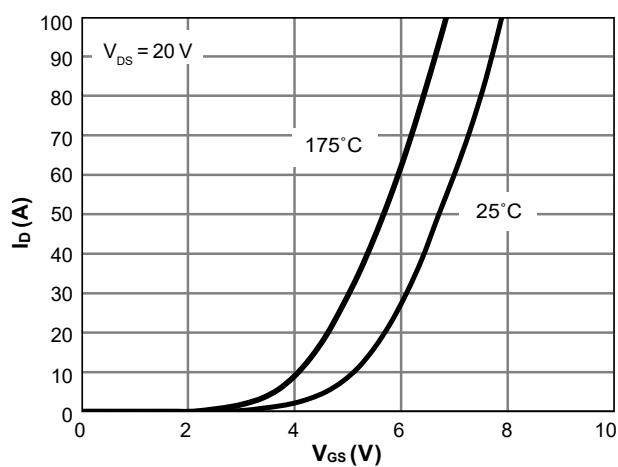


Figure 5. Transfer Characteristics

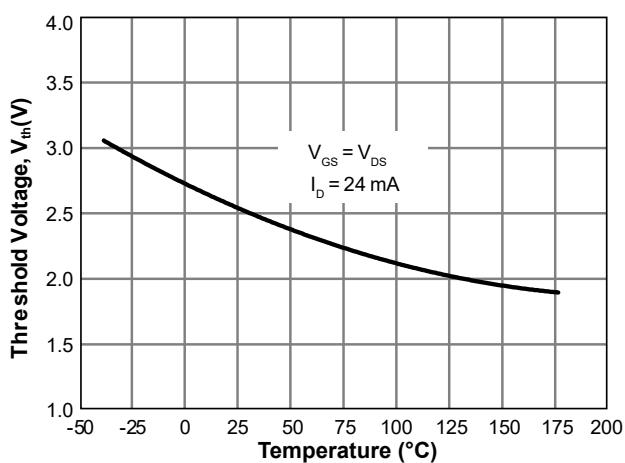


Figure 6. Threshold Voltage vs. Junction Temperature

## Typical Electrical and Thermal Characteristics (Continued)

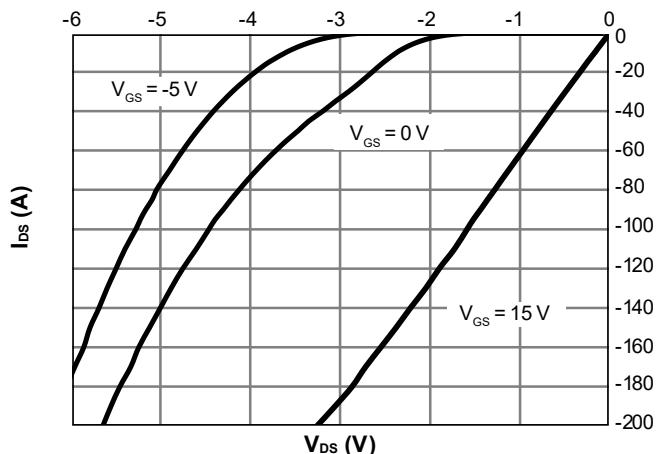


Figure 7. Body-Diode Characteristics at 25°C

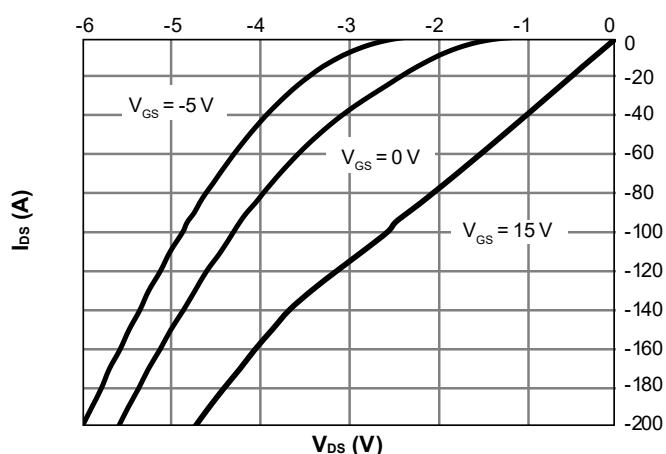


Figure 8. Body-Diode Characteristics at 175°C

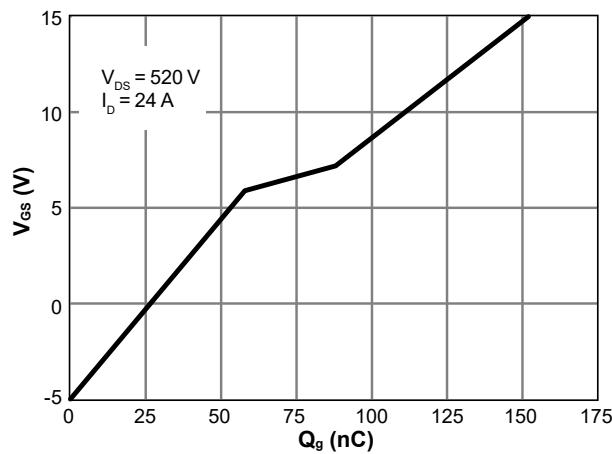


Figure 9. Gate-Charge Characteristics

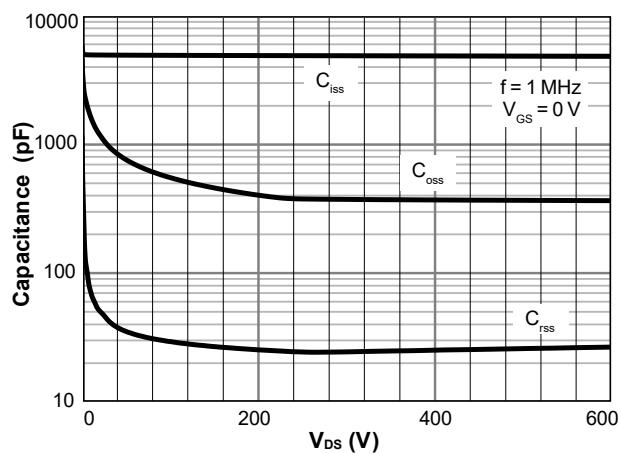


Figure 10. Capacitance Characteristics

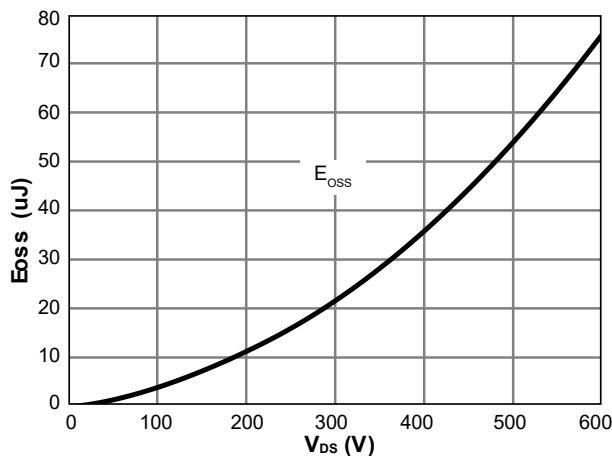


Figure 11.  $E_{oss}$  stored Energy

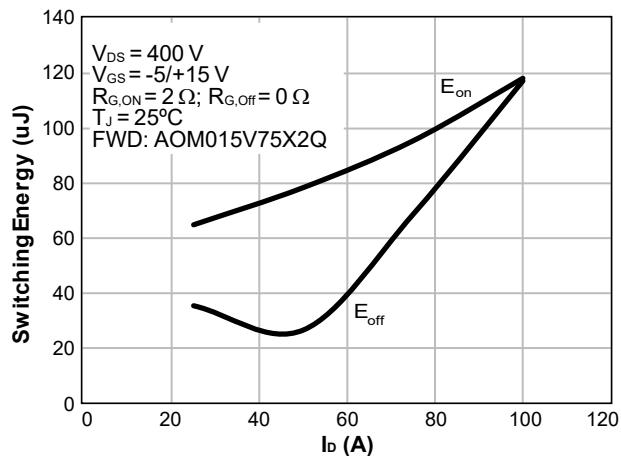


Figure 12. Switching Energy vs. Drain Current

## Typical Electrical and Thermal Characteristics (Continued)

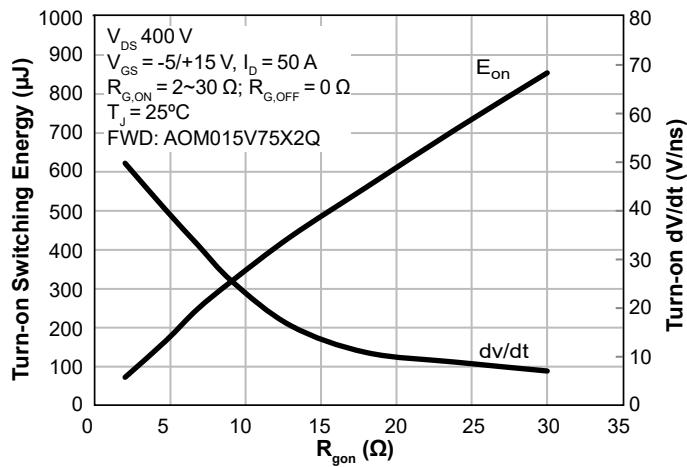


Figure 13. Turn-On Energy and  $dV/dt$  vs.  
 External Gate Resistance

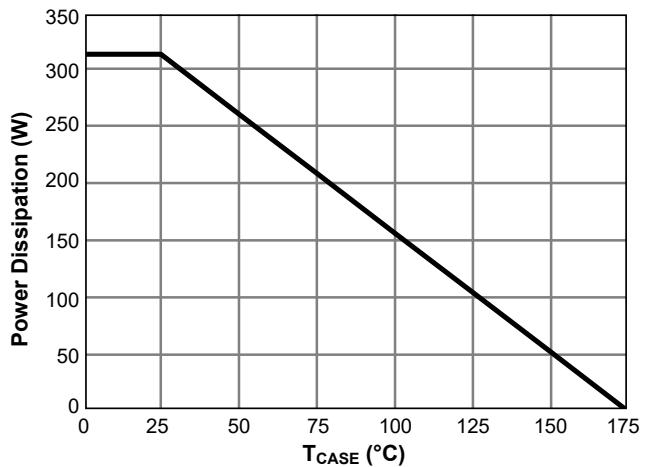


Figure 14. Power De-rating (Note I)

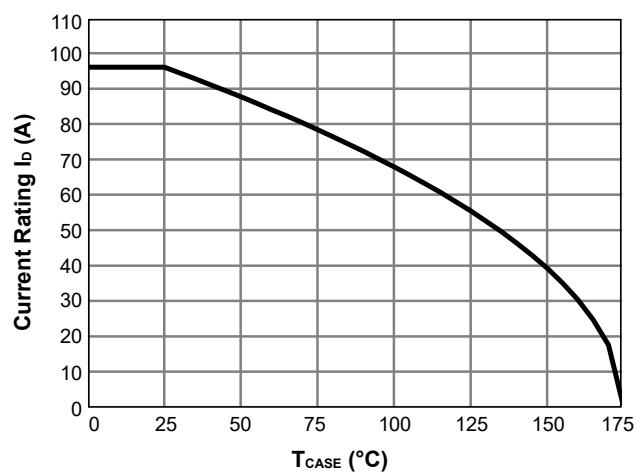


Figure 15. Current De-rating (Note I)

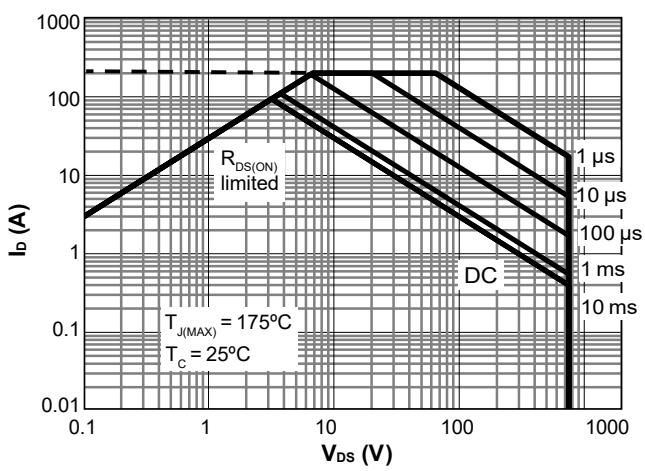


Figure 16. Maximum Forward Biased Safe Operating  
 Area for AOM015V75X2Q (Note I)

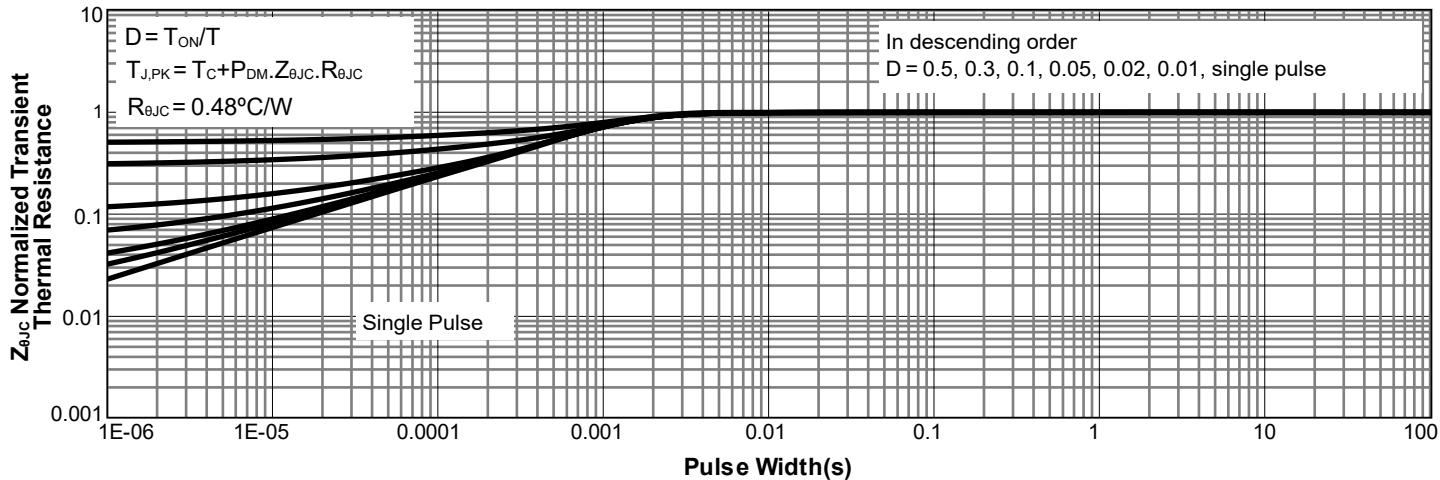


Figure 17. Normalized Maximum Transient Thermal Impedance for AOM015V75X2Q (Note I)

## Test Circuits and Waveforms

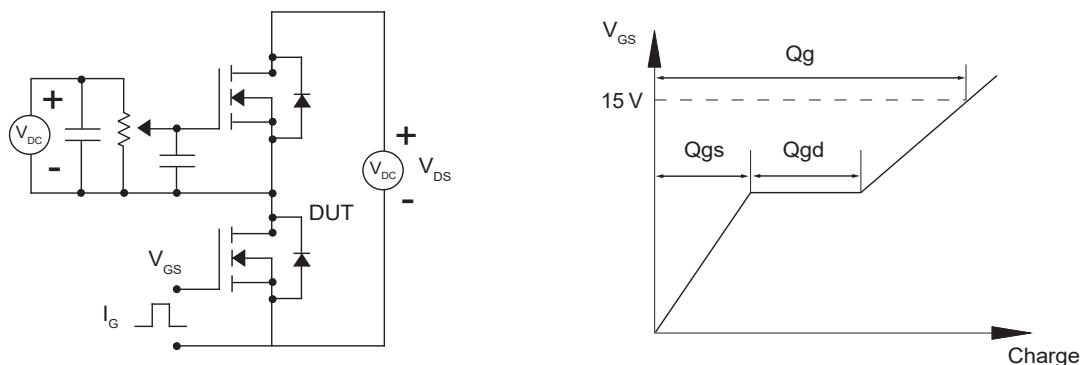


Figure 18. Gate Charge Test Circuits and Waveforms

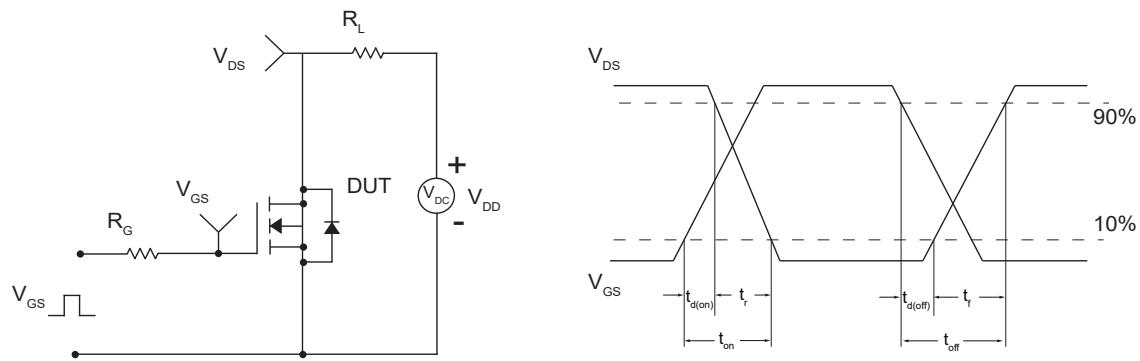


Figure 19. Resistive Switching Test Circuit and Waveforms

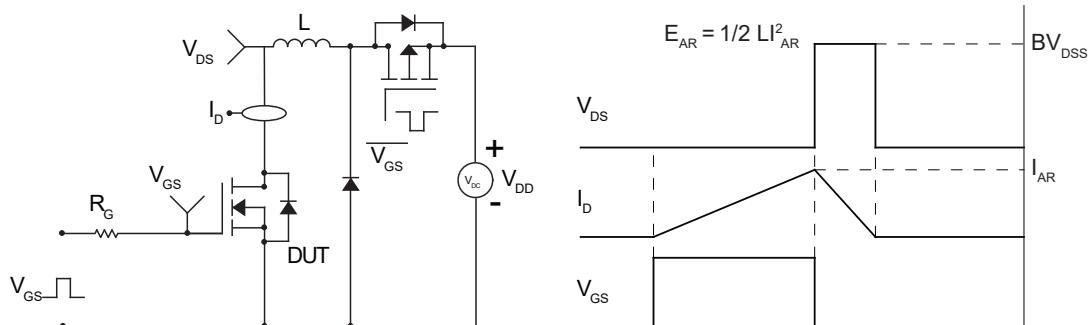


Figure 20. Unclamped Inductive Switching (UIS) Test Circuit and Waveforms

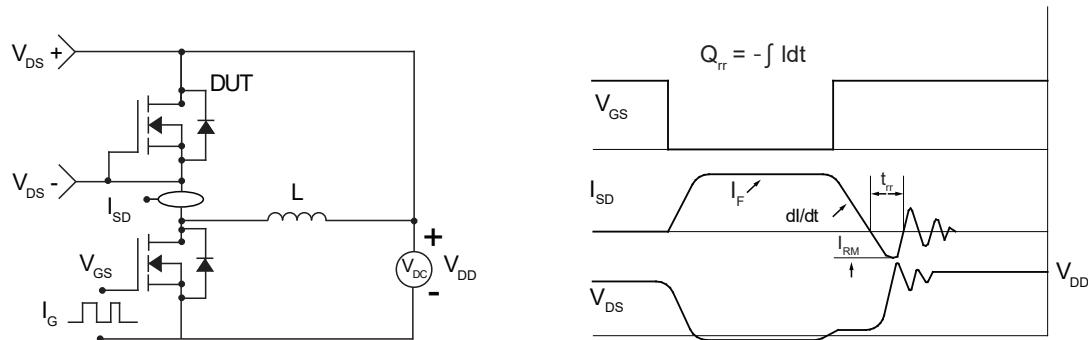
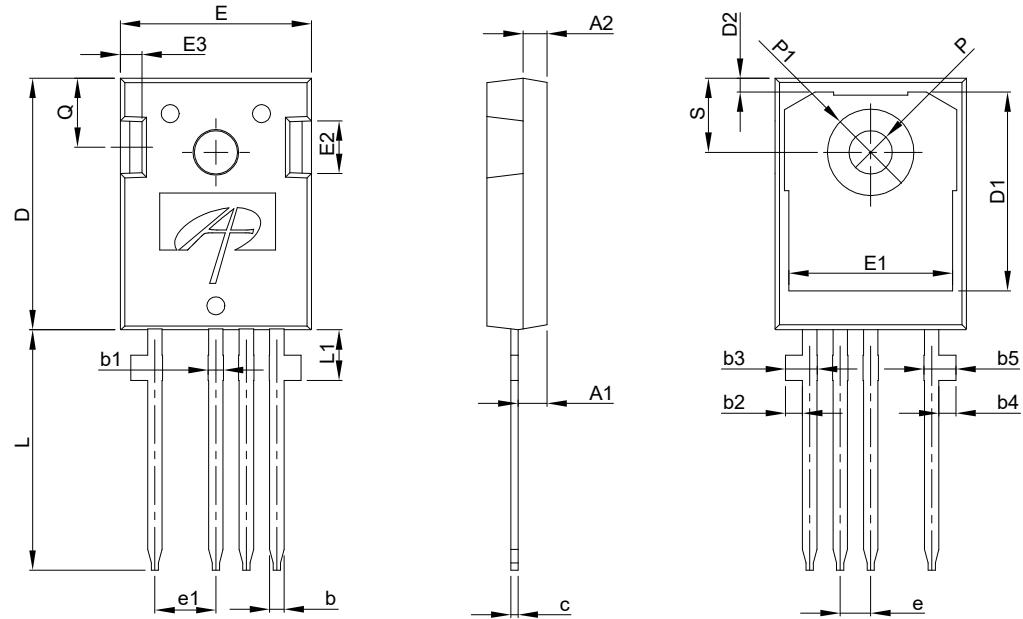


Figure 21. Diode Recovery Test Circuits and Waveforms

## Package Dimensions, TO-247-4L



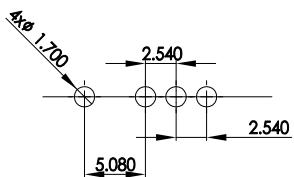
TOP VIEW

SIDE VIEW

BOTTOM VIEW



SIDE VIEW



RECOMMENDED THROUGH HOLES  
FOR LAND PATTERN

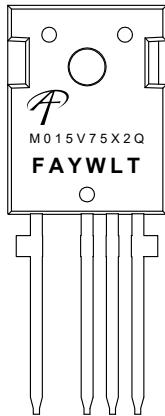
NOTE:

1. CONTROLLED DIMENSIONS ARE IN MILLIMETERS.

SYMBOLS	DIM. IN MM			DIM. IN INCH		
	MIN.	NOM.	MAX.	MIN.	NOM.	MAX.
A	4.90	5.00	5.10	0.193	0.197	0.201
A1	2.32	2.42	2.52	0.091	0.095	0.099
A2	1.90	2.00	2.10	0.075	0.079	0.083
b	1.17	1.22	1.27	0.046	0.048	0.050
b1	1.20	1.30	1.40	0.047	0.051	0.055
b2	1.31	1.41	1.51	0.052	0.056	0.059
b3	2.45	2.65	2.85	0.096	0.104	0.112
b4	1.31	1.41	1.51	0.052	0.056	0.059
b5	2.45	2.65	2.85	0.096	0.104	0.112
c	0.57	0.62	0.67	0.022	0.024	0.026
D	20.80	20.95	21.10	0.819	0.825	0.831
D1	16.25	16.55	16.85	0.640	0.652	0.663
D2	1.00	1.15	1.30	0.039	0.045	0.051
E	15.77	15.92	16.07	0.621	0.627	0.632
E1	13.43	13.63	13.83	0.529	0.536	0.544
E2	4.29	4.39	4.49	0.169	0.173	0.177
E3	1.70	1.80	1.90	0.067	0.071	0.075
e	2.54BSC			0.1000BSC		
e1	5.08BSC			0.2000BSC		
N	4			4		
L	19.82	20.02	20.22	0.780	0.788	0.796
L1	4.01	4.21	4.41	0.158	0.166	0.174
P	3.50	3.60	3.70	0.138	0.142	0.146
P1	7.00	7.20	7.40	0.276	0.283	0.291
Q	5.65	5.75	5.85	0.222	0.226	0.230
S	6.07	6.17	6.27	0.239	0.243	0.247

## Part Marking

AOM015V75X2Q  
TO-247-4L



## LEGAL DISCLAIMER

Applications or uses as critical components in life support devices or systems are not authorized. Alpha and Omega Semiconductor does not assume any liability arising out of such applications or uses of its products. AOS reserves the right to make changes to product specifications without notice. It is the responsibility of the customer to evaluate suitability of the product for their intended application. Customer shall comply with applicable legal requirements, including all applicable export control rules, regulations and limitations.

AOS's products are provided subject to AOS's terms and conditions of sale which are set forth at:  
[http://www.aosmd.com/terms\\_and\\_conditions\\_of\\_sale](http://www.aosmd.com/terms_and_conditions_of_sale)

## LIFE SUPPORT POLICY

ALPHA AND OMEGA SEMICONDUCTOR PRODUCTS ARE NOT AUTHORIZED FOR USE AS CRITICAL COMPONENTS IN LIFE SUPPORT DEVICES OR SYSTEMS.

As used herein:

1. Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body or (b) support or sustain life, and (c) whose failure to perform when properly used in accordance with instructions for use provided in the labeling, can be reasonably expected to result in a significant injury of the user.
2. A critical component in any component of a life support device, or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.