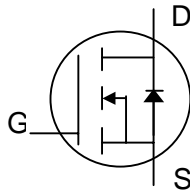




**N-channel Enhancement-mode Power MOSFET**

- Simple Drive Requirement**
- Repetitive Avalanche Rated**
- Fast Switching Performance**
- RoHS-compliant, halogen-free**



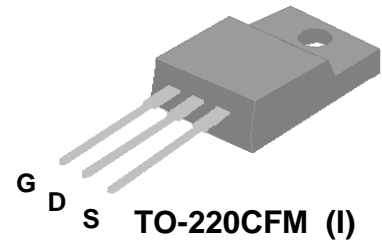
$BV_{DSS}$	700V
$R_{DS(ON)}$	4.4Ω
$I_D$	2.5A

**Description**

Advanced Power MOSFETs from APEC provide the designer with the best combination of fast switching, low on-resistance and cost-effectiveness.

The AP03N70I-H-HF-3 is in the TO-220CFM isolated through-hole package which is widely used in commercial and industrial applications where a small PCB footprint or an attached isolated heatsink is required.

This device is well suited for use in high voltage applications such as off-line AC/DC converters.



**Absolute Maximum Ratings**

Symbol	Parameter	Rating	Units
$V_{DS}$	Drain-Source Voltage	700	V
$V_{GS}$	Gate-Source Voltage	± 30	V
$I_D$ at $T_C=25^{\circ}C$	Continuous Drain Current <sup>3</sup>	2.5	A
$I_D$ at $T_C=100^{\circ}C$	Continuous Drain Current <sup>3</sup>	1.6	A
$I_{DM}$	Pulsed Drain Current <sup>1</sup>	8	A
$P_D$ at $T_C=25^{\circ}C$	Total Power Dissipation	29	W
	Linear Derating Factor	0.23	W/°C
$E_{AS}$	Single Pulse Avalanche Energy <sup>2</sup>	32	mJ
$I_{AR}$	Avalanche Current	2.5	A
$T_{STG}$	Storage Temperature Range	-55 to 150	°C
$T_J$	Operating Junction Temperature Range	-55 to 150	°C

**Thermal Data**

Symbol	Parameter	Value	Unit
Rthj-c	Maximum Thermal Resistance, Junction-case	4.3	°C/W
Rthj-a	Maximum Thermal Resistance, Junction-ambient	65	°C/W

**Ordering Information**

**AP03N70I-H-HF-3TB** : in RoHS-compliant halogen-free TO-220CFM, shipped in tubes (50pcstube)



**Electrical Specifications at  $T_j=25^\circ\text{C}$  (unless otherwise specified)**

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
$BV_{DSS}$	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D=1mA$	700	-	-	V
$\Delta BV_{DSS}/\Delta T_j$	Breakdown Voltage Temperature Coefficient	Reference to $25^\circ\text{C}$ , $I_D=1mA$	-	0.6	-	V/ $^\circ\text{C}$
$R_{DS(ON)}$	Static Drain-Source On-Resistance <sup>3</sup>	$V_{GS}=10V, I_D=1.6A$	-	-	4.4	$\Omega$
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS}=V_{GS}, I_D=250\mu A$	2	-	4	V
$g_{fs}$	Forward Transconductance	$V_{DS}=10V, I_D=1.6A$	-	2	-	S
$I_{DSS}$	Drain-Source Leakage Current	$V_{DS}=600V, V_{GS}=0V$	-	-	10	$\mu A$
	Drain-Source Leakage Current ( $T_j=150^\circ\text{C}$ )	$V_{DS}=480V, V_{GS}=0V$	-	-	100	$\mu A$
$I_{GSS}$	Gate-Source Leakage	$V_{GS}=\pm 30V, V_{DS}=0V$	-	-	$\pm 100$	nA
$Q_g$	Total Gate Charge <sup>3</sup>	$I_D=1A$	-	12	20	nC
$Q_{gs}$	Gate-Source Charge	$V_{DS}=480V$	-	3	-	nC
$Q_{gd}$	Gate-Drain ("Miller") Charge	$V_{GS}=10V$	-	4	-	nC
$t_{d(on)}$	Turn-on Delay Time <sup>3</sup>	$V_{DD}=300V$	-	8.5	-	ns
$t_r$	Rise Time	$I_D=2.5A$	-	6	-	ns
$t_{d(off)}$	Turn-off Delay Time	$R_G=10\Omega, V_{GS}=10V$	-	19	-	ns
$t_f$	Fall Time	$R_D=120\Omega$	-	8	-	ns
$C_{iss}$	Input Capacitance	$V_{GS}=0V$	-	590	950	pF
$C_{oss}$	Output Capacitance	$V_{DS}=25V$	-	50	-	pF
$C_{rss}$	Reverse Transfer Capacitance	$f=1.0MHz$	-	6	-	pF

**Source-Drain Diode**

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
$V_{SD}$	Forward On Voltage <sup>3</sup>	$I_S=3A, V_{GS}=0V$	-	-	1.5	V
$t_{rr}$	Reverse Recovery Time <sup>3</sup>	$I_S=3A, V_{GS}=0V,$	-	407	-	ns
$Q_{rr}$	Reverse Recovery Charge	$di/dt=100A/\mu s$	-	2110	-	nC

**Notes:**

1. Pulse width limited by maximum junction temperature.
2. Starting  $T_j=25^\circ\text{C}$ ,  $V_{DD}=50V$ ,  $L=15mH$ ,  $R_G=25\Omega$ ,  $I_{AS}=3A$ .
3. Pulse test - pulse width  $\leq 300\mu s$ , duty cycle  $\leq 2\%$

THIS PRODUCT IS SENSITIVE TO ELECTROSTATIC DISCHARGE, PLEASE HANDLE WITH CAUTION.

USE OF THIS PRODUCT AS A CRITICAL COMPONENT IN LIFE SUPPORT OR OTHER SIMILAR SYSTEMS IS NOT AUTHORIZED.

APEC DOES NOT ASSUME ANY LIABILITY ARISING OUT OF THE APPLICATION OR USE OF ANY PRODUCT OR CIRCUIT DESCRIBED HEREIN; NEITHER DOES IT CONVEY ANY LICENSE UNDER ITS PATENT RIGHTS, NOR THE RIGHTS OF OTHERS.

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Typical Electrical Characteristics

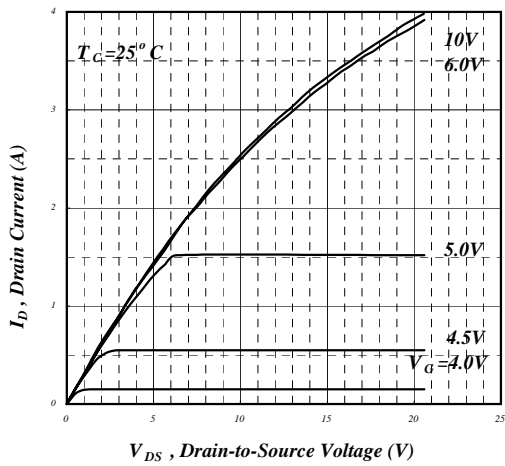


Fig 1. Typical Output Characteristics

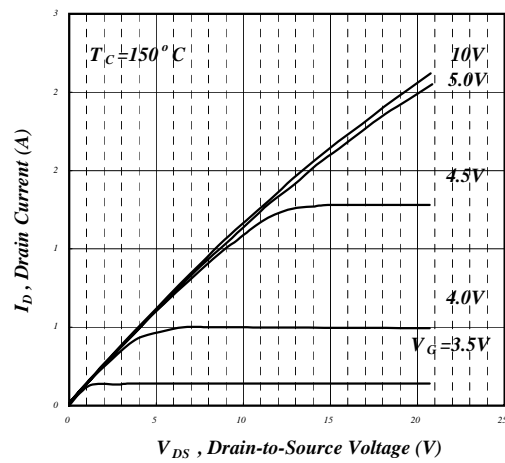


Fig 2. Typical Output Characteristics

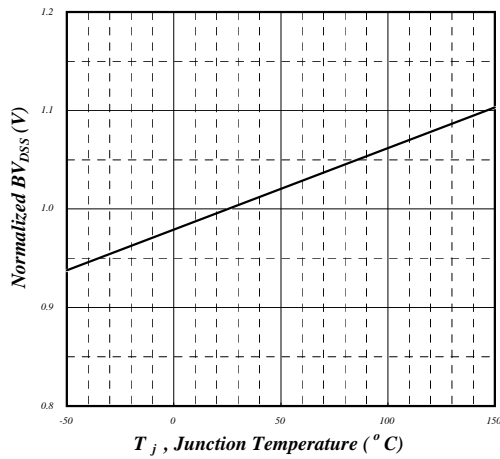


Fig 3. Normalized  $BV_{DSS}$  vs. Junction Temperature

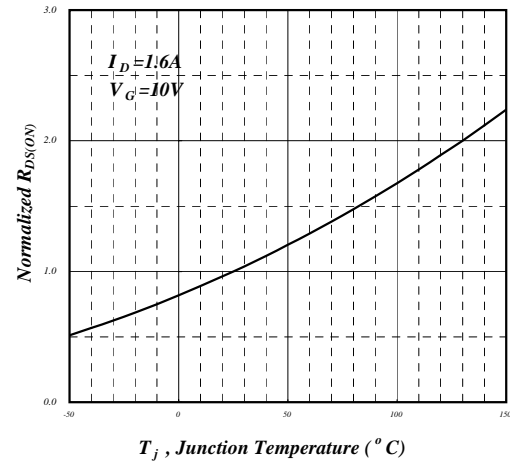


Fig 4. Normalized On-Resistance vs. Junction Temperature

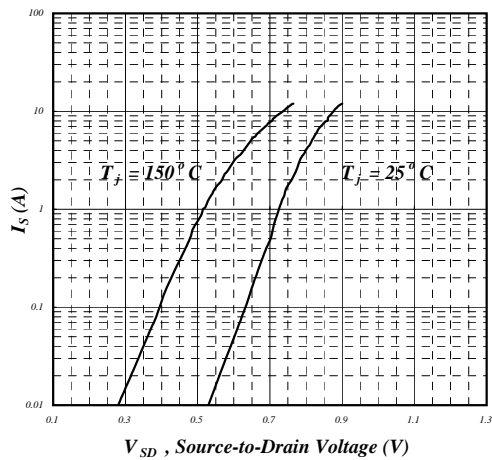


Fig 5. Forward Characteristic of Reverse Diode

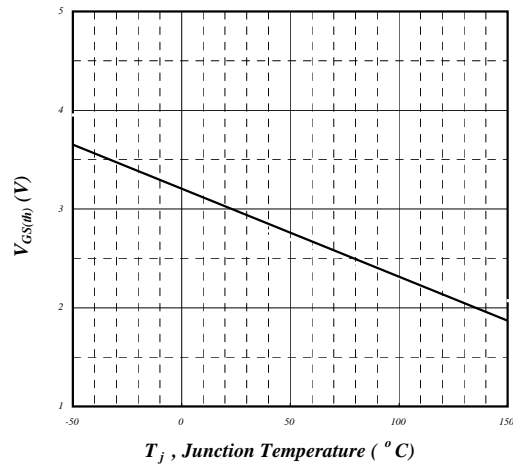


Fig 6. Gate Threshold Voltage vs. Junction Temperature



Typical Electrical Characteristics (cont.)

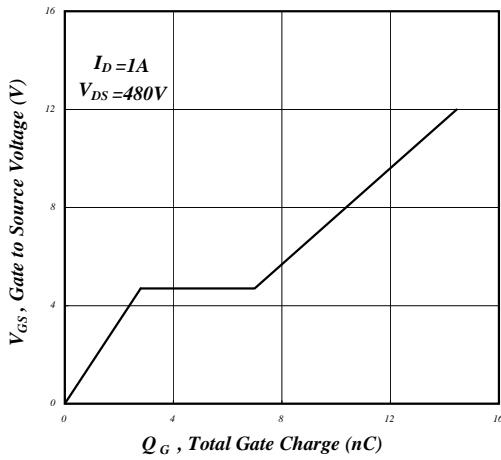


Fig 7. Gate Charge Characteristics

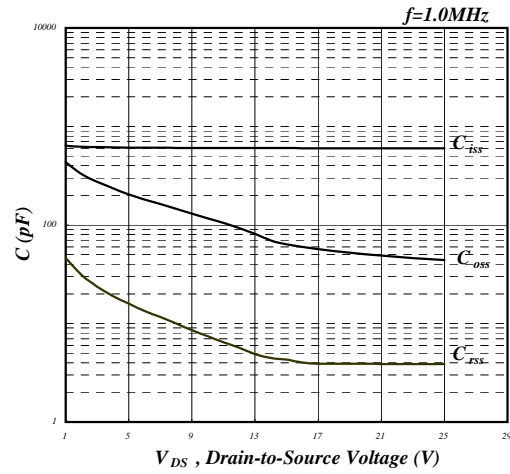


Fig 8. Typical Capacitance Characteristics

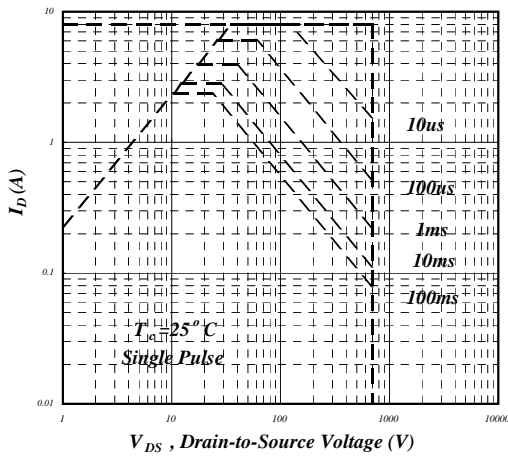


Fig 9. Maximum Safe Operating Area

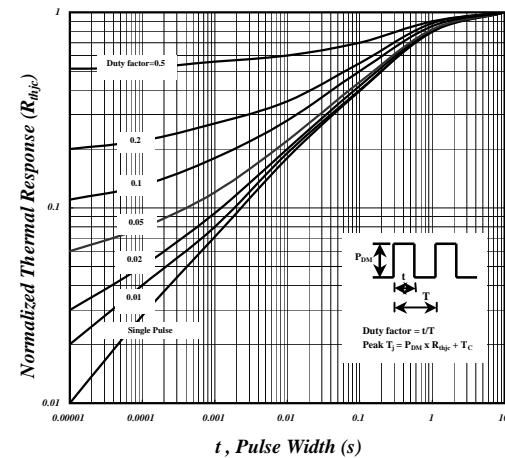


Fig 10. Effective Transient Thermal Impedance

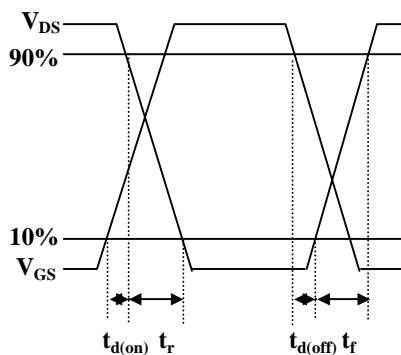


Fig 11. Switching Time Waveforms

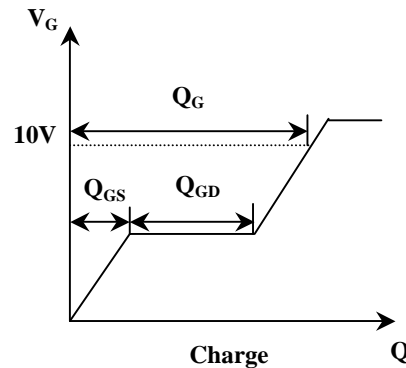
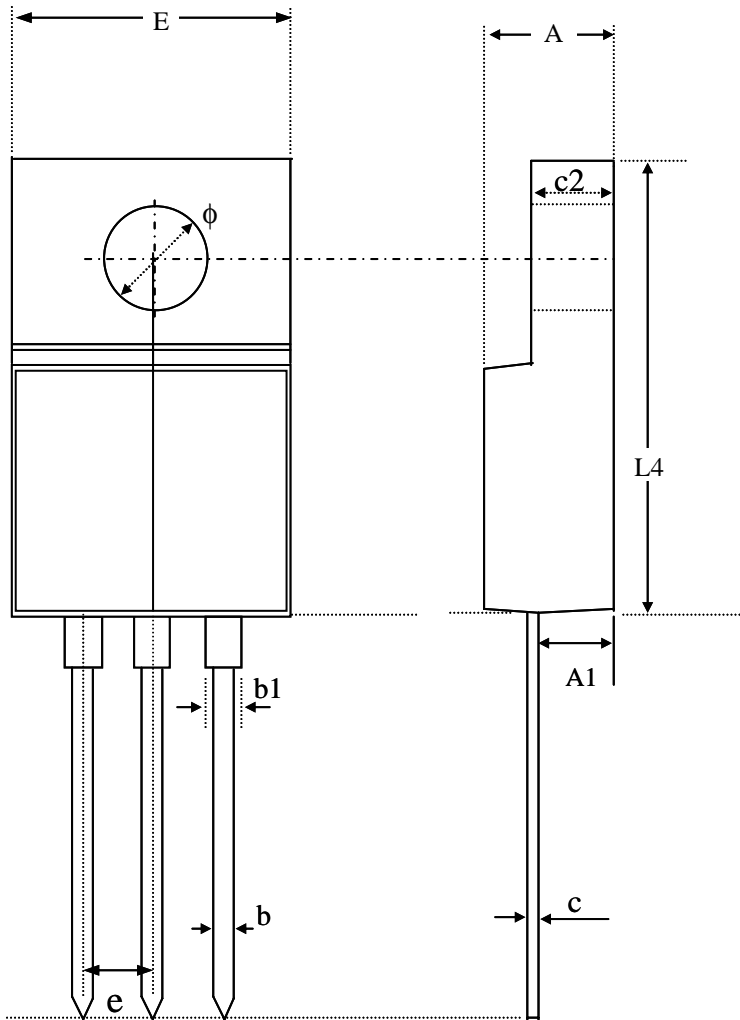


Fig 12. Gate Charge Waveform



**Package Dimensions: TO-220CFM**



SYMBOLS	Millimeters		
	MIN	NOM	MAX
<b>A</b>	4.30	4.60	4.90
<b>A1</b>	2.30	2.65	3.00
<b>b</b>	0.50	0.70	0.90
<b>b1</b>	0.95	1.20	1.50
<b>c</b>	0.45	0.65	0.80
<b>c2</b>	2.30	2.60	2.90
<b>E</b>	9.70	10.00	10.40
<b>L4</b>	14.70	15.40	16.10
$\phi$	----	3.20	----
<b>e</b>	----	2.54	----

1. All dimensions are in millimeters.
2. Dimensions do not include mold protrusions.

**Marking Information:**

