



**Advanced Power
Electronics Corp.**

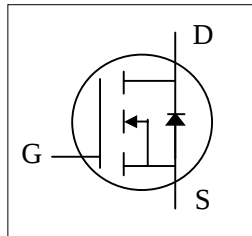
AP03N90P-HF

Halogen-Free Product

N-CHANNEL ENHANCEMENT MODE

POWER MOSFET

- ▼ 100% Avalanche Test
- ▼ Simple Drive Requirement
- ▼ Fast Switching Characteristic
- ▼ RoHS Compliant & Halogen-Free

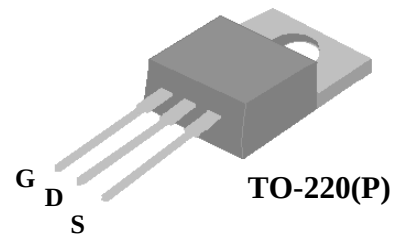


BV_{DSS}	900V
$R_{DS(ON)}$	5.2Ω
I_D	3A

Description

AP03N90 series are specially designed as main switching devices for universal 90~265VAC off-line AC/DC converter applications.

The TO-220 package is widely preferred for commercial-industrial applications. The device is suited for switch mode power supplies, DC-AC converters and high current high speed switching circuits.



Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	900	V
V_{GS}	Gate-Source Voltage	± 30	V
$I_D@T_C=25^\circ\text{C}$	Continuous Drain Current, V_{GS} @ 10V	3	A
$I_D@T_C=100^\circ\text{C}$	Continuous Drain Current, V_{GS} @ 10V	1.89	A
I_{DM}	Pulsed Drain Current ¹	10	A
$P_D@T_C=25^\circ\text{C}$	Total Power Dissipation	83.3	W
E_{AS}	Single Pulse Avalanche Energy ³	4.5	mJ
T_{STG}	Storage Temperature Range	-55 to 150	$^\circ\text{C}$
T_J	Operating Junction Temperature Range	-55 to 150	$^\circ\text{C}$

Thermal Data

Symbol	Parameter	Value	Units
R_{thj-c}	Maximum Thermal Resistance, Junction-case	1.5	$^\circ\text{C}/\text{W}$
R_{thj-a}	Maximum Thermal Resistance, Junction-ambient	62	$^\circ\text{C}/\text{W}$


Electrical Characteristics@T_j=25°C(unless otherwise specified)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =250uA	900	-	-	V
R _{DS(ON)}	Static Drain-Source On-Resistance ²	V _{GS} =10V, I _D =1.2A	-	-	5.2	Ω
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _D =250uA	2	-	4	V
g _{fs}	Forward Transconductance	V _{DS} =10V, I _D =1A	-	2	-	S
I _{DSS}	Drain-Source Leakage Current	V _{DS} =720V, V _{GS} =0V	-	-	25	uA
I _{GSS}	Gate-Source Leakage	V _{GS} =±30V, V _{DS} =0V	-	-	±100	nA
Q _g	Total Gate Charge ²	I _D =1A	-	18	29	nC
Q _{gs}	Gate-Source Charge	V _{DS} =480V	-	3.5	-	nC
Q _{gd}	Gate-Drain ("Miller") Charge	V _{GS} =10V	-	7	-	nC
t _{d(on)}	Turn-on Delay Time ²	V _{DD} =300V	-	20	-	ns
t _r	Rise Time	I _D =1A	-	14	-	ns
t _{d(off)}	Turn-off Delay Time	R _G =50Ω	-	105	-	ns
t _f	Fall Time	V _{GS} =10V	-	24	-	ns
C _{iss}	Input Capacitance	V _{GS} =0V	-	800	1280	pF
C _{oss}	Output Capacitance	V _{DS} =25V	-	55	-	pF
C _{rss}	Reverse Transfer Capacitance	f=1.0MHz	-	4	-	pF
R _g	Gate Resistance	f=1.0MHz	-	4	6	Ω

Source-Drain Diode

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
V _{SD}	Forward On Voltage ²	I _S =1.2A, V _{GS} =0V	-	-	1.5	V
t _{rr}	Reverse Recovery Time ²	I _S =1A, V _{GS} =0V	-	320	-	ns
Q _{rr}	Reverse Recovery Charge	dI/dt=100A/μs	-	1.3	-	μC

Notes:

- 1.Pulse width limited by max. junction temperature.
- 2.Pulse test
- 3.Starting T_j=25°C , V_{DD}=50V , L=1mH , R_G=25Ω

THIS PRODUCT IS SENSITIVE TO ELECTROSTATIC DISCHARGE, PLEASE HANDLE WITH CAUTION.

USE OF THIS PRODUCT AS A CRITICAL COMPONENT IN LIFE SUPPORT OR OTHER SIMILAR SYSTEMS IS NOT AUTHORIZED.

APEC DOES NOT ASSUME ANY LIABILITY ARISING OUT OF THE APPLICATION OR USE OF ANY PRODUCT OR CIRCUIT DESCRIBED HEREIN; NEITHER DOES IT CONVEY ANY LICENSE UNDER ITS PATENT RIGHTS, NOR THE RIGHTS OF OTHERS.

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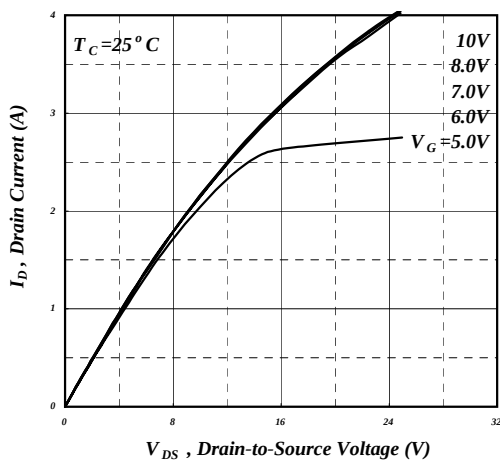


Fig 1. Typical Output Characteristics

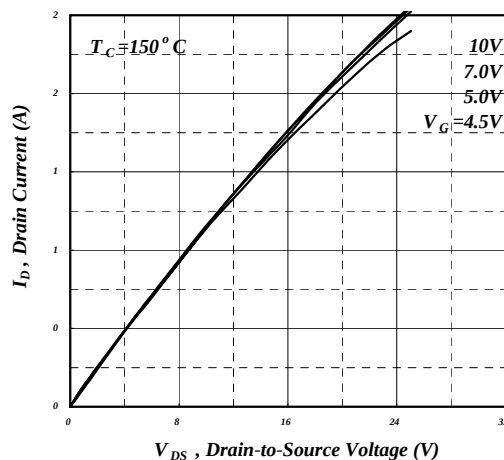


Fig 2. Typical Output Characteristics

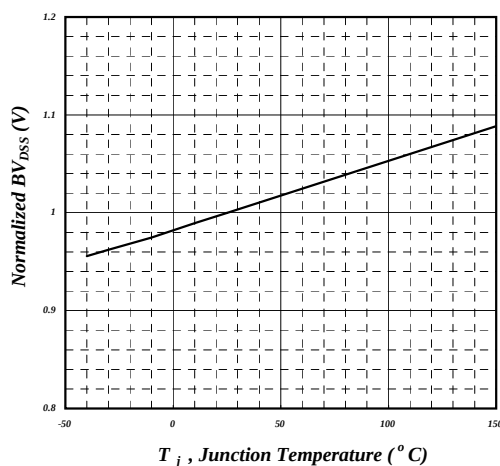


Fig 3. Normalized BV_{DSS} v.s. Junction Temperature

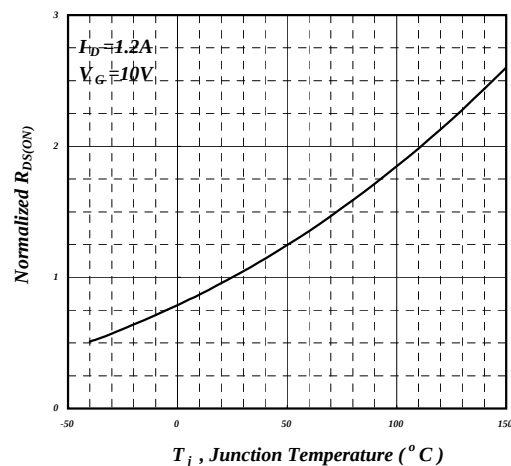


Fig 4. Normalized On-Resistance v.s. Junction Temperature

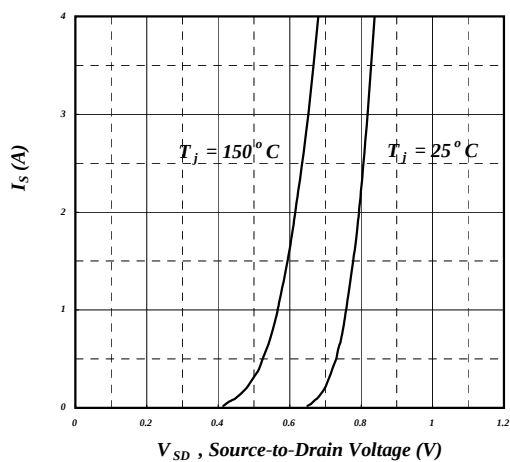


Fig 5. Forward Characteristic of Reverse Diode

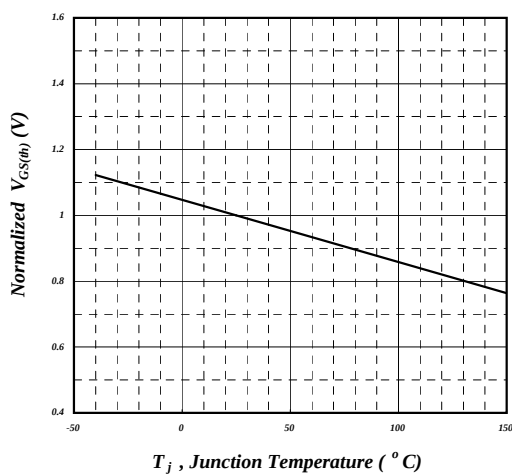


Fig 6. Gate Threshold Voltage v.s. Junction Temperature

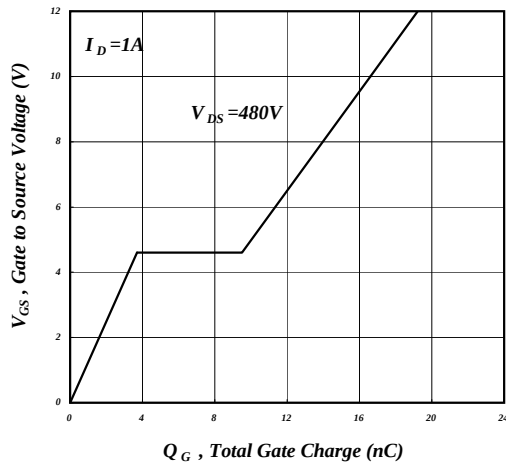


Fig 7. Gate Charge Characteristics

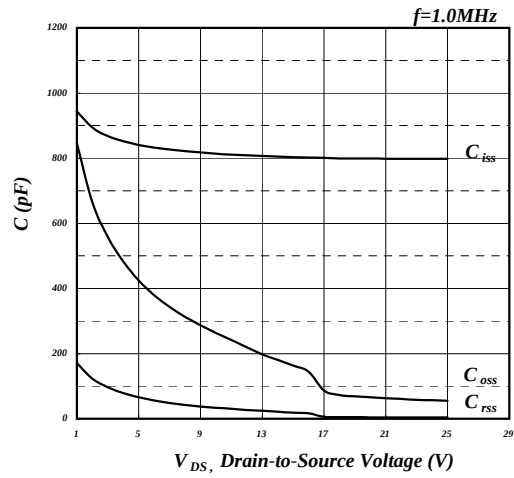


Fig 8. Typical Capacitance Characteristics

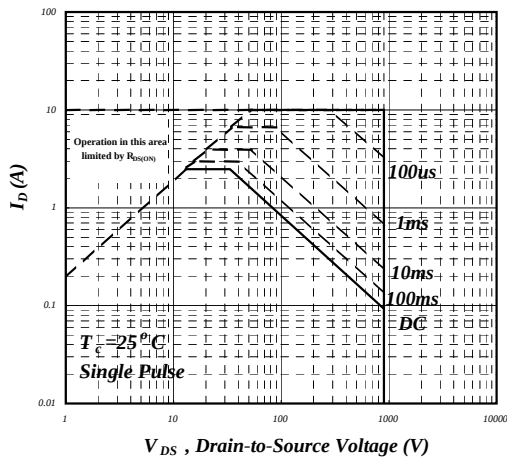


Fig 9. Maximum Safe Operating Area

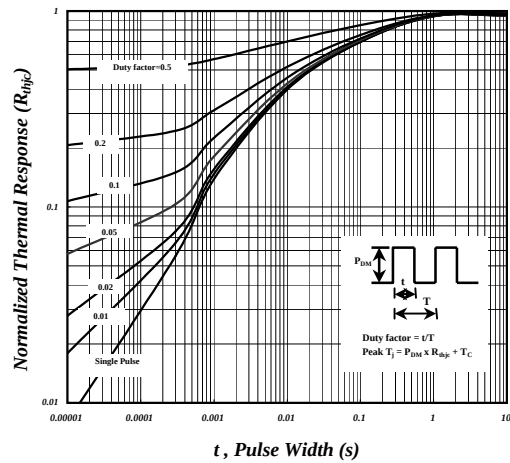


Fig 10. Effective Transient Thermal Impedance

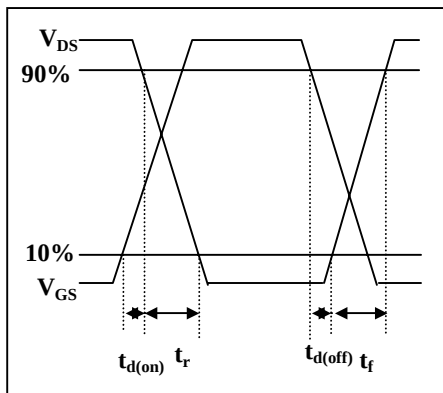


Fig 11. Switching Time Waveform

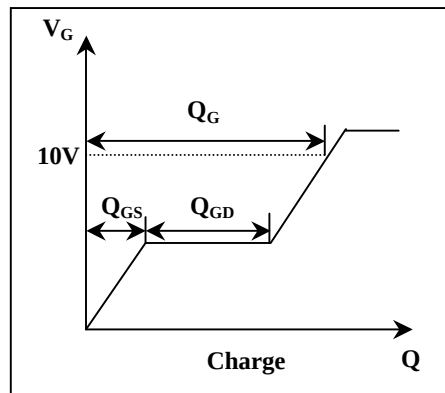


Fig 12. Gate Charge Waveform