



Advanced Power
Electronics Corp.

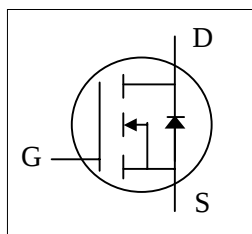
AP10N60W

RoHS-compliant Product

N-CHANNEL ENHANCEMENT MODE

POWER MOSFET

- ▼ 100% Avalanche Test
- ▼ Fast Switching Characteristic
- ▼ Simple Drive Requirement

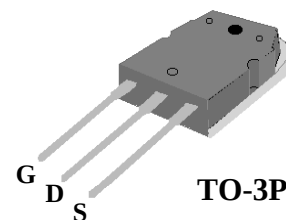


BV_{DSS}	600V
$R_{DS(ON)}$	0.75 Ω
I_D	10A

Description

AP10N60 series are specially designed as main switching devices for universal 90~265VAC off-line AC/DC converter applications. The TO-3P type provide high blocking voltage to overcome voltage surge and sag in the toughest power system with the best combination of fast switching, ruggedized design and cost-effectiveness.

The TO-3P package is widely preferred for commercial-industrial applications. The device is suited for switch mode power supplies, DC-AC converters and high current high speed switching circuits.



Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	600	V
V_{GS}	Gate-Source Voltage	± 30	V
$I_D@T_C=25^{\circ}C$	Continuous Drain Current, V_{GS} @ 10V	10	A
$I_D@T_C=100^{\circ}C$	Continuous Drain Current, V_{GS} @ 10V	5.8	A
I_{DM}	Pulsed Drain Current ¹	36	A
$P_D@T_C=25^{\circ}C$	Total Power Dissipation	156	W
E_{AS}	Single Pulse Avalanche Energy ²	50	mJ
I_{AR}	Avalanche Current	10	A
T_{STG}	Storage Temperature Range	-55 to 150	$^{\circ}C$
T_J	Operating Junction Temperature Range	-55 to 150	$^{\circ}C$

Thermal Data

Symbol	Parameter	Value	Unit
R_{thj-c}	Maximum Thermal Resistance, Junction-case	0.8	$^{\circ}C/W$
R_{thj-a}	Maximum Thermal Resistance, Junction-ambient	40	$^{\circ}C/W$

Electrical Characteristics@ $T_J=25^{\circ}\text{C}$ (unless otherwise specified)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D=1mA$	600	-	-	V
$R_{DS(ON)}$	Static Drain-Source On-Resistance ³	$V_{GS}=10V, I_D=4A$	-	-	0.75	Ω
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS}=V_{GS}, I_D=250\mu A$	2	-	4	V
g_{fs}	Forward Transconductance	$V_{DS}=10V, I_D=4A$	-	3.8	-	S
I_{DSS}	Drain-Source Leakage Current	$V_{DS}=480V, V_{GS}=0V$	-	-	25	μA
	Drain-Source Leakage Current ($T_J=150^{\circ}\text{C}$)	$V_{DS}=480V, V_{GS}=0V$	-	-	250	μA
I_{GSS}	Gate-Source Leakage	$V_{GS}=\pm 30V$	-	-	± 100	nA
Q_g	Total Gate Charge ³	$I_D=8A$	-	45	72	nC
Q_{gs}	Gate-Source Charge	$V_{DS}=480V$	-	11	-	nC
Q_{gd}	Gate-Drain ("Miller") Charge	$V_{GS}=10V$	-	15	-	nC
$t_{d(on)}$	Turn-on Delay Time ³	$V_{DD}=300V$	-	16	-	ns
t_r	Rise Time	$I_D=4A$	-	9	-	ns
$t_{d(off)}$	Turn-off Delay Time	$R_G=10\Omega, V_{GS}=4.7V$	-	48	-	ns
t_f	Fall Time	$R_D=75\Omega$	-	11	-	ns
C_{iss}	Input Capacitance	$V_{GS}=0V$	-	2380	3800	pF
C_{oss}	Output Capacitance	$V_{DS}=15V$	-	475	-	pF
C_{rss}	Reverse Transfer Capacitance	$f=1.0MHz$	-	4.3	-	pF

Source-Drain Diode

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
V_{SD}	Forward On Voltage ³	$T_J=25^{\circ}\text{C}, I_S=8A, V_{GS}=0V$	-	-	1.5	V
t_{rr}	Reverse Recovery Time ³	$I_S=8A, V_{GS}=0V,$	-	550	-	ns
Q_{rr}	Reverse Recovery Charge	$di/dt=100A/\mu s$	-	9.2	-	μC

Notes:

- 1.Pulse width limited by Max. junction temperature.
- 2.Starting $T_J=25^{\circ}\text{C}$, $V_{DD}=50V$, $L=1.0mH$, $R_G=25\Omega$, $I_{AS}=10A$.
- 3.Pulse test

THIS PRODUCT IS AN ELECTROSTATIC SENSITIVE, PLEASE HANDLE WITH CAUTION.

THIS PRODUCT HAS BEEN QUALIFIED FOR CONSUMER MARKET. APPLICATIONS OR USES AS CRITERIAL COMPONENT IN LIFE SUPPORT DEVICE OR SYSTEM ARE NOT AUTHORIZED.

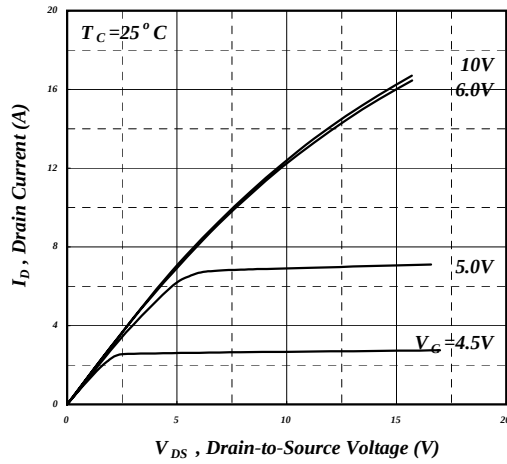


Fig 1. Typical Output Characteristics

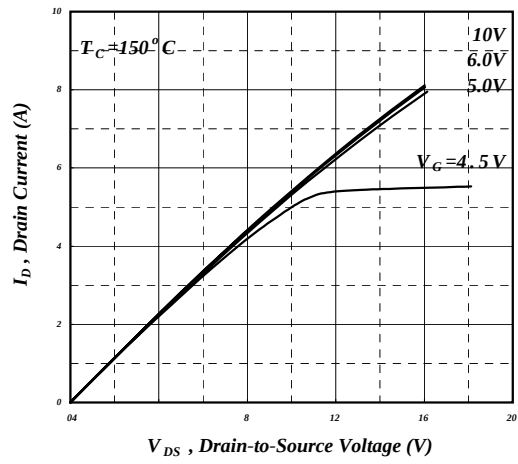


Fig 2. Typical Output Characteristics

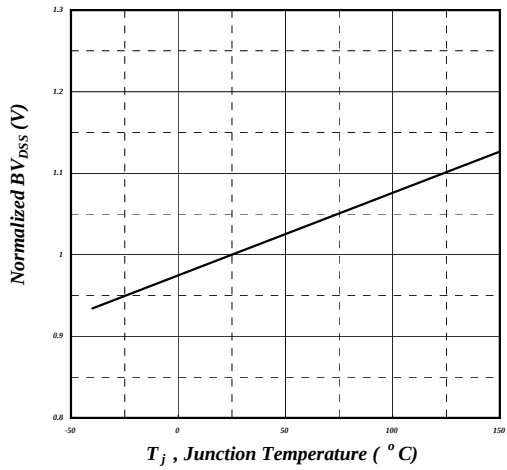


Fig 3. Normalized BV_{DSS} v.s. Junction Temperature

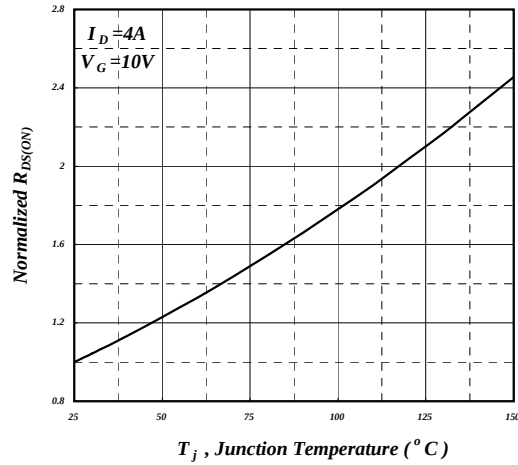


Fig 4. Normalized On-Resistance v.s. Junction Temperature

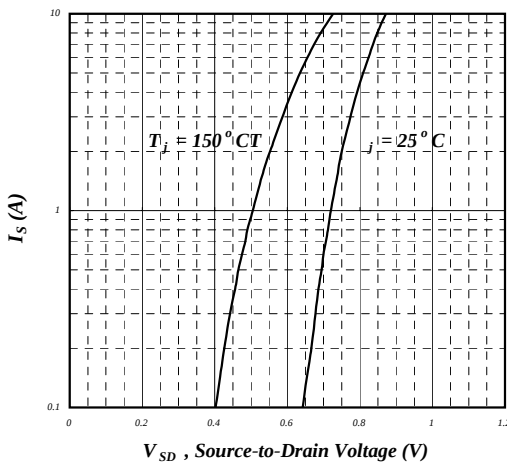


Fig 5. Forward Characteristic of Reverse Diode

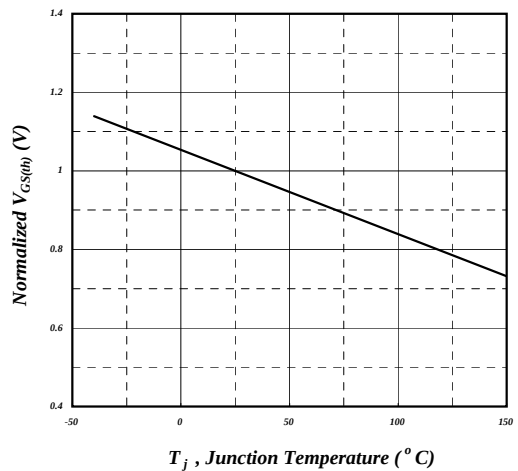


Fig 6. Gate Threshold Voltage v.s. Junction Temperature

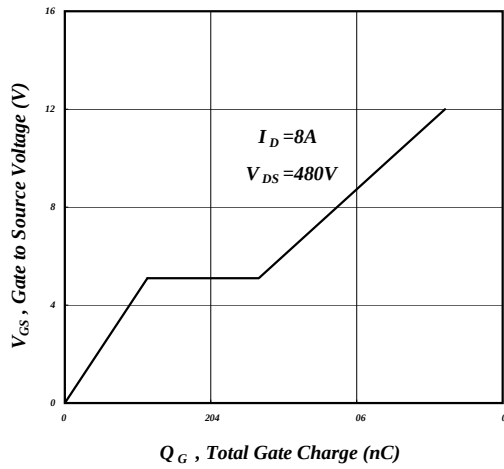


Fig 7. Gate Charge Characteristics

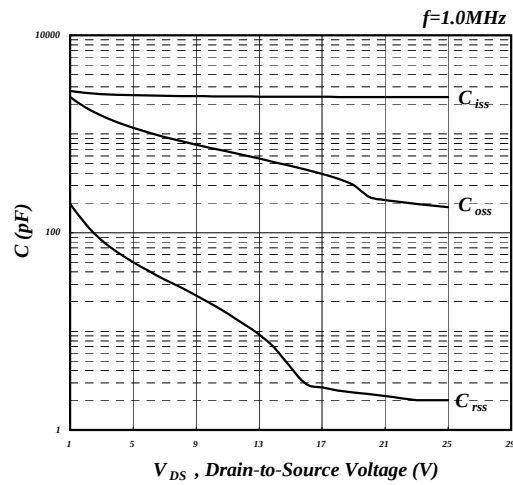


Fig 8. Typical Capacitance Characteristics

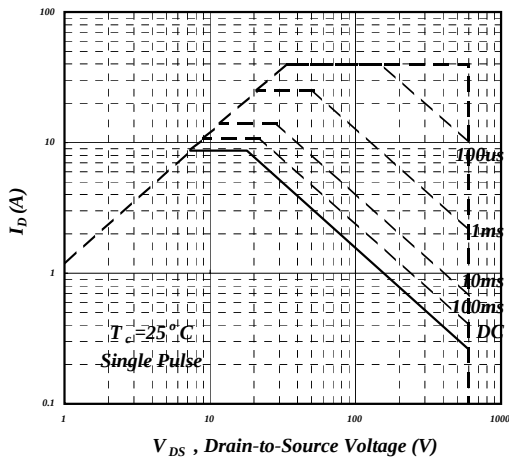


Fig 9. Maximum Safe Operating Area

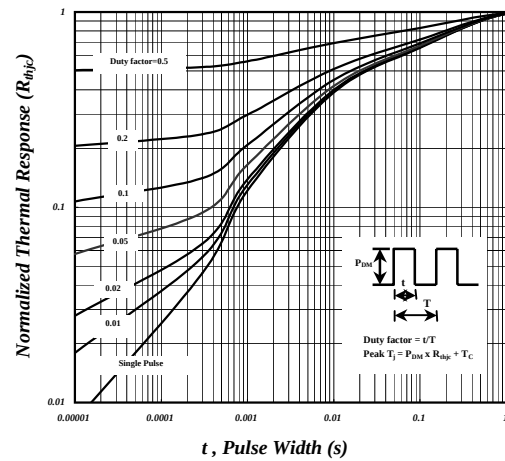


Fig 10. Effective Transient Thermal Impedance

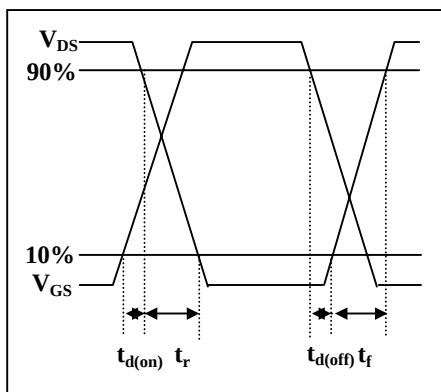


Fig 11. Switching Time Waveform

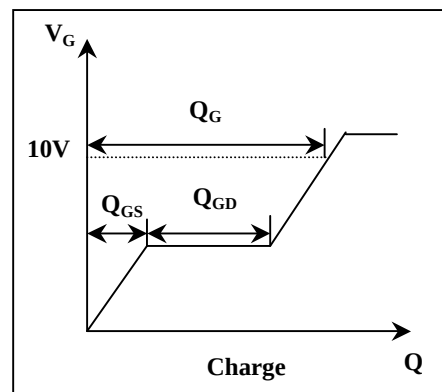
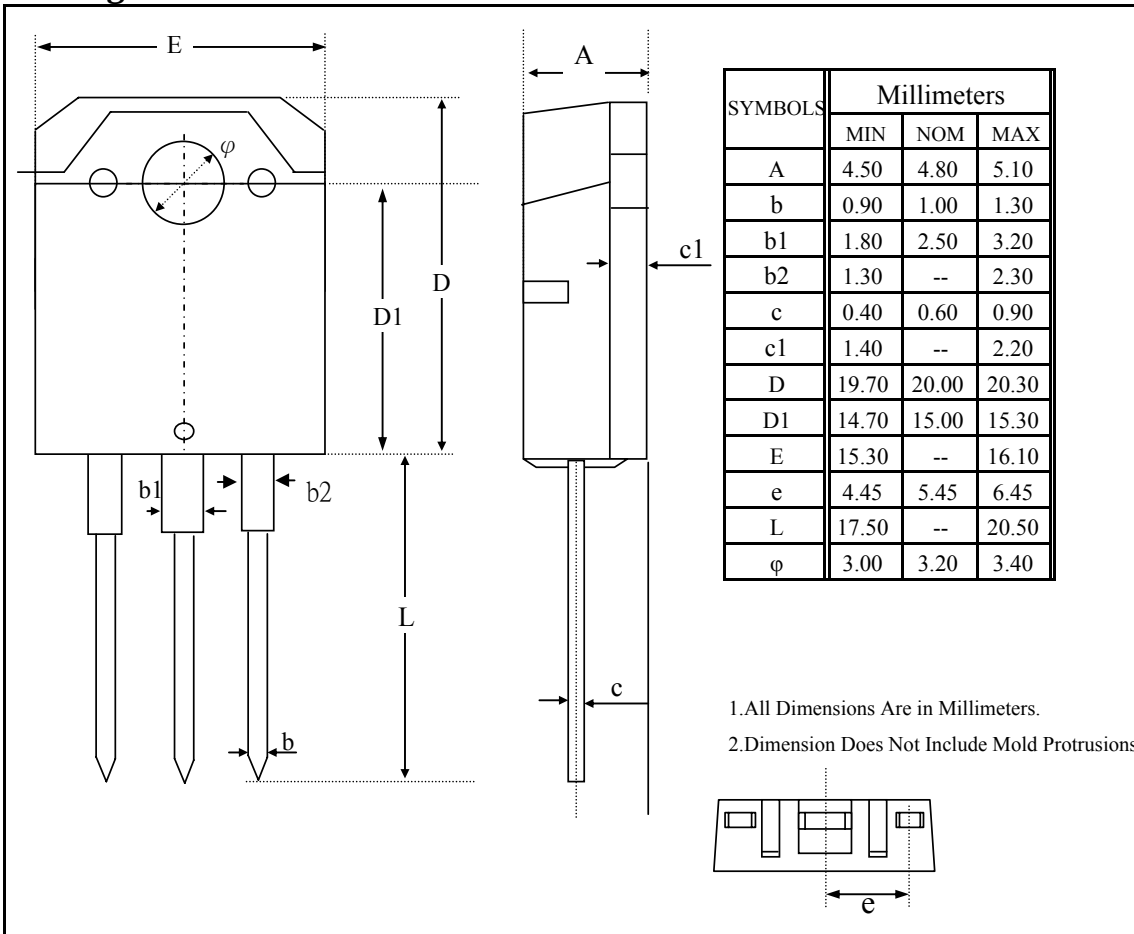


Fig 12. Gate Charge Waveform



Package Outline : TO-3P



Part Marking Information & Packing : TO-3P

