

20V N-Channel Enhancement Mode MOSFET

Description

The AP20N02S uses advanced trench technology to provide excellent $R_{DS(ON)}$, low gate charge and operation with gate voltages as low as 2.5V. This device is suitable for use as a Battery protection or in other Switching application.

General Features

$V_{DS}=20V$ $I_D=20A$

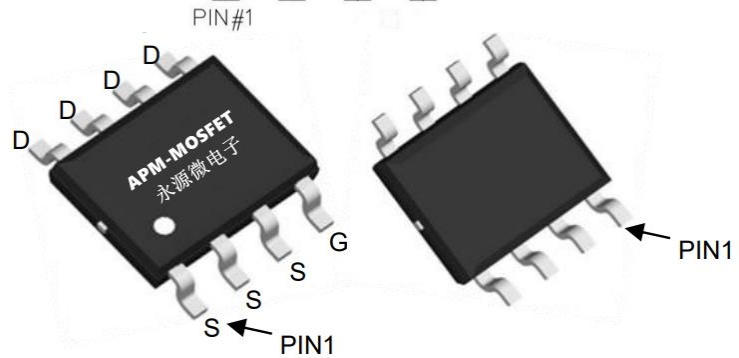
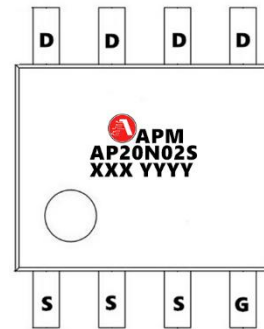
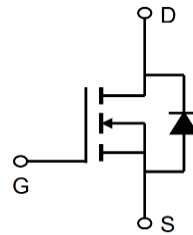
$R_{DS(ON)} < 7.5m\Omega$ @ $V_{GS}=4.5V$ (Type: **5.1m Ω**)

Application

Battery protection

Load switch

Uninterruptible power supply



Package Marking and Ordering Information

Product ID	Pack	Marking	Qty(PCS)
AP20N02S	SOP-8L	AP20N02S XXX YYYY	3000

Absolute Maximum Ratings (TC=25°C unless otherwise noted)

Symbol	Parameter	Max.	Units
VDSS	Drain-Source Voltage	20	V
VGSS	Gate-Source Voltage	±12	V
ID@TA=25°C	Continuous Drain Current, VGS @ 4.5V	20	A
ID@TA=70°C	Continuous Drain Current, VGS @ 4.5V	16	A
IDM	Pulsed Drain Current ^{note1}	60	A
EAS	Single Pulsed Avalanche Energy ^{note2}	47.6	mJ
PD@TA=25°C	Power Dissipation	37	W
TJ, TSTG	Operating and Storage Temperature Range	-55 to +175	°C
RθJA	Thermal Resistance Junction-Ambient ¹	85	°C/W
RθJC	Thermal Resistance, Junction to Case	4	°C/W

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Electrical Characteristics (TC=25°C, unless otherwise noted)

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Units
V(BR)DSS	Drain-Source Breakdown Voltage	VGS=0V, ID=250μA	20	24	-	V
IDSS	Zero Gate Voltage Drain Current	VDS=20V, VGS=0V,	-	-	1.0	μA
IGSS	Gate to Body Leakage Current	VDS=0V, VGS=±12V	-	-	±100	nA
VGS(th)	Gate Threshold Voltage	VDS=VGS, ID=250μA	0.5	0.85	1.2	V
RDS(on)	Static Drain-Source on-Resistance note3	VGS=4.5V, ID=30A	-	5.1	7.0	mΩ
		VGS=2.5V, ID=20A	-	7.0	10	
Ciss	Input Capacitance	VDS=10V, VGS=0V, f = 1.0MHz	-	1832	-	pF
Coss	Output Capacitance		-	289	-	pF
Crss	Reverse Transfer Capacitance		-	271	-	pF
Qg	Total Gate Charge	VDS=10V, ID=30A, VGS=4.5V	-	23	-	nC
Qgs	Gate-Source Charge		-	4.5	-	nC
Qgd	Gate-Drain("Miller") Charge		-	7.3	-	nC
td(on)	Turn-on Delay Time	VDS=10V, ID=30A, RGEN=3Ω, VGS =4.5V	-	15	-	ns
tr	Turn-on Rise Time		-	37	-	ns
td(off)	Turn-off Delay Time		-	52	-	ns
tf	Turn-off Fall Time		-	21	-	ns
IS	Maximum Continuous Drain to Source Diode Forward Current		-	-	60	A
ISM	Maximum Pulsed Drain to Source Diode Forward Current		-	-	210	A
VSD	Drain to Source Diode Forward Voltage	VGS = 0V, IS=25A	-	-	1.2	V

Notes:

- 1、 The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper.
- 2、 The data tested by pulsed , pulse width .The EAS data shows Max. rating .
- 3、 The power dissipation is limited by 175°C junction temperature
- 4、 The data is theoretically the same as ID and IDM , in real applications , should be limited by total power dissipation.

Typical Characteristics

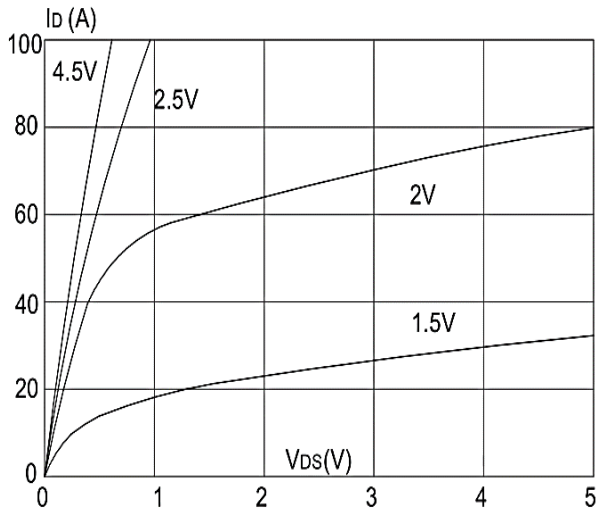


Figure 1: Output Characteristics

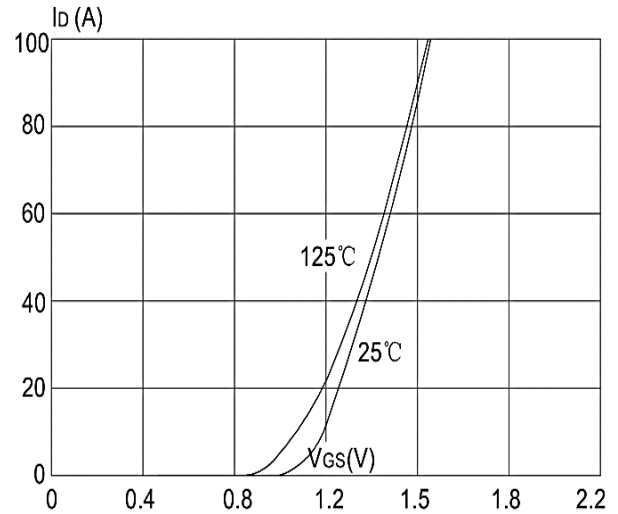


Figure 2: Typical Transfer Characteristics

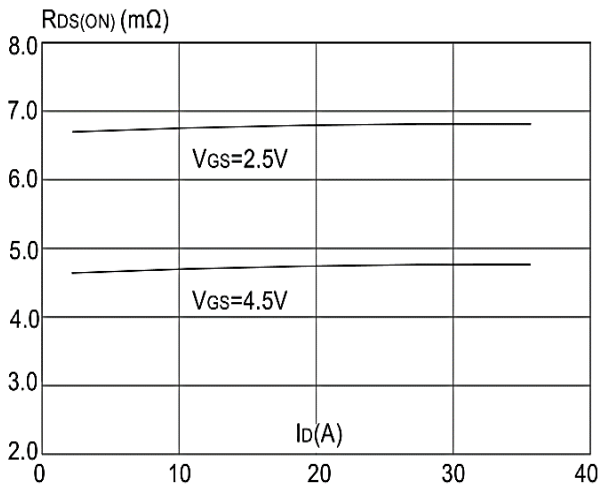


Figure 3: On-resistance vs. Drain Current

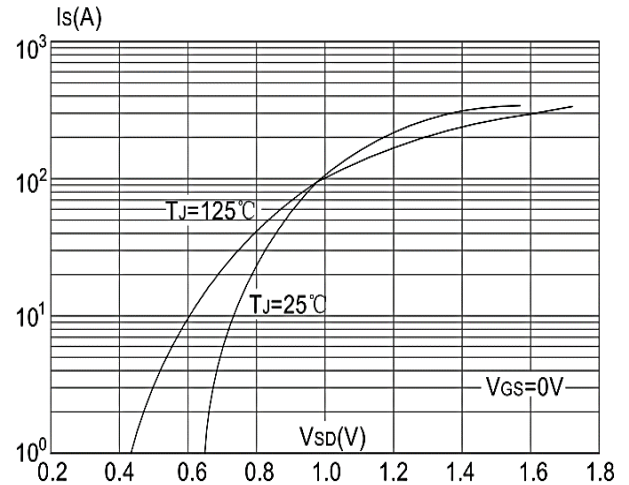


Figure 4: Body Diode Characteristics

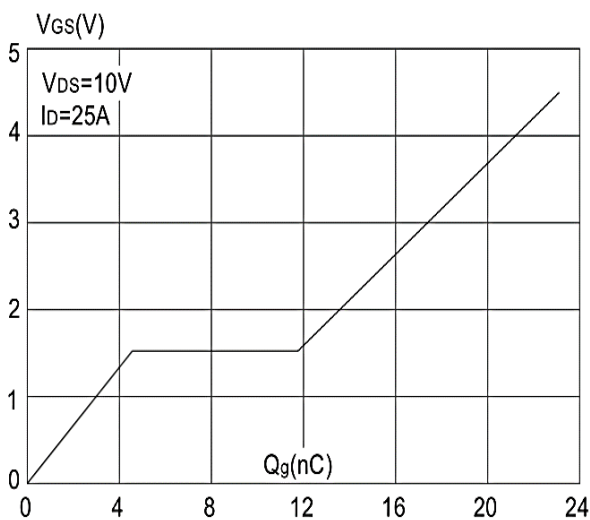


Figure 5: Gate Charge Characteristics

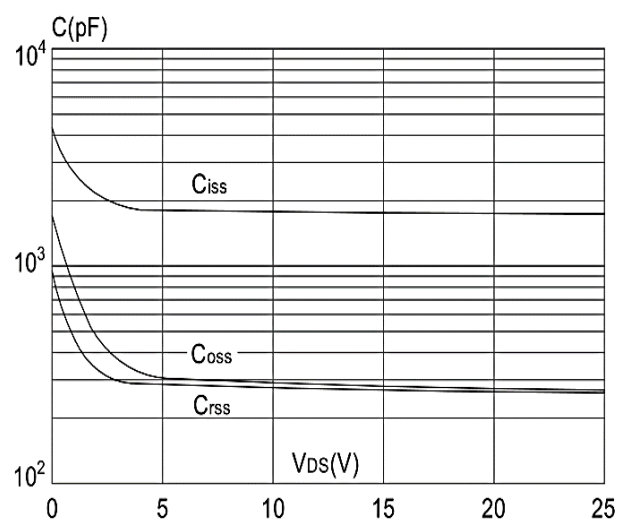


Figure 6: Capacitance Characteristics



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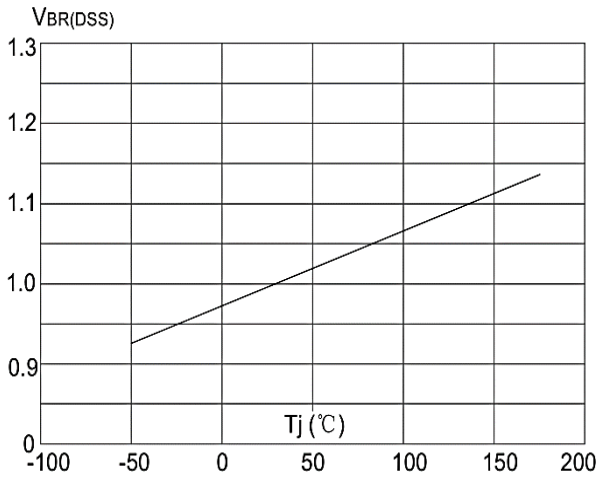


Figure 7: Normalized Breakdown Voltage vs. Junction Temperature

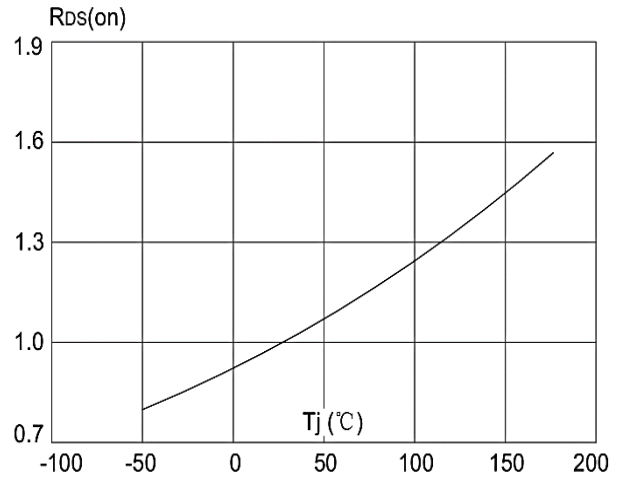


Figure 8: Normalized on Resistance vs. Junction Temperature

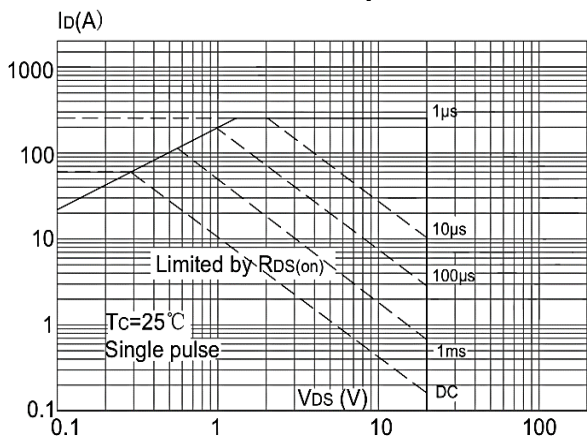


Figure 9: Maximum Safe Operating Area vs. Case Temperature

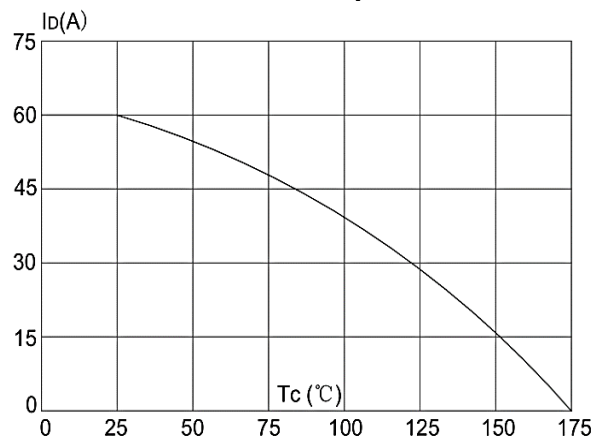


Figure 10: Maximum Continuous Drain Current vs. Case Temperature

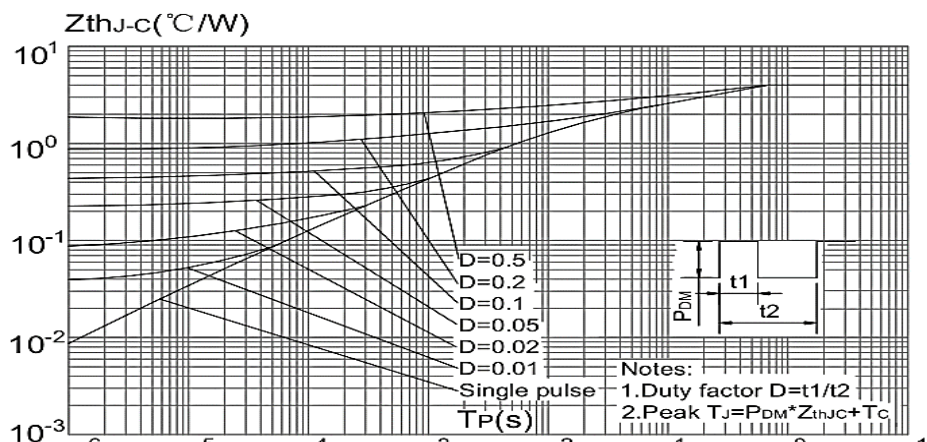
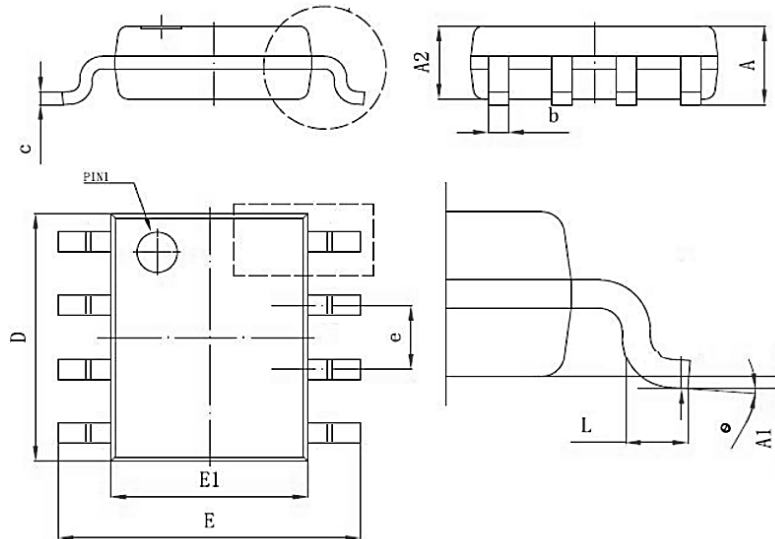


Figure.11: Maximum Effective Transient Thermal Impedance, Junction-to-Case

Package Mechanical Data-SOP-8L-Double



Symbol	Dim in mm		
	Min	Typ	Max
A	1.35	1.55	1.75
A1	0.02	0.15	0.25
A2	1.425	1.45	1.475
b	0.3	0.4	0.5
c	0.15	0.2	0.25
D	4.8	5	5.2
E	5.8	6	6.2
E1	3.8	4	4.2
e	1.27BSC		
L	0.4		1.27
θ	0°		8°

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Edition	Date	Change
REV1.0	2023/4/31	Initial release

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