



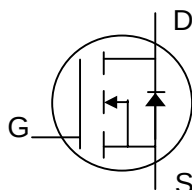
N-channel Enhancement-mode Power MOSFET

Simple Drive Requirement

Small Package Outline

Surface Mount Device

RoHS-compliant, Halogen-free



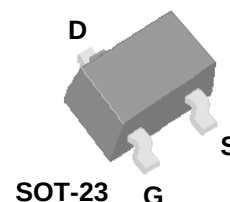
BV_{DSS}	20V
$R_{DS(ON)}$	35m Ω
I_D	5.3A

Description

Advanced Power MOSFETs from APEC provide the designer with the best combination of fast switching, low on-resistance and cost-effectiveness.

The AP2306GN-HF-3 is in the popular SOT-23 small surface-mount package which is widely used in commercial and industrial applications where a small board footprint is required.

This device is well suited for use in medium current applications such as load switches.



Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	20	V
V_{GS}	Gate-Source Voltage	± 12	V
I_D at $T_A = 25^\circ\text{C}$	Continuous Drain Current ³	5.3	A
I_D at $T_A = 70^\circ\text{C}$	Continuous Drain Current ³	4.3	A
I_{DM}	Pulsed Drain Current ¹	10	A
P_D at $T_A = 25^\circ\text{C}$	Total Power Dissipation	1.38	W
T_{STG}	Storage Temperature Range	-55 to 150	$^\circ\text{C}$
T_J	Operating Junction Temperature Range	-55 to 150	$^\circ\text{C}$

Thermal Data

Symbol	Parameter	Value	Unit
Rthj-a	Maximum Thermal Resistance, Junction-ambient	90	$^\circ\text{C/W}$

Ordering Information

AP2306GN-HF-3TR RoHS-compliant, halogen-free SOT-23, shipped on tape and reel, 3000pcs/reel



Electrical Specifications at $T_j=25^{\circ}\text{C}$ (unless otherwise specified)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D=250\mu A$	20	-	-	V
$R_{DS(ON)}$	Static Drain-Source On-Resistance ²	$V_{GS}=10V, I_D=5.5A$	-	-	30	m Ω
		$V_{GS}=4.5V, I_D=5.3A$	-	-	35	m Ω
		$V_{GS}=2.5V, I_D=2.6A$	-	-	50	m Ω
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS}=V_{GS}, I_D=250\mu A$	0.5	-	1.25	V
g_{fs}	Forward Transconductance	$V_{DS}=5V, I_D=5.3A$	-	13	-	S
I_{DSS}	Drain-Source Leakage Current	$V_{DS}=20V, V_{GS}=0V$	-	-	1	μA
	Drain-Source Leakage Current ($T_j=55^{\circ}\text{C}$)	$V_{DS}=16V, V_{GS}=0V$	-	-	10	μA
I_{GSS}	Gate-Source Leakage	$V_{GS}=\pm 12V, V_{DS}=0V$	-	-	± 100	nA
Q_g	Total Gate Charge ²	$I_D=5.3A$	-	8.7	-	nC
Q_{gs}	Gate-Source Charge	$V_{DS}=10V$	-	1.5	-	nC
Q_{gd}	Gate-Drain ("Miller") Charge	$V_{GS}=4.5V$	-	3.6	-	nC
$t_{d(on)}$	Turn-on Delay Time ²	$V_{DS}=15V$	-	6	-	ns
t_r	Rise Time	$I_D=1A$	-	14	-	ns
$t_{d(off)}$	Turn-off Delay Time	$R_G=2\Omega, V_{GS}=10V$	-	18.4	-	ns
t_f	Fall Time	$R_D=15\Omega$	-	2.8	-	ns
C_{iss}	Input Capacitance	$V_{GS}=0V$	-	603	-	pF
C_{oss}	Output Capacitance	$V_{DS}=15V$	-	144	-	pF
C_{rss}	Reverse Transfer Capacitance	$f=1.0\text{MHz}$	-	111	-	pF
R_g	Gate Resistance	$f=1.0\text{MHz}$	-	1.4	2.1	Ω

Source-Drain Diode

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
V_{SD}	Forward On Voltage ²	$I_S=1.2A, V_{GS}=0V$	-	-	1.2	V
t_{rr}	Reverse Recovery Time ²	$I_S=5A, V_{GS}=0V,$	-	16.8	-	ns
Q_{rr}	Reverse Recovery Charge	$di/dt=100A/\mu s$	-	11	-	nC

Notes:

1. Pulse width limited by maximum junction temperature.
2. Pulse test - pulse width $\leq 300\mu s$, duty cycle $\leq 2\%$
3. Surface mounted on 1 in² copper pad of FR4 board, $t \leq 10\text{sec}$; 270°C/W when mounted on minimum copper pad.

THIS PRODUCT IS SENSITIVE TO ELECTROSTATIC DISCHARGE, PLEASE HANDLE WITH CAUTION.

USE OF THIS PRODUCT AS A CRITICAL COMPONENT IN LIFE SUPPORT OR OTHER SIMILAR SYSTEMS IS NOT AUTHORIZED.

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Typical Electrical Characteristics

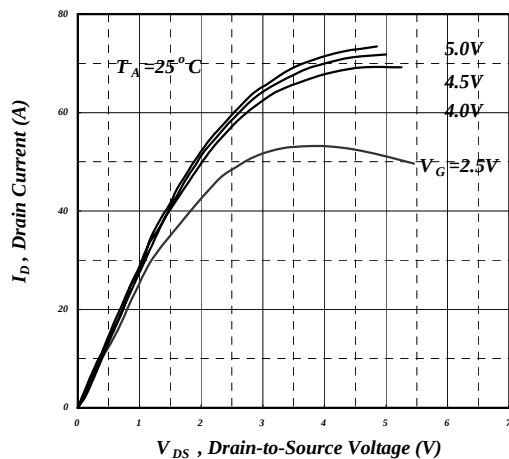


Fig 1. Typical Output Characteristics

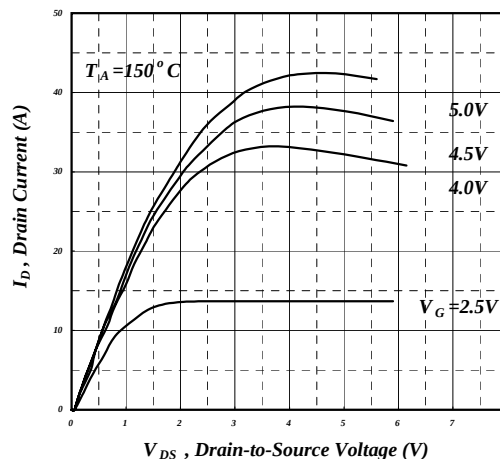


Fig 2. Typical Output Characteristics

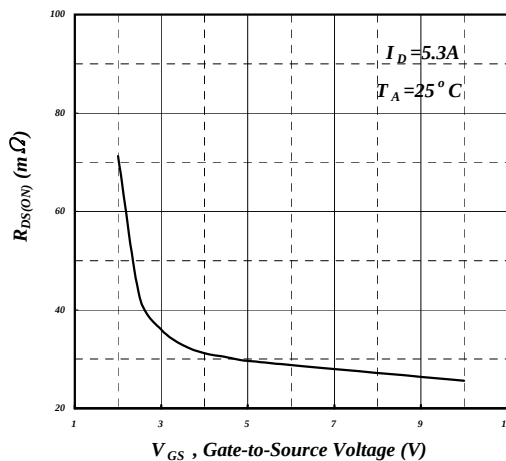


Fig 3. On-Resistance vs.
Gate Voltage

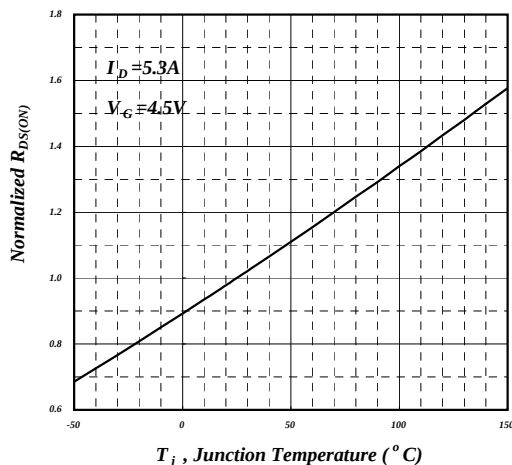


Fig 4. Normalized On-Resistance
vs. Junction Temperature

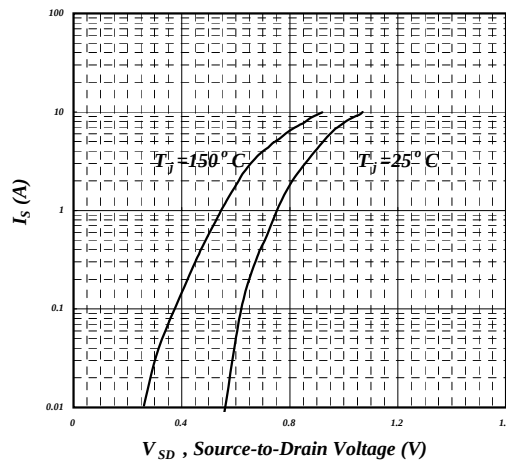


Fig 5. Forward Characteristic of
Reverse Diode

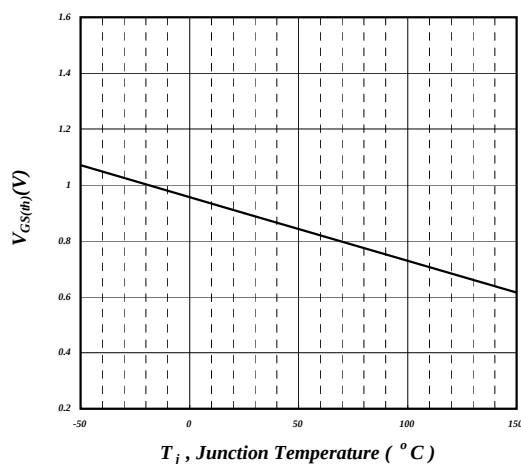


Fig 6. Gate Threshold Voltage vs.
Junction Temperature



Typical Electrical Characteristics (cont.)

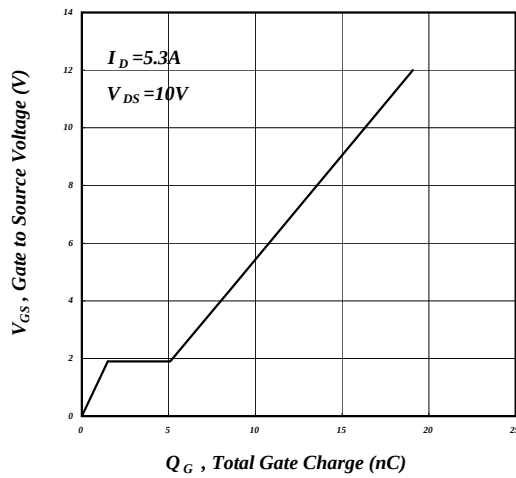


Fig 7. Gate Charge Characteristics

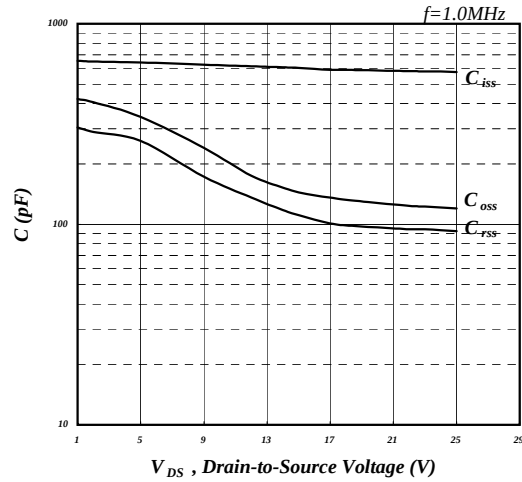


Fig 8. Typical Capacitance Characteristics

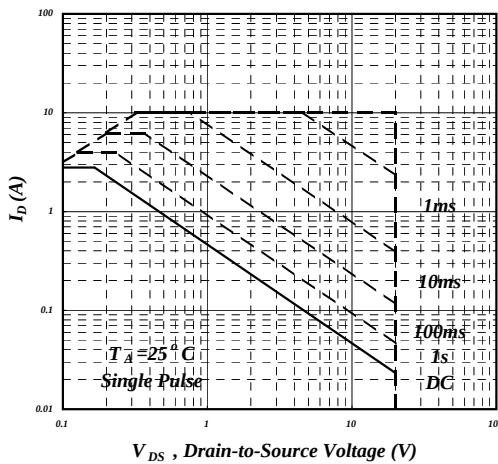


Fig 9. Maximum Safe Operating Area

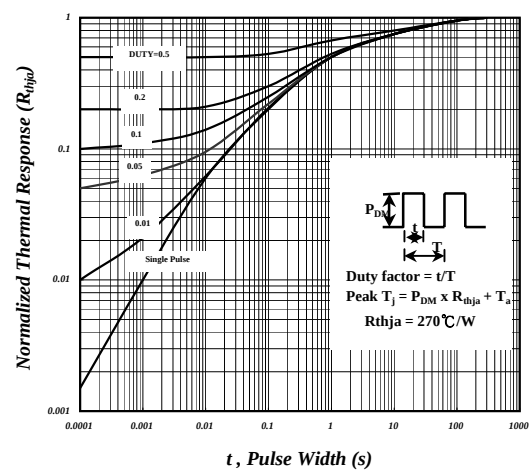


Fig 10. Effective Transient Thermal Impedance

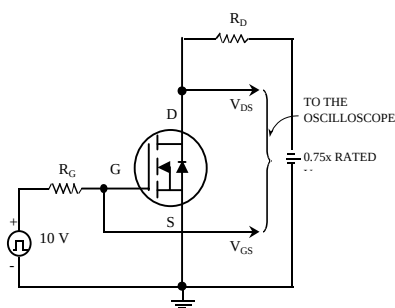


Fig 11. Switching Time Test Circuit

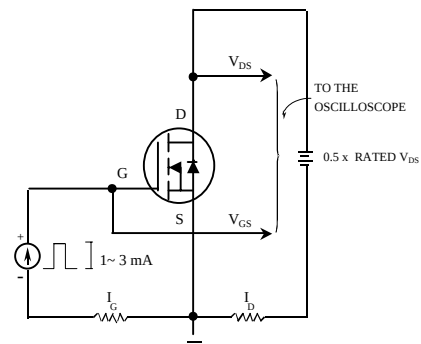
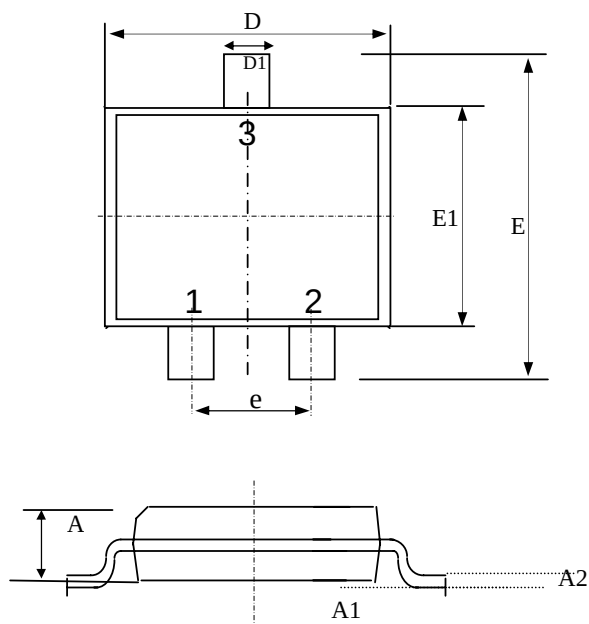


Fig 12. Gate Charge Test Circuit



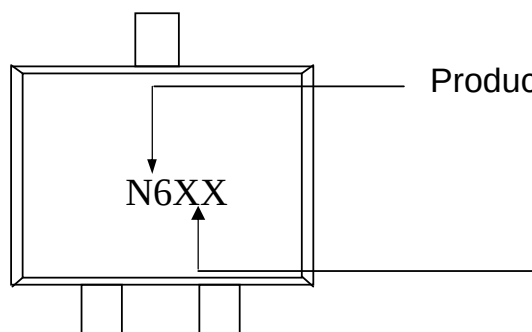
Package Dimensions: SOT-23



SYMBOLS	Millimeters		
	MIN	NOM	MAX
A	1.00	1.15	1.30
A1	0.00	--	0.10
A2	0.10	0.15	0.25
D1	0.30	0.40	0.50
e	1.70	2.00	2.30
D	2.70	2.90	3.10
E	2.40	2.65	3.00
E1	1.40	1.50	1.60

1. All dimensions are in millimeters.
2. Dimensions do not include mold protrusions.

Marking Information: SOT-23



Date/lot code

For details of how to convert this
to standard YYWW date code format,
please contact us directly.