



**Advanced Power
Electronics Corp.**

AP2607GY

RoHS-compliant Product

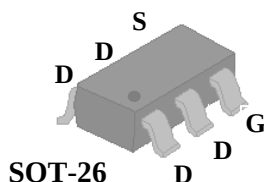
P-CHANNEL ENHANCEMENT MODE

POWER MOSFET

▼ Simple Drive Requirement

▼ Small Package Outline

▼ Surface Mount Device



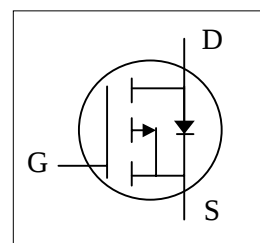
SOT-26

BV_{DSS}	-20V
$R_{DS(ON)}$	52m Ω
I_D	-5A

Description

Advanced Power MOSFETs utilized advanced processing techniques to achieve the lowest possible on-resistance, extremely efficient and cost-effectiveness device.

The SOT-26 package is widely used for all commercial-industrial applications.



Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	-20	V
V_{GS}	Gate-Source Voltage	± 8	V
$I_D@T_A=25^\circ\text{C}$	Continuous Drain Current ³	-5	A
$I_D@T_A=70^\circ\text{C}$	Continuous Drain Current ³	-4	A
I_{DM}	Pulsed Drain Current ¹	-20	A
$P_D@T_A=25^\circ\text{C}$	Total Power Dissipation	2	W
T_{STG}	Storage Temperature Range	-55 to 150	$^\circ\text{C}$
T_J	Operating Junction Temperature Range	-55 to 150	$^\circ\text{C}$

Thermal Data

Symbol	Parameter	Value	Unit
R_{thj-a}	Maximum Thermal Resistance, Junction-ambient ³	62.5	$^\circ\text{C}/\text{W}$



AP2607GY

Electrical Characteristics@T_j=25°C(unless otherwise specified)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =-250uA	-20	-	-	V
R _{DS(ON)}	Static Drain-Source On-Resistance ²	V _{GS} =-4.5V, I _D =-5A	-	-	52	mΩ
		V _{GS} =-2.5V, I _D =-4A	-	-	65	mΩ
		V _{GS} =-1.8V, I _D =-1A	-	-	90	mΩ
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _D =-250uA	-0.3	-	-1	V
g _{fs}	Forward Transconductance	V _{DS} =-5V, I _D =-5A	-	9	-	S
I _{DSS}	Drain-Source Leakage Current	V _{DS} =-16V, V _{GS} =0V	-	-	-1	uA
	Drain-Source Leakage Current (T _j =55°C)	V _{DS} =-16V, V _{GS} =0V	-	-	-10	uA
I _{GSS}	Gate-Source Leakage	V _{GS} = ± 8V	-	-	±100	nA
Q _g	Total Gate Charge ²	I _D =-5A	-	13	21	nC
Q _{gs}	Gate-Source Charge	V _{DS} =-10V	-	2.1	-	nC
Q _{gd}	Gate-Drain ("Miller") Charge	V _{GS} =-4.5V	-	5	-	nC
t _{d(on)}	Turn-on Delay Time ²	V _{DS} =-10V	-	10	-	ns
t _r	Rise Time	I _D =-1A	-	18	-	ns
t _{d(off)}	Turn-off Delay Time	R _G =3.3Ω, V _{GS} =-5V	-	23	-	ns
t _f	Fall Time	R _D =10Ω	-	31	-	ns
C _{iss}	Input Capacitance	V _{GS} =0V	-	1030	1650	pF
C _{oss}	Output Capacitance	V _{DS} =-20V	-	120	-	pF
C _{rss}	Reverse Transfer Capacitance	f=1.0MHz	-	105	-	pF

Source-Drain Diode

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
V _{SD}	Forward On Voltage ²	I _S =-1.2A, V _{GS} =0V	-	-	-1.2	V
t _{rr}	Reverse Recovery Time ²	I _S =-5A, V _{GS} =0V,	-	29	-	ns
Q _{rr}	Reverse Recovery Charge	dI/dt=100A/μs	-	20	-	nC

Notes:

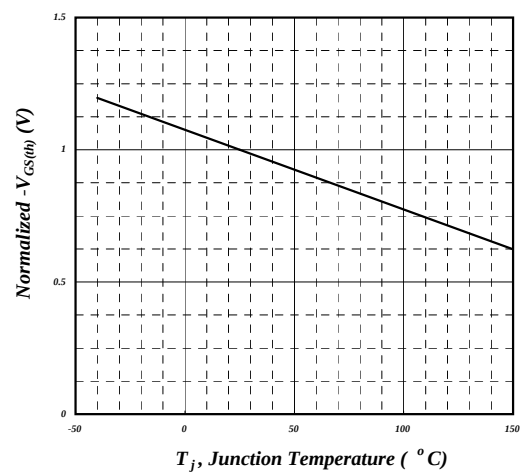
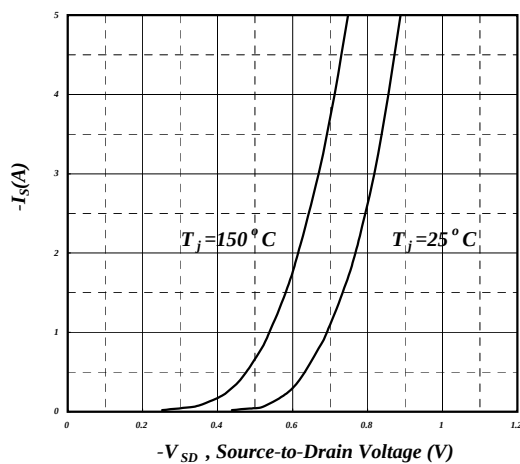
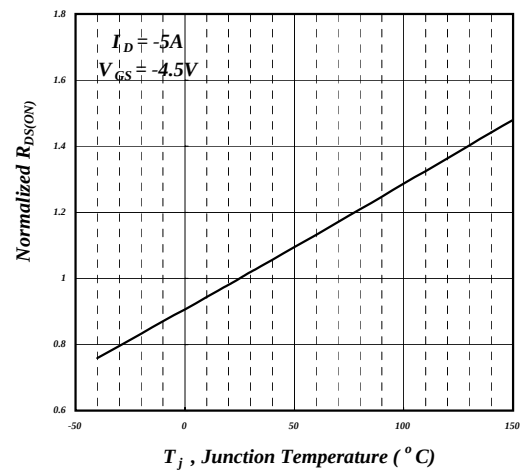
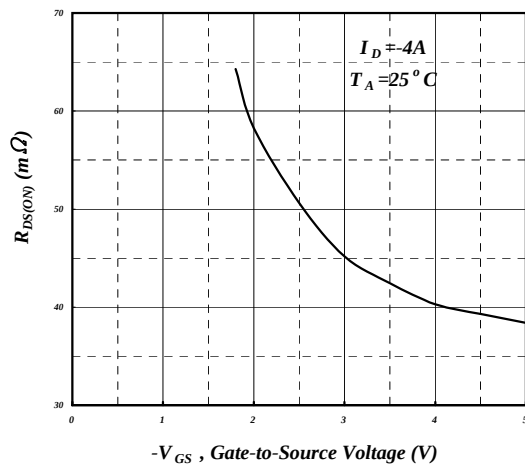
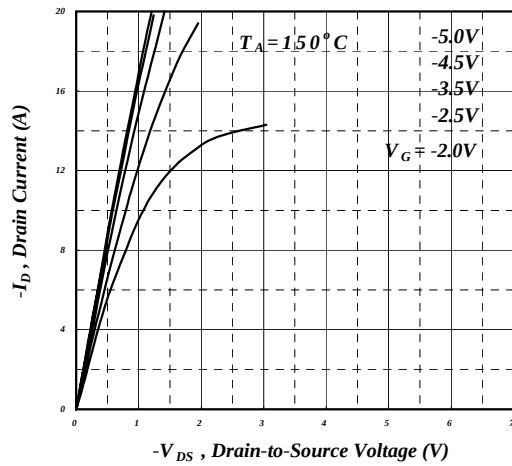
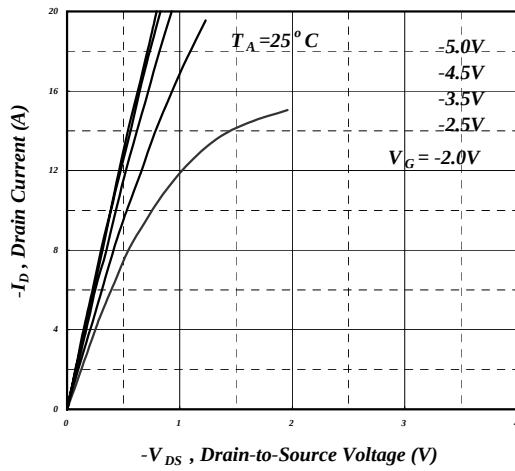
- 1.Pulse width limited by Max. junction temperature.
- 2.Pulse test
- 3.Surface mounted on 1 in² copper pad of FR4 board ; 156°C /W when mounted on min. copper pad.

THIS PRODUCT IS SENSITIVE TO ELECTROSTATIC DISCHARGE, PLEASE HANDLE WITH CAUTION.

USE OF THIS PRODUCT AS A CRITICAL COMPONENT IN LIFE SUPPORT OR OTHER SIMILAR SYSTEMS IS NOT AUTHORIZED.

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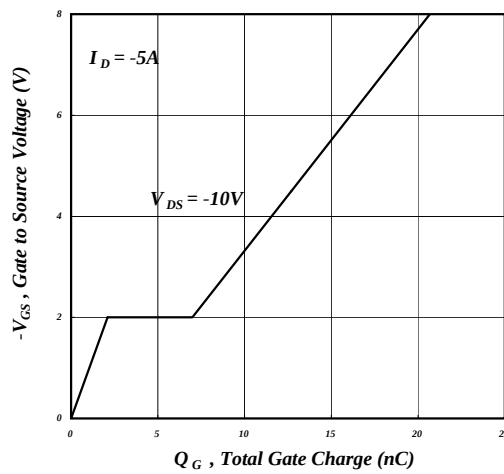


Fig 7. Gate Charge Characteristics

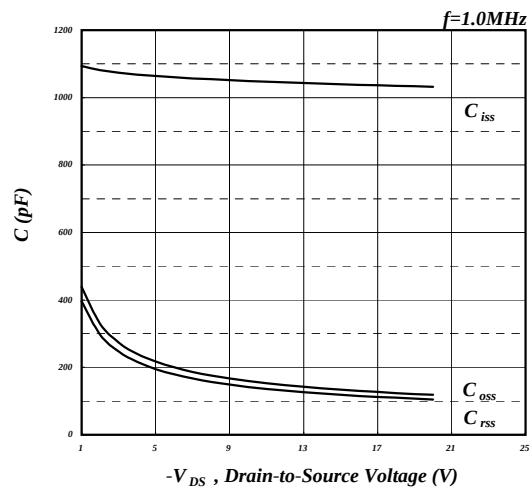


Fig 8. Typical Capacitance Characteristics

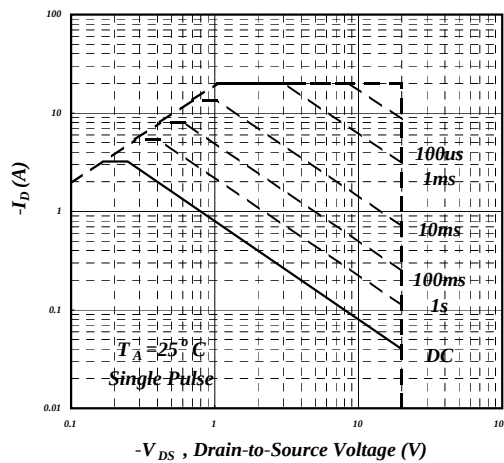


Fig 9. Maximum Safe Operating Area

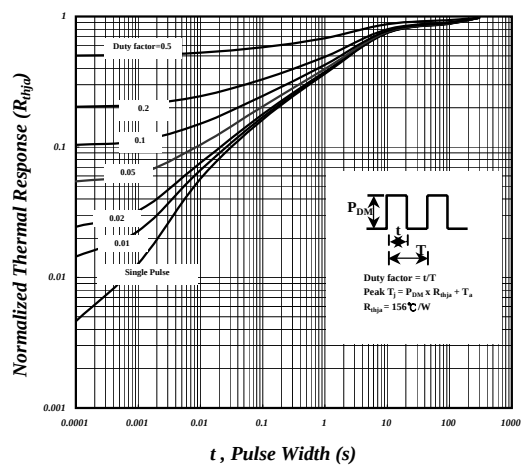


Fig 10. Effective Transient Thermal Impedance

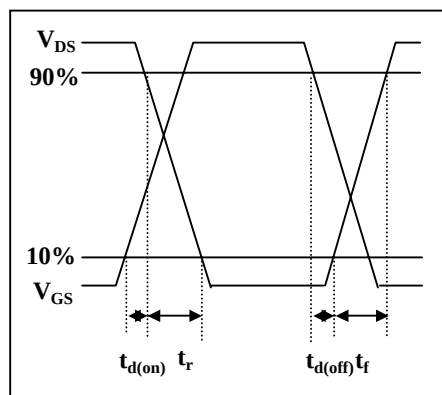


Fig 11. Switching Time Waveform

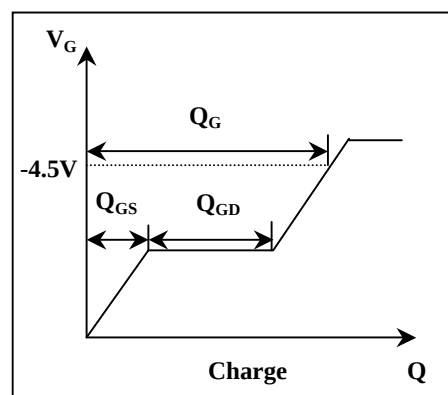
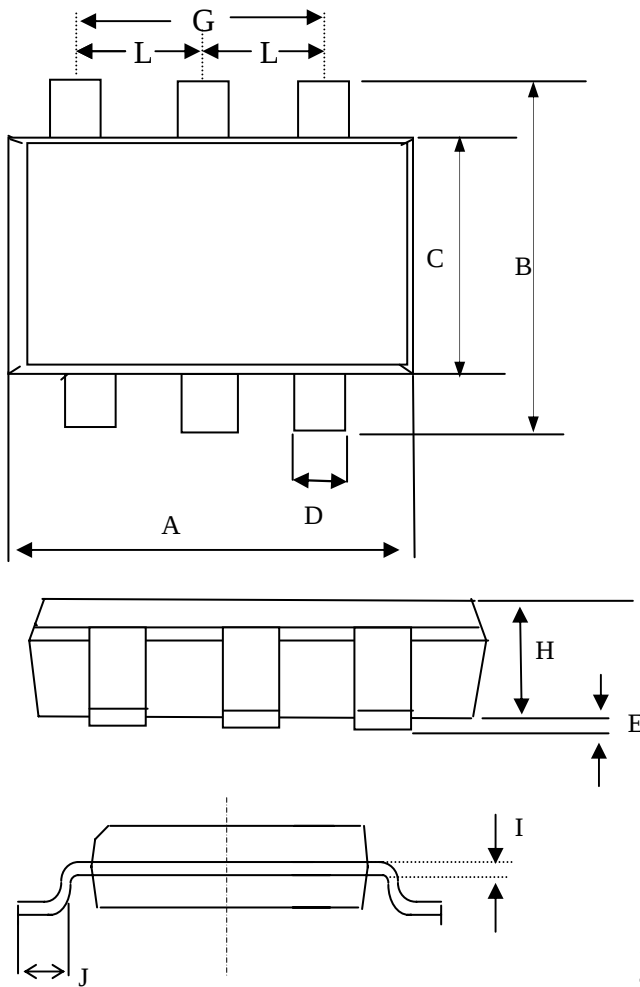


Fig 12. Gate Charge Waveform



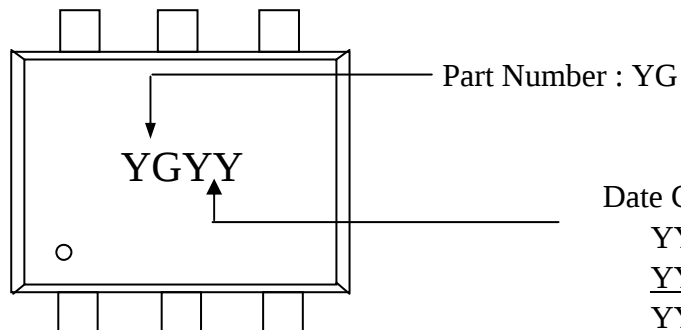
Package Outline : SOT-26



SYMBOLS	Millimeters		
	MIN	NOM	MAX
A	2.70	2.90	3.10
B	2.60	2.80	3.00
C	1.40	1.60	1.80
D	0.30	0.43	0.55
E	0.00	0.05	0.10
H	1.20REF		
G	1.90REF		
I	0.12REF		
J	0.37REF		
L	0.95REF		

- 1.All Dimension Are In Millimeters.
- 2.Dimension Does Not Include Mold Protrusions.

Part Marking Information & Packing : SOT-26



Date Code

YY:2004,2008,2012...

YY:2003,2007,2011...

YY:2002,2006,2010...

YY:2001,2005,2009...