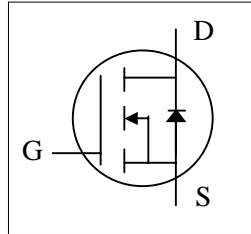




- ▼ Lower On-resistance
- ▼ Fast Switching Characteristic
- ▼ Simple Drive Requirement
- ▼ RoHS Compliant

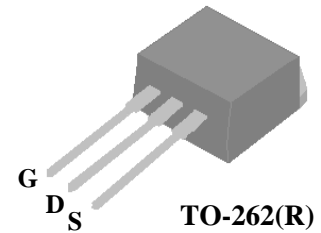


$BV_{DSS}$	650V
$R_{DS(ON)}$	1 $\Omega$
$I_D$	10A

### Description

AP2761 series are from Advanced Power innovated design and silicon process technology to achieve the lowest possible on-resistance and fast switching performance. It provides the designer with an extreme efficient device for use in a wide range of power applications.

The TO-262 package is widely preferred for commercial-industrial through-hole applications and suited for low voltage applications such as DC/DC converters.



### Absolute Maximum Ratings @ $T_J=25^\circ\text{C}$ (unless otherwise specified)

Symbol	Parameter	Rating	Units
$V_{DS}$	Drain-Source Voltage	650	V
$V_{GS}$	Gate-Source Voltage	$\pm 30$	V
$I_D @ T_C=25^\circ\text{C}$	Drain Current, $V_{GS}$ @ 10V	10	A
$I_D @ T_C=100^\circ\text{C}$	Drain Current, $V_{GS}$ @ 10V	4.4	A
$I_{DM}$	Pulsed Drain Current <sup>1</sup>	18	A
$P_D @ T_C=25^\circ\text{C}$	Total Power Dissipation	104	W
	Linear Derating Factor	0.8	W/ $^\circ\text{C}$
$I_{AR}$	Avalanche Current <sup>2</sup>	10	A
$T_{STG}$	Storage Temperature Range	-55 to 150	$^\circ\text{C}$
$T_J$	Operating Junction Temperature Range	-55 to 150	$^\circ\text{C}$

### Thermal Data

Symbol	Parameter	Value	Units
Rthj-c	Maximum Thermal Resistance, Junction-case	1.2	$^\circ\text{C}/\text{W}$
Rthj-a	Maximum Thermal Resistance, Junction-ambient	62	$^\circ\text{C}/\text{W}$



# AP2761R-A-HF

## Electrical Characteristics @T<sub>j</sub>=25°C(unless otherwise specified)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V, I <sub>D</sub> =1mA	650	-	-	V
ΔBV <sub>DSS</sub> /ΔT <sub>j</sub>	Breakdown Voltage Temperature Coefficient	Reference to 25°C, I <sub>D</sub> =1mA	-	0.6	-	V/°C
R <sub>DS(ON)</sub>	Static Drain-Source On-Resistance <sup>3</sup>	V <sub>GS</sub> =10V, I <sub>D</sub> =3.5A	-	-	1	Ω
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250uA	2	-	4	V
g <sub>fs</sub>	Forward Transconductance	V <sub>DS</sub> =10V, I <sub>D</sub> =3.5A	-	4.5	-	S
I <sub>DSS</sub>	Drain-Source Leakage Current	V <sub>DS</sub> =600V, V <sub>GS</sub> =0V	-	-	10	uA
	Drain-Source Leakage Current (T <sub>j</sub> =125°C)	V <sub>DS</sub> =480V, V <sub>GS</sub> =0V	-	-	100	uA
I <sub>GSS</sub>	Gate-Source Leakage	V <sub>GS</sub> =±30V, V <sub>DS</sub> =0V	-	-	±100	nA
Q <sub>g</sub>	Total Gate Charge <sup>3</sup>	I <sub>D</sub> =10A	-	53	85	nC
Q <sub>gs</sub>	Gate-Source Charge	V <sub>DS</sub> =520V	-	10	-	nC
Q <sub>gd</sub>	Gate-Drain ("Miller") Charge	V <sub>GS</sub> =10V	-	15	-	nC
t <sub>d(on)</sub>	Turn-on Delay Time <sup>3</sup>	V <sub>DD</sub> =320V	-	16	-	ns
t <sub>r</sub>	Rise Time	I <sub>D</sub> =10A	-	20	-	ns
t <sub>d(off)</sub>	Turn-off Delay Time	R <sub>G</sub> =10Ω	-	82	-	ns
t <sub>f</sub>	Fall Time	V <sub>GS</sub> =10V	-	36	-	ns
C <sub>iss</sub>	Input Capacitance	V <sub>GS</sub> =0V	-	2750	4430	pF
C <sub>oss</sub>	Output Capacitance	V <sub>DS</sub> =15V	-	160	-	pF
C <sub>rss</sub>	Reverse Transfer Capacitance	f=1.0MHz	-	6	-	pF

## Source-Drain Diode

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
V <sub>SD</sub>	Forward On Voltage <sup>3</sup>	I <sub>S</sub> =10A, V <sub>GS</sub> =0V	-	-	1.5	V
t <sub>rr</sub>	Reverse Recovery Time <sup>3</sup>	I <sub>S</sub> =10A, V <sub>GS</sub> =0V,	-	610	-	ns
Q <sub>rr</sub>	Reverse Recovery Charge	di/dt=100A/μs	-	8.64	-	μC

### Notes:

- 1.Pulse width limited by Max. junction temperature.
- 2.Starting T<sub>j</sub>=25°C , V<sub>DD</sub>=50V , L=1.2mH , R<sub>G</sub>=25Ω
- 3.Pulse test

THIS PRODUCT IS SENSITIVE TO ELECTROSTATIC DISCHARGE, PLEASE HANDLE WITH CAUTION.

USE OF THIS PRODUCT AS A CRITICAL COMPONENT IN LIFE SUPPORT OR OTHER SIMILAR SYSTEMS IS NOT AUTHORIZED.

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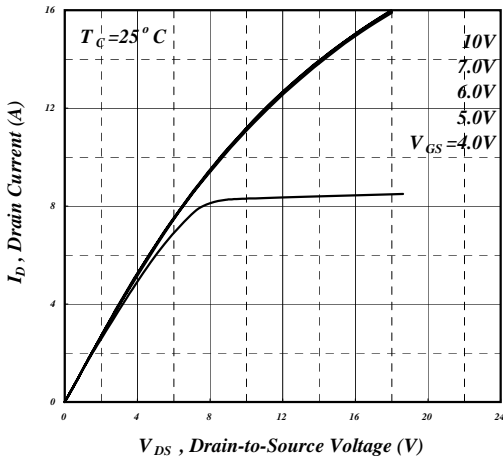


Fig 1. Typical Output Characteristics

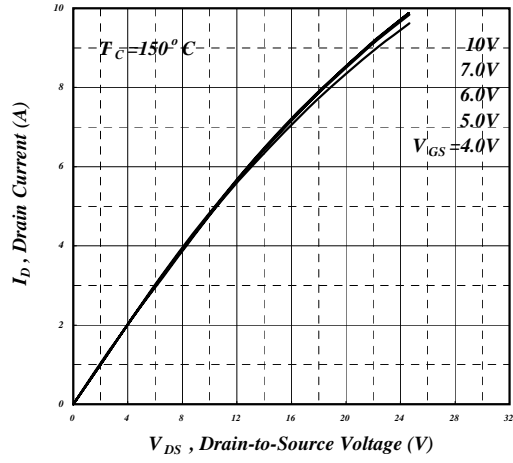


Fig 2. Typical Output Characteristics

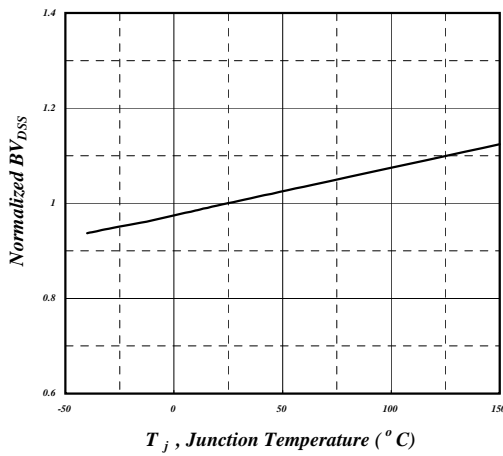


Fig 3. On-Resistance v.s. Gate Voltage

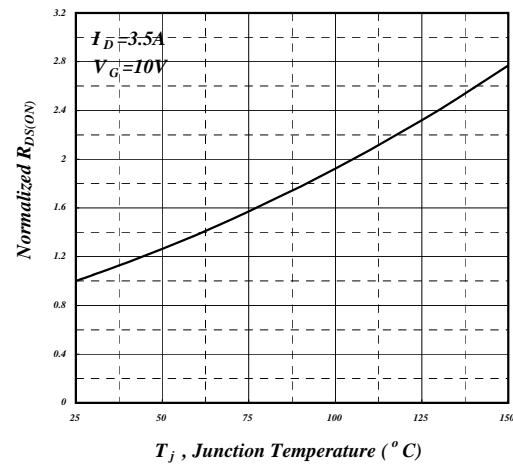


Fig 4. Normalized On-Resistance v.s. Junction Temperature

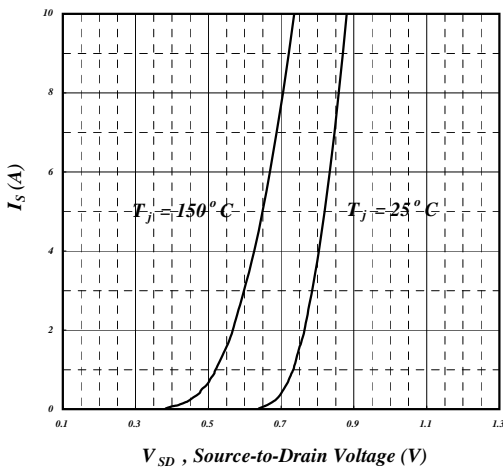


Fig 5. Forward Characteristic of Reverse Diode

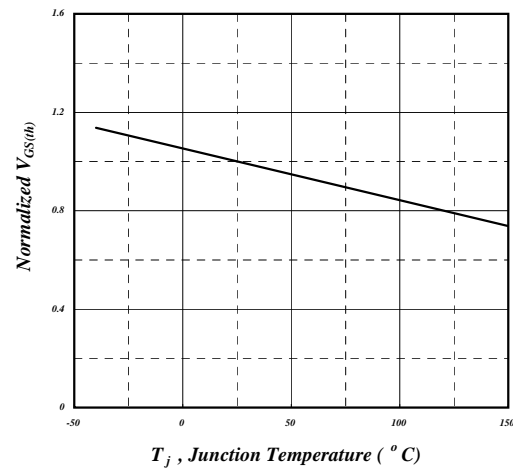


Fig 6. Gate Threshold Voltage v.s. Junction Temperature

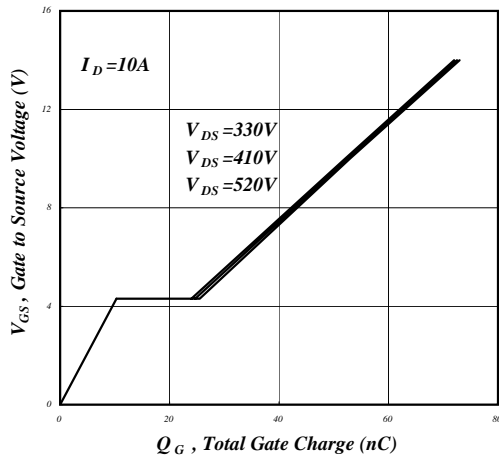


Fig 7. Gate Charge Characteristics

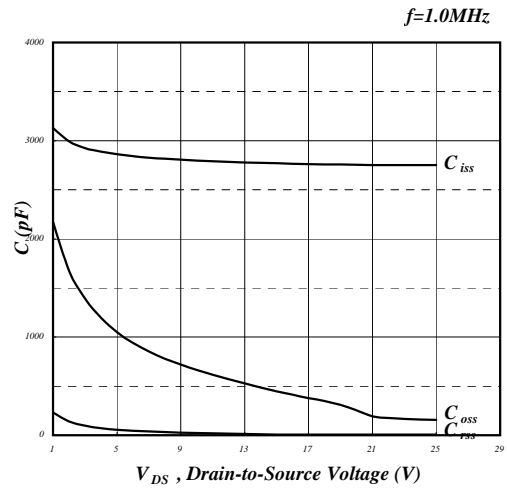


Fig 8. Typical Capacitance Characteristics

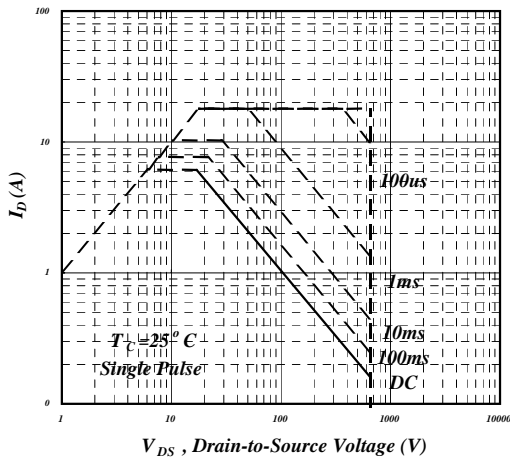


Fig 9. Maximum Safe Operating Area

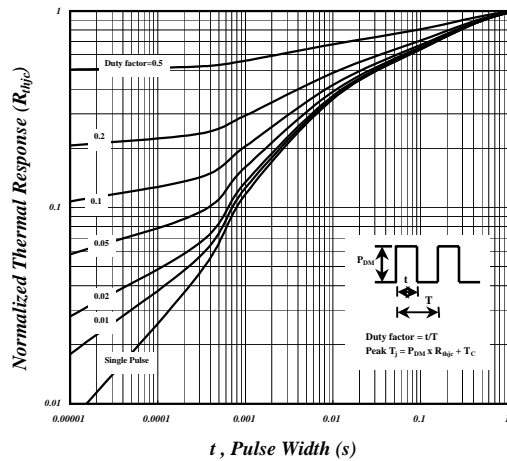


Fig 10. Effective Transient Thermal Impedance

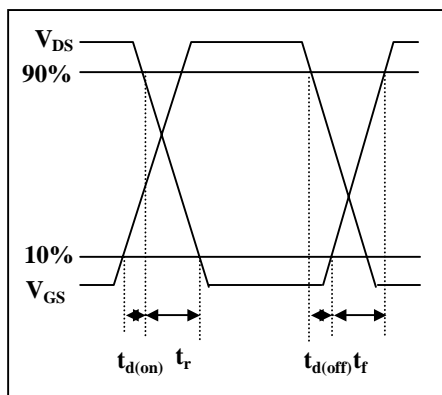


Fig 11. Switching Time Waveform

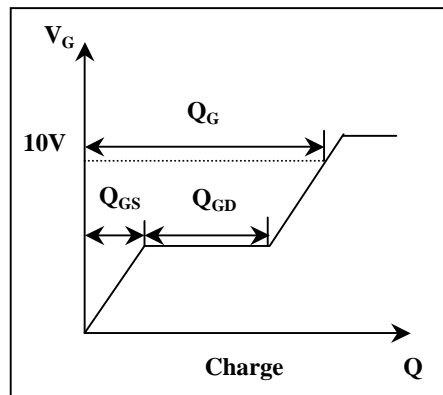


Fig 12. Gate Charge Waveform



## MARKING INFORMATION

