

-100V P-Channel Enhancement Mode MOSFET

Description

The AP40P10P/T uses advanced trench technology to provide excellent $R_{DS(ON)}$, low gate charge and operation with gate voltages as low as 4.5V. This device is suitable for use as a Battery protection or in other Switching application.

General Features

$V_{DS} = -100V$ $I_D = -40A$

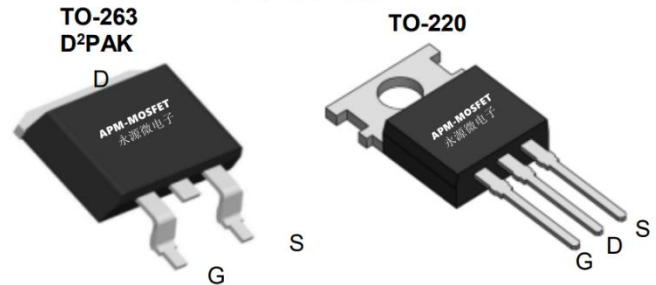
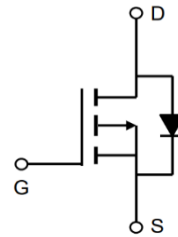
$R_{DS(ON)} < 55m\Omega$ @ $V_{GS}=10V$ (Type: 48m Ω)

Application

Brushless motor

Load switch

Uninterruptible power supply



Package Marking and Ordering Information

Product ID	Pack	Marking	Qty(PCS)
AP40P10P	TO-220-3L	AP40P10P XXX YYYY	1000
AP40P10T	TO-263-3L	AP40P10T XXX YYYY	800

Absolute Maximum Ratings ($T_C=25^\circ C$ unless otherwise noted)

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	-100	V
V_{GS}	Gate-Source Voltage	± 20	V
$I_D @ T_C=25^\circ C$	Continuous Drain Current, $V_{GS} @ -10V^1$	-40	A
$I_D @ T_C=100^\circ C$	Continuous Drain Current, $V_{GS} @ -10V^1$	-29	A
IDM	Pulsed Drain Current ²	-120	A
EAS	Single Pulse Avalanche Energy ³	560	mJ
IAS	Avalanche Current	-29	A
$P_D @ T_C=25^\circ C$	Total Power Dissipation ⁴	104	W
TSTG	Storage Temperature Range	-55 to 150	$^\circ C$
T_J	Operating Junction Temperature Range	-55 to 150	$^\circ C$
$R_{\theta JA}$	Thermal Resistance Junction-Ambient ¹	62.5	$^\circ C/W$
$R_{\theta JC}$	Thermal Resistance Junction-Case ¹	1.22	$^\circ C/W$

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P-Channel Electrical Characteristics (T_J =25 °C, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BVDSS	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =-250uA	-100	-110	---	V
RDS(ON)	Static Drain-Source On-Resistance ²	V _{GS} =-10V, I _D =-10A	---	48	55	mΩ
		V _{GS} =-4.5V, I _D =-8A	---	51	58	
VGS(th)	Gate Threshold Voltage	V _{GS} =V _{DS} , I _D =-250uA	-1.2	-1.8	-2.5	V
IDSS	Drain-Source Leakage Current	V _{DS} =-100V, V _{GS} =0V, T _J =25°C	---	---	-1	uA
IGSS	Gate-Source Leakage Current	V _{GS} =±20V, V _{DS} =0V	---	---	±100	nA
gfs	Forward Transconductance	V _{DS} =-10V, I _D =-10A	---	32	---	S
Q _g	Total Gate Charge	V _{DS} =-80V V _{GS} =-10V I _D =-14A	---	92	---	nC
Q _{gs}	Gate-Source Charge		---	17.5	---	
Q _{gd}	Gate-Drain Charge		---	14	---	
Td(on)	Turn-On Delay Time	V _{DD} =-50V, V _{GS} =-10V ,R _G =3.3Ω, I _D =-14A	---	20.5	---	ns
T _r	Rise Time		---	32.2	---	
Td(off)	Turn-Off Delay Time		---	123	---	
T _f	Fall Time		---	63.7	---	
Ciss	Input Capacitance	V _{DS} =-25V, V _{GS} =0V, f=1MHz	---	6516	---	pF
Coss	Output Capacitance		---	223	---	
Crss	Reverse Transfer Capacitance		---	125	---	
IS	Continuous Source Current ^{1,5}	V _G =V _D =0V, Force Current	---	---	-40	A
VSD	Diode Forward Voltage ²	V _{GS} =0V, I _S =-1A, T _J =25°C	---	---	-1.2	V
trr	Reverse Recovery Time	I _F =-14A, di/dt=-100A/μs, T _J =25°C	---	31.2	---	nS
Q _{rr}	Reverse Recovery Charge		---	31.97	---	nC

Note :

- 1、 The data tested by surface mounted on a 1 inch 2 FR-4 board with 2OZ copper.
- 2、 The data tested by pulsed , pulse width ≅ 300us , duty cycle ≅ 2%
- 3、 The EAS data shows Max. rating . The test condition is V DD =-25V,V GS =-10V,L=0.1mH,IAS =-29A
- 4、 The power dissipation is limited by 150°C junction temperature
- 5、 The data is theoretically the same as I D and I DM , in real applications , should be limited by total power dissipation.

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Typical Characteristics

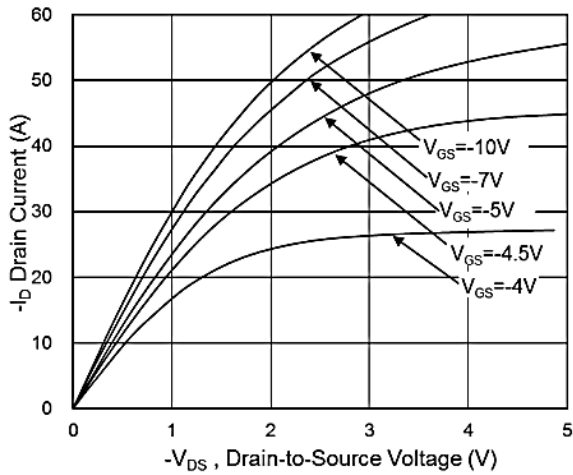


Fig.1 Typical Output Characteristics

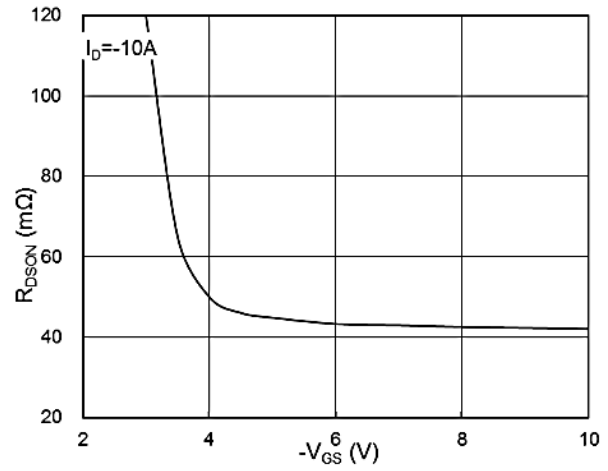


Fig.2 On-Resistance vs G-S Voltage

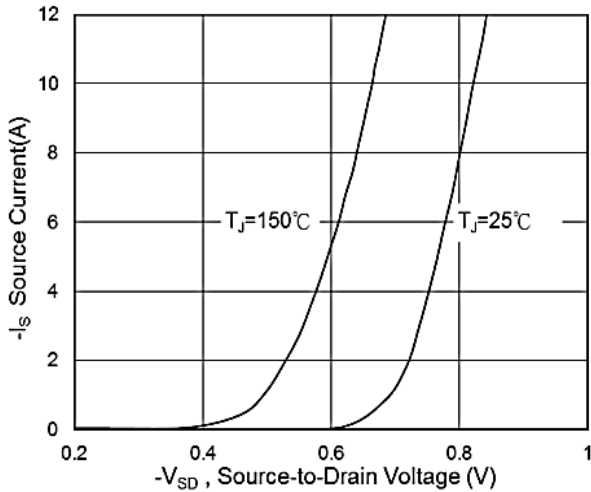


Fig.3 Typical S-D Diode Forward Voltage

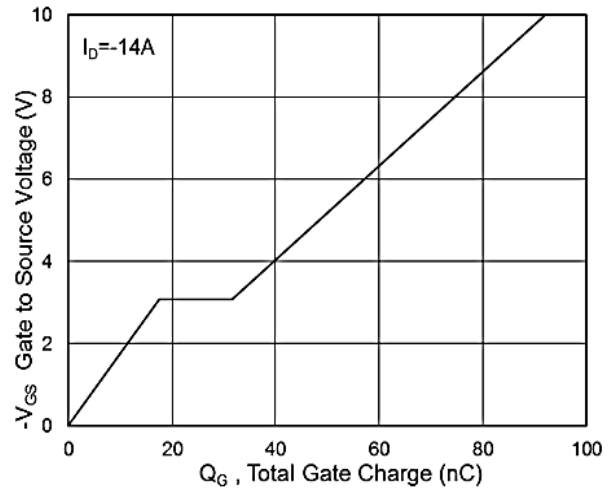


Fig.4 Gate-Charge Characteristics

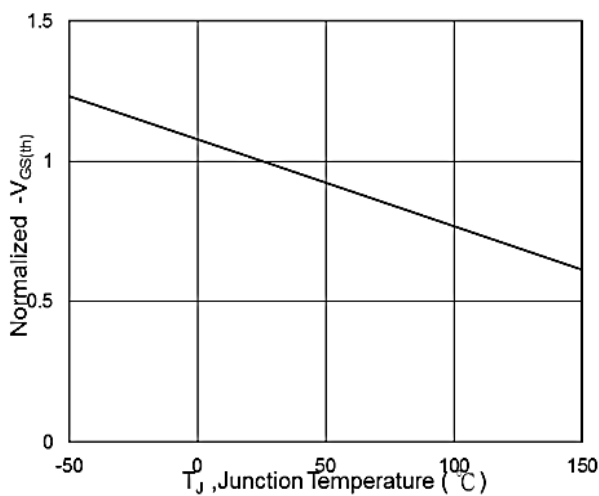


Fig.5 Normalized $V_{GS(th)}$ vs T_J

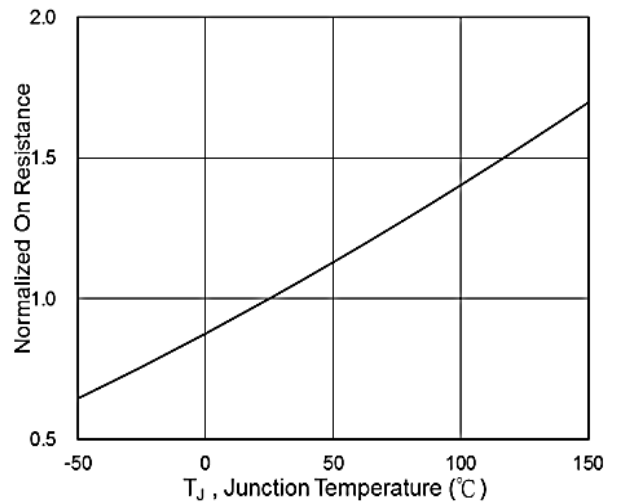


Fig.6 Normalized $R_{DS(on)}$ vs T_J



-100V P-Channel Enhancement Mode MOSFET

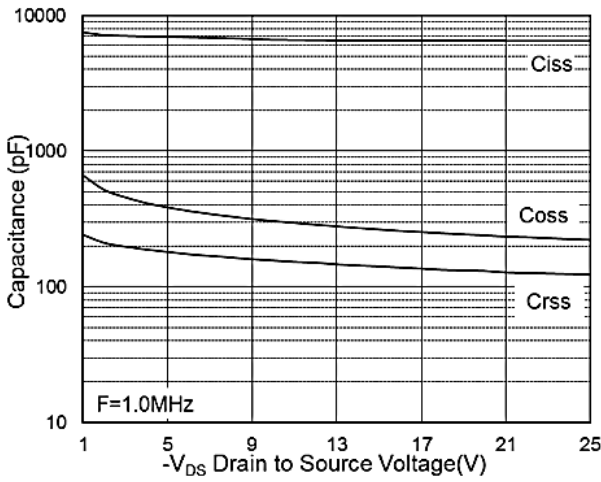


Fig.7 Capacitance

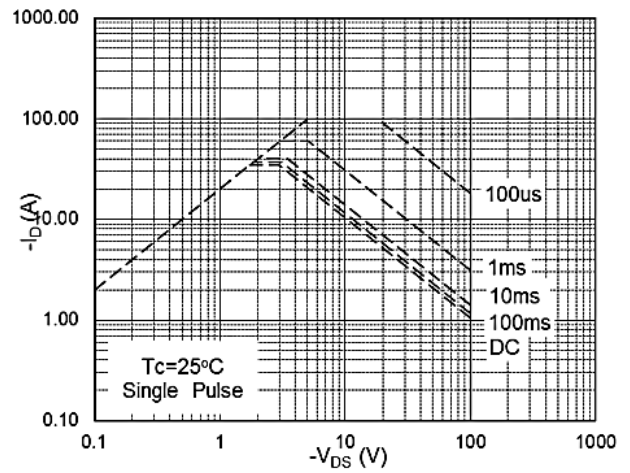


Fig.8 Safe Operating Area

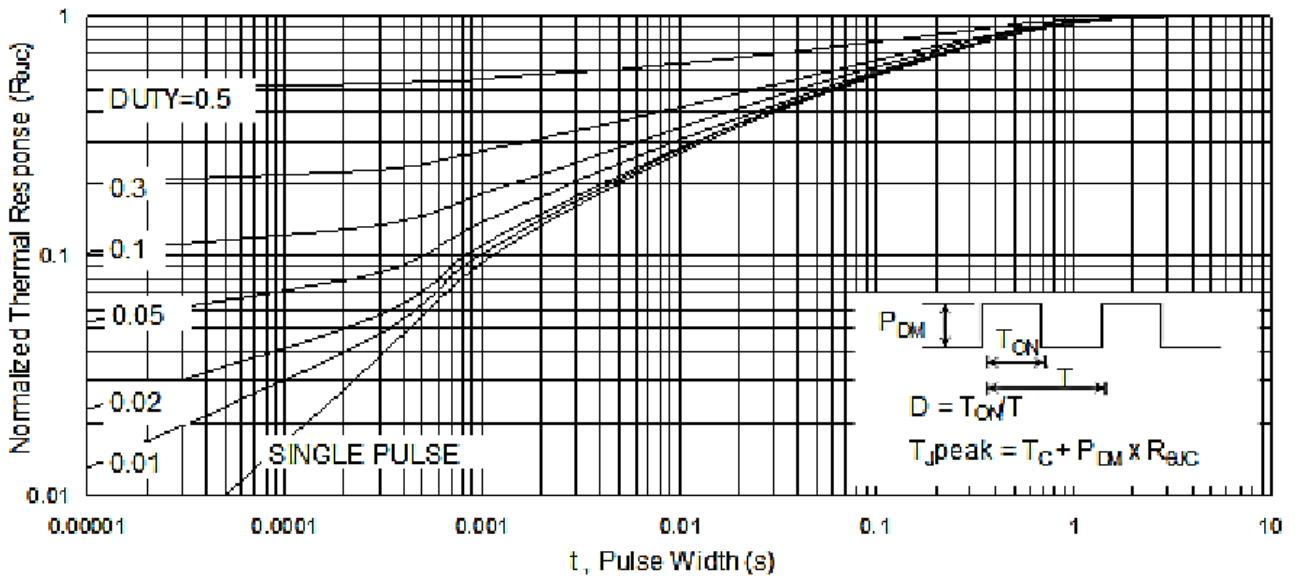


Fig.9 Normalized Maximum Transient Thermal Impedance

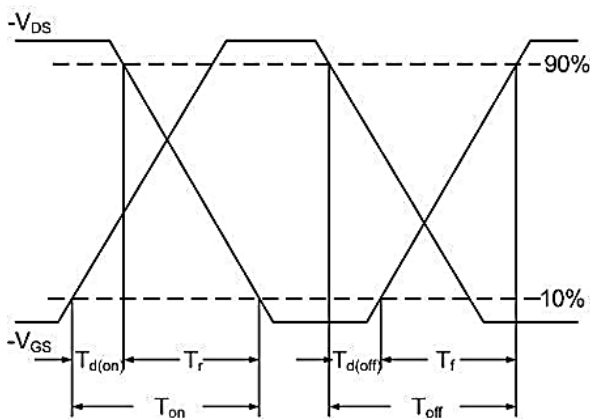


Fig.10 Switching Time Waveform

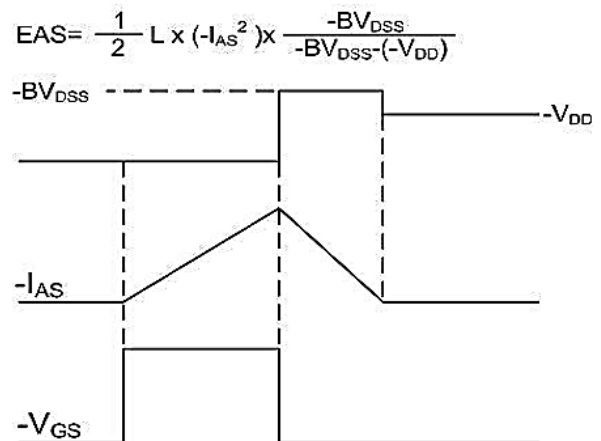
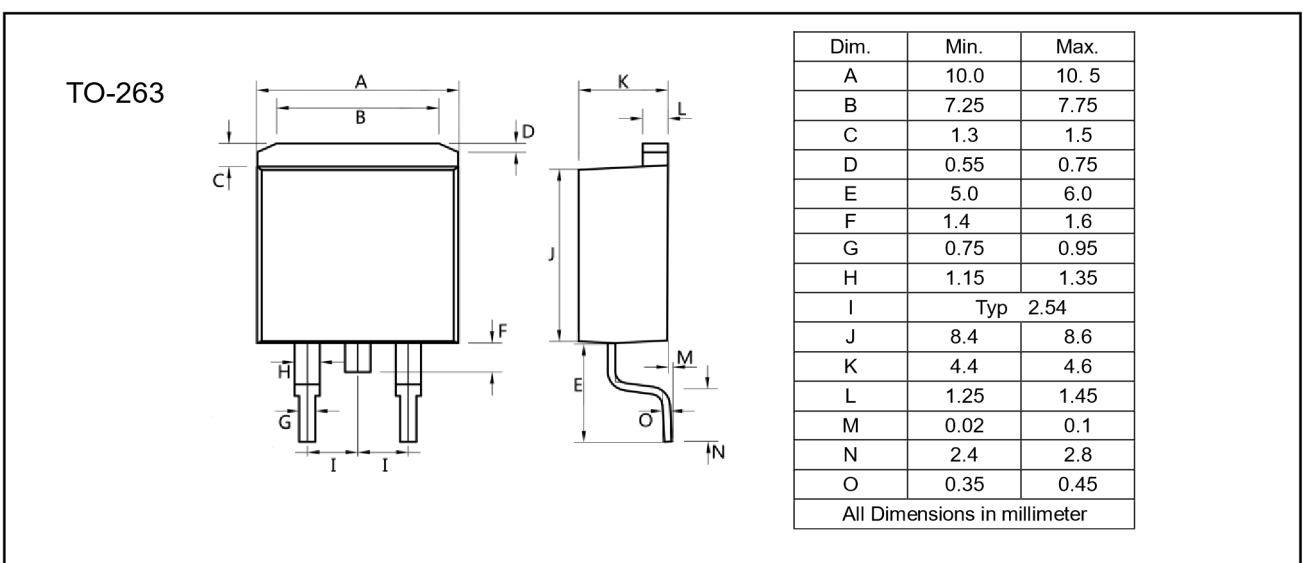
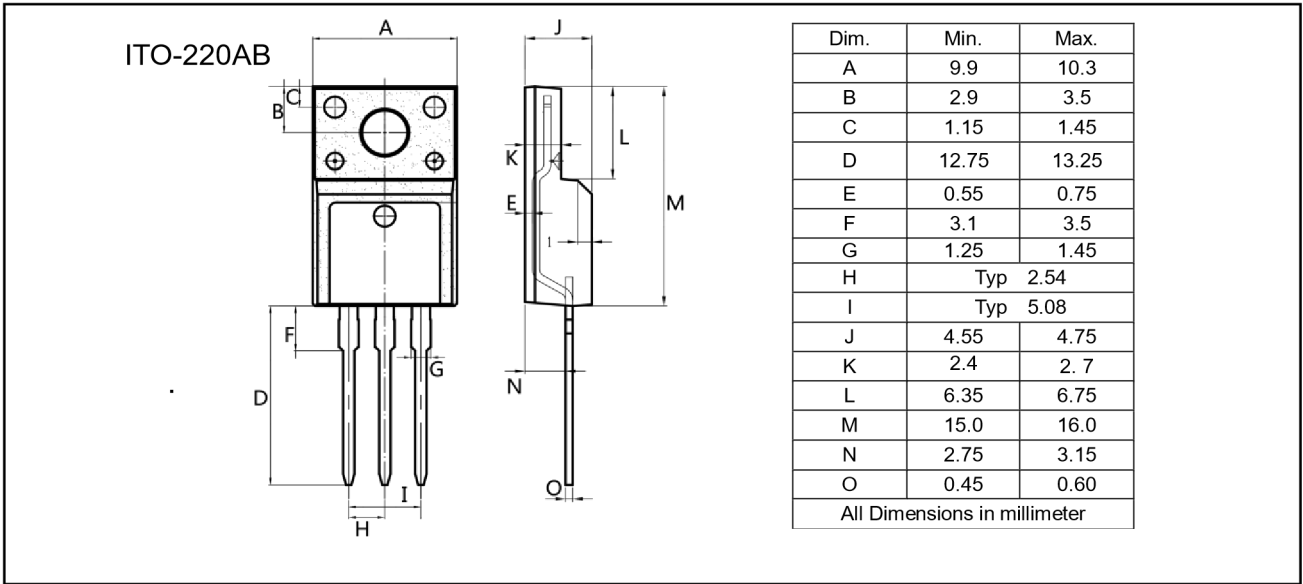
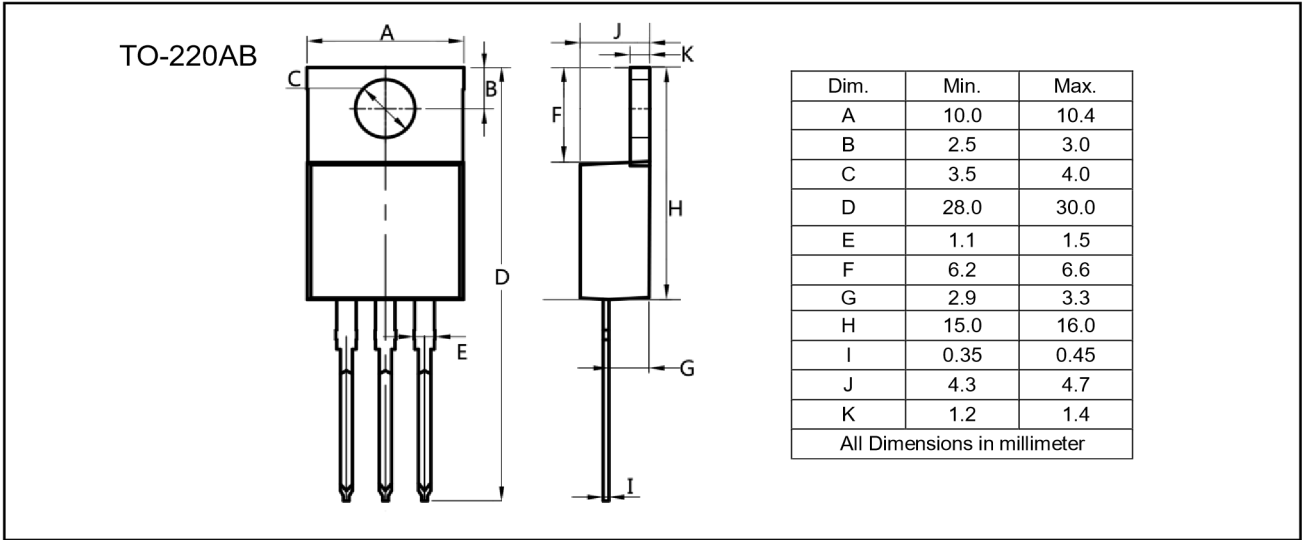


Fig.11 Unclamped Inductive Waveform

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Edition	Date	Change
REV1.0	2023/4/13	Initial release

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