



# AP4435

## Preliminary Data Sheet

### 30 P-Channel Enhancement-Mode MOSFET

$V_{DS} = -30V$

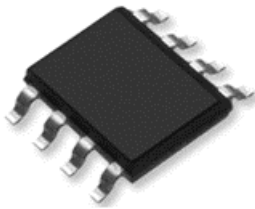
$R_{DS(ON)}, V_{GS}@-10V, I_{ds}@-9.1A} = 21m\Omega$

$R_{DS(ON)}, V_{GS}@-4.5V, I_{ds}@-6.9A} = 35m\Omega$

#### Features

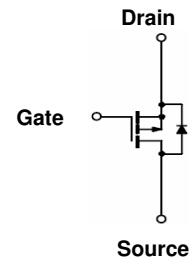
Advanced trench process technology  
High Density Cell Design For Ultra Low On-Resistance

S0-8



Top View

Internal Schematic Diagram



P-Channel MOSFET

#### Maximum Ratings and Thermal Characteristics ( $T_A = 25^\circ C$ unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	$V_{DS}$	-30	V
Gate-Source Voltage	$V_{GS}$	$\pm 20$	
Continuous Drain Current	$I_D$	-7	A
Pulsed Drain Current <sup>1)</sup>	$I_{DM}$	-50	A
		1.5	W
		0.9	W
Maximum Power Dissipation			
Operating Junction and Storage Temperature Range	$T_J, T_{stg}$	-55 to 150	$^\circ C$
Junction-to-Ambient Thermal Resistance (PCB mounted) <sup>2)</sup>	$R_{\theta JA}$	85	$^\circ C/W$
<b>Note:</b> 1. Repetitive Rating: Pulse width limited by the maximum junction temperature			
2. 1-in <sup>2</sup> 2oz Cu PCB board			
3. Guaranteed by design, not subject to production testing			

# AP4435

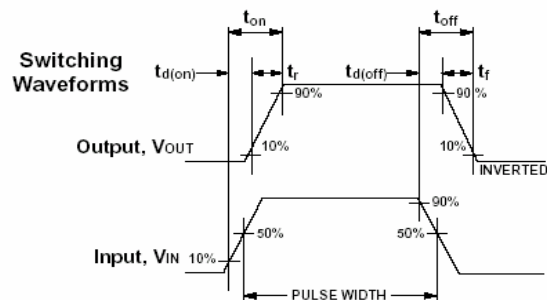
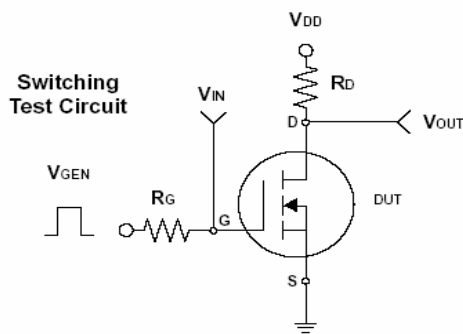
## Preliminary Data Sheet

### P-Channel Enhancement-Mode MOSFET

#### ELECTRICAL CHARACTERISTICS

Parameter	Symbol	Test Condition	Min	Typ	Max	Unit
<b>Static</b>						
Drain-Source Breakdown Voltage	$BV_{DSS}$	$V_{GS} = 0V, I_D = -250\mu A$	-30			V
Drain-Source On-State Resistance	$R_{DS(on)}$	$V_{GS} = -10V, I_D = -9.1A$		17.0	21.0	m $\Omega$
Drain-Source On-State Resistance	$R_{DS(on)}$	$V_{GS} = -4.5V, I_D = -6.9A$		23.0	35.0	
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = -250\mu A$	-1		-3	V
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS} = -30V, V_{GS} = 0V$			-1	$\mu A$
Gate Body Leakage	$I_{GSS}$	$V_{GS} = \pm 20V, V_{DS} = 0V$			$\pm 100$	nA
Forward Transconductance	$g_{fs}$	$V_{DS} = -10V, I_D = -9.1A$		24		S
<b>Dynamic</b>						
Total Gate Charge	$Q_g$	$V_{DS} = 20V, I_D = 5.7A$ $V_{GS} = 10V$				nC
Gate-Source Charge	$Q_{gs}$					
Gate-Drain Charge	$Q_{gd}$					
Turn-On Delay Time	$t_{d(on)}$	$V_{DD} = 20V, R_L = 20\Omega$ $I_D = 1A, V_{GEN} = 10V$ $R_G = 6\Omega$				ns
Turn-On Rise Time	$t_r$					
Turn-Off Delay Time	$t_{d(off)}$					
Turn-Off Fall Time	$t_f$					
Input Capacitance	$C_{iss}$	$V_{DS} = 8V, V_{GS} = 0V$ $f = 1.0\text{ MHz}$				pF
Output Capacitance	$C_{oss}$					
Reverse Transfer Capacitance	$C_{rss}$					
<b>Source-Drain Diode</b>						
Max. Diode Forward Current	$I_S$					A
Diode Forward Voltage	$V_{SD}$	$I_S = 1.8A, V_{GS} = 0V$				V

Note: Pulse test: pulse width  $\leq 300\mu s$ , duty cycle  $\leq 2\%$



## **Disclaimer Notice**

### **Notice**

Specification of the products displayed herein are subject to change without notice. Continuous development may necessitate changes in technical data without notice. GMOS Semiconductor Sdn. Bhd. or anyone on its behalf, assumes no responsibility or liability for any errors or inaccuracies.