



**Advanced Power
Electronics Corp.**

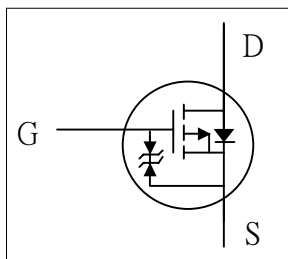
AP4455GYT-HF

Halogen-Free Product

P-CHANNEL ENHANCEMENT MODE

POWER MOSFET

- ▼ Simple Drive Requirement
- ▼ Small Size & Lower Profile
- ▼ RoHS Compliant & Halogen-Free

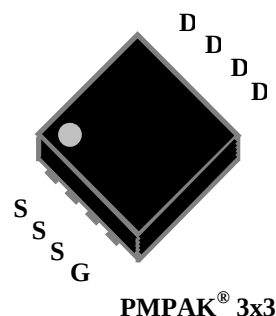


BV_{DSS}	-30V
$R_{DS(ON)}$	21m Ω
I_D	-10.6A

Description

Advanced Power MOSFETs from APEC provide the designer with the best combination of fast switching, ruggedized device design, low on-resistance and cost-effectiveness.

The PMPAK[®] 3x3 package is special for DC-DC converters application and lower 1.0mm profile with backside heat sink.



Absolute Maximum Ratings@T_j=25°C(unless otherwise specified)

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	-30	V
V_{GS}	Gate-Source Voltage	± 20	V
$I_D@T_A=25^\circ\text{C}$	Drain Current ³ , V_{GS} @ 10V	-10.6	A
$I_D@T_A=70^\circ\text{C}$	Drain Current ³ , V_{GS} @ 10V	-8.5	A
I_{DM}	Pulsed Drain Current ¹	-40	A
$P_D@T_A=25^\circ\text{C}$	Total Power Dissipation	3.57	W
T_{STG}	Storage Temperature Range	-55 to 150	$^\circ\text{C}$
T_J	Operating Junction Temperature Range	-55 to 150	$^\circ\text{C}$

Thermal Data

Symbol	Parameter	Value	Unit
Rthj-c	Maximum Thermal Resistance, Junction-case	5	$^\circ\text{C}/\text{W}$
Rthj-a	Maximum Thermal Resistance, Junction-ambient ³	35	$^\circ\text{C}/\text{W}$



AP4455GYT-HF

Electrical Characteristics@T_j=25°C(unless otherwise specified)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =-250uA	-30	-	-	V
R _{DS(ON)}	Static Drain-Source On-Resistance ²	V _{GS} =-10V, I _D =-8A	-	15	21	mΩ
		V _{GS} =-4.5V, I _D =-5A	-	21.3	36	mΩ
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _D =-250uA	-1	-1.6	-3	V
g _{fs}	Forward Transconductance	V _{DS} =-10V, I _D =-8A	-	20	-	S
I _{DSS}	Drain-Source Leakage Current	V _{DS} =-24V, V _{GS} =0V	-	-	-10	uA
I _{GSS}	Gate-Source Leakage	V _{GS} =±20V, V _{DS} =0V	-	-	±30	uA
Q _g	Total Gate Charge	I _D =-8A	-	19	30	nC
Q _{gs}	Gate-Source Charge	V _{DS} =-15V	-	4.5	-	nC
Q _{gd}	Gate-Drain ("Miller") Charge	V _{GS} =-4.5V	-	9	-	nC
t _{d(on)}	Turn-on Delay Time	V _{DS} =-15V	-	10	-	ns
t _r	Rise Time	I _D =-1A	-	6	-	ns
t _{d(off)}	Turn-off Delay Time	R _G =3.3Ω	-	60	-	ns
t _f	Fall Time	V _{GS} =-10V	-	40	-	ns
C _{iss}	Input Capacitance	V _{GS} =0V	-	1700	2720	pF
C _{oss}	Output Capacitance	V _{DS} =-15V	-	280	-	pF
C _{rss}	Reverse Transfer Capacitance	f=1.0MHz	-	250	-	pF
R _g	Gate Resistance	f=1.0MHz	3	6	12	Ω

Source-Drain Diode

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
V _{SD}	Forward On Voltage ²	I _S =-2.9A, V _{GS} =0V	-	-	-1.2	V
t _{rr}	Reverse Recovery Time	I _S =-8A, V _{GS} =0V,	-	23	-	ns
Q _{rr}	Reverse Recovery Charge	dI/dt=100A/μs	-	13	-	nC

Notes:

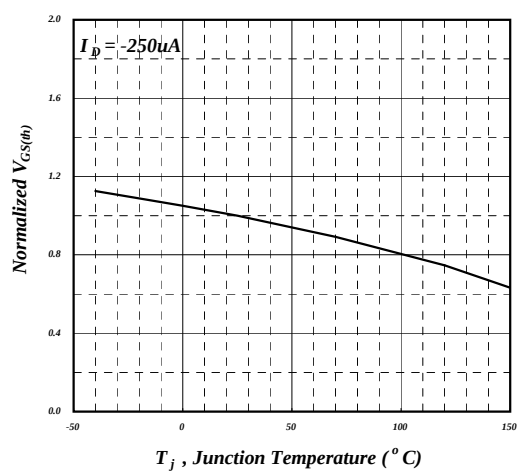
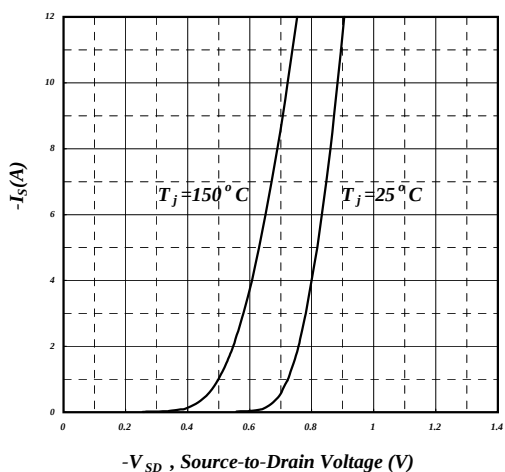
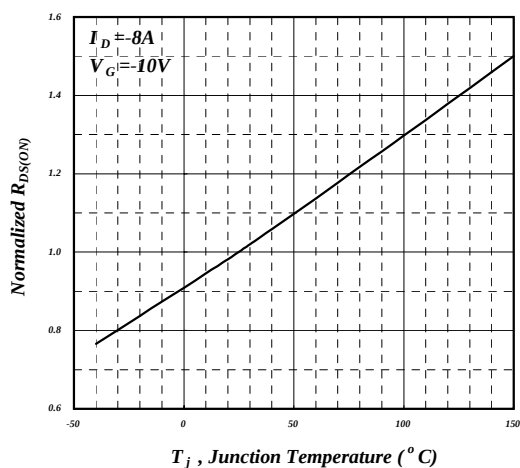
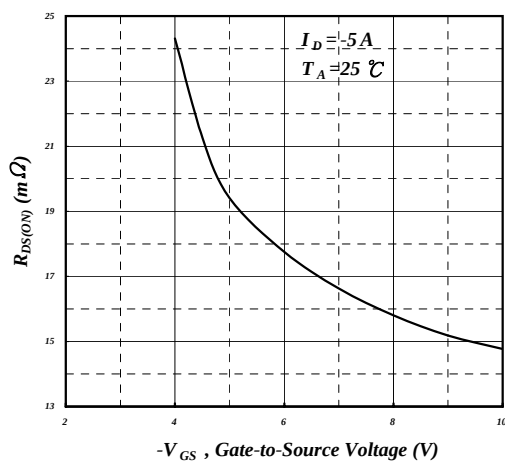
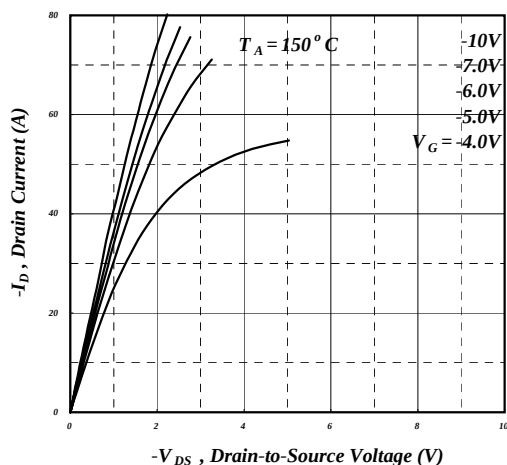
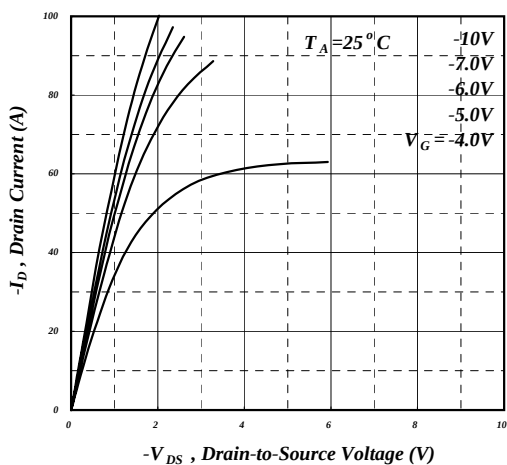
- 1.Pulse width limited by Max. junction temperature.
- 2.Pulse test
- 3.Surface mounted on 1 in² copper pad of FR4 board, t ≤10sec ; 85°C/W at steady state.

THIS PRODUCT IS SENSITIVE TO ELECTROSTATIC DISCHARGE, PLEASE HANDLE WITH CAUTION.

USE OF THIS PRODUCT AS A CRITICAL COMPONENT IN LIFE SUPPORT OR OTHER SIMILAR SYSTEMS IS NOT AUTHORIZED.

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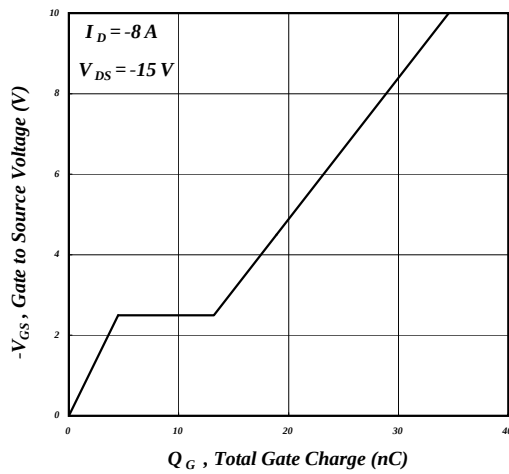


Fig 7. Gate Charge Characteristics

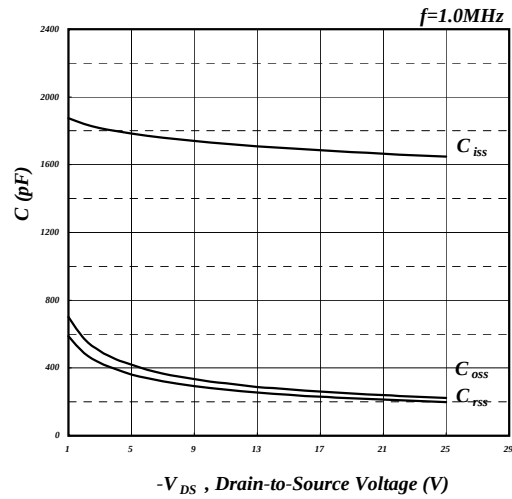


Fig 8. Typical Capacitance Characteristics

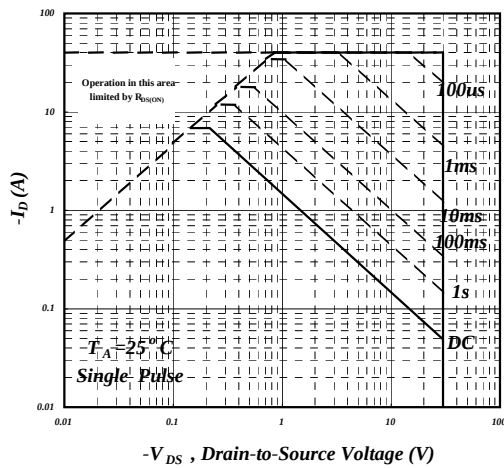


Fig 9. Maximum Safe Operating Area

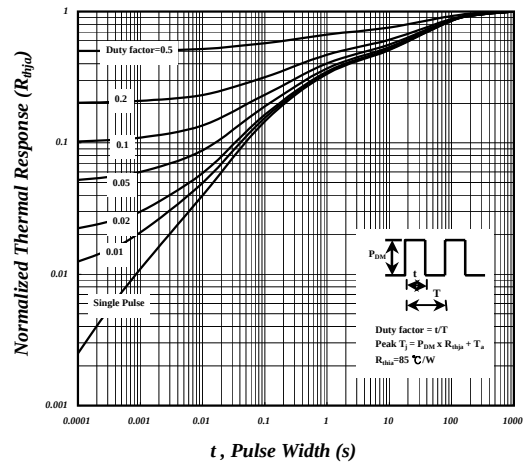


Fig 10. Effective Transient Thermal Impedance

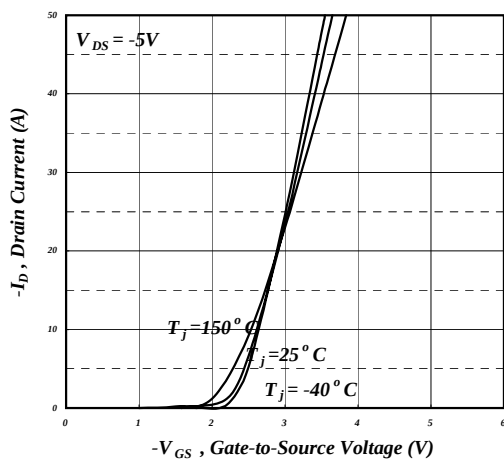


Fig 11. Transfer Characteristics

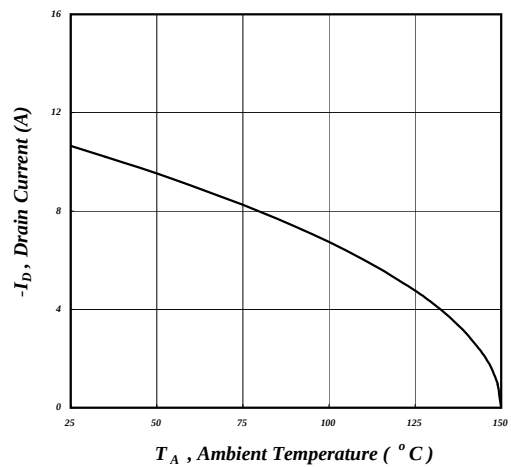


Fig 12. Drain Current v.s. Ambient Temperature



MARKING INFORMATION

