

## 30V N+P-Channel Enhancement Mode MOSFET

### Description

The AP4606B uses advanced trench technology to provide excellent  $R_{DS(ON)}$ , low gate charge and operation with gate voltages as low as 4.5V. This device is suitable for use as a Battery protection or in other Switching application.

### General Features

$V_{DS} = 30V$   $I_D = 6.8A$

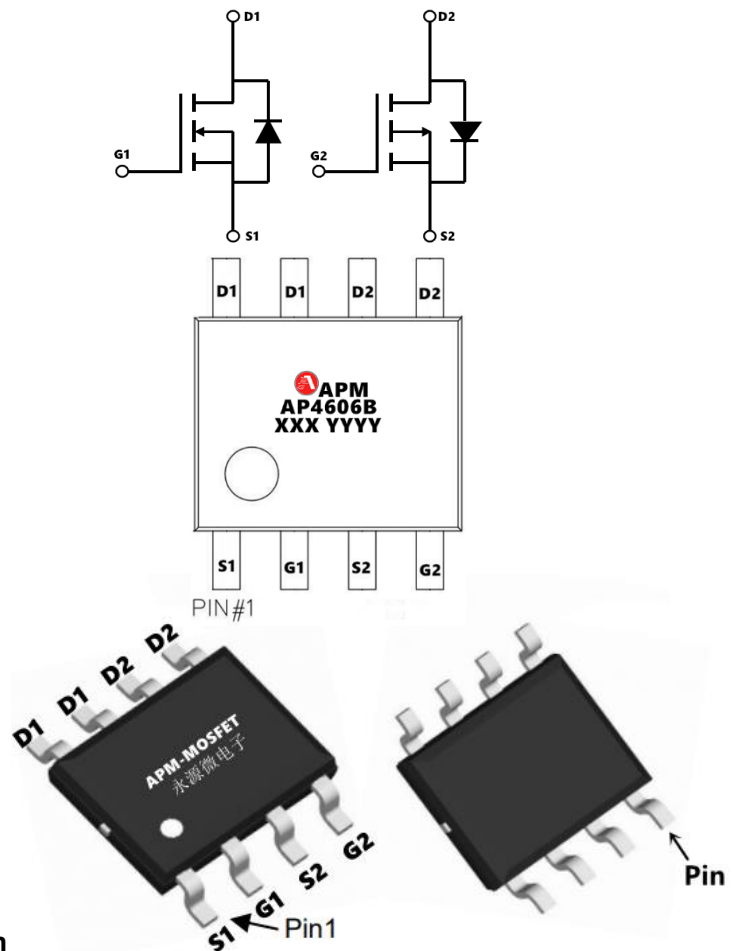
$R_{DS(ON)} < 28m\Omega$  @  $V_{GS}=10V$  (Type: 20m $\Omega$ )

$V_{DS} = -30V$   $I_D = -6.1A$

$R_{DS(ON)} < 50m\Omega$  @  $V_{GS}=-10V$  (Type: 42m $\Omega$ )

### Application

BLDC



### Package Marking and Ordering Information

Product ID	Pack	Marking	Qty(PCS)
AP4606B	SOP-8L	AP4606B XXX YYYY	3000

### Absolute Maximum Ratings ( $T_C=25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	N-Ch	P-Ch	Units
$V_{DS}$	Drain-Source Voltage	30	-30	V
$V_{GS}$	Gate-Source Voltage	$\pm 20$	$\pm 20$	V
$I_D @ T_A=25^\circ\text{C}$	Continuous Drain Current, $V_{GS} @ 10V^1$	6.8	-6.1	A
$I_D @ T_A=70^\circ\text{C}$	Continuous Drain Current, $V_{GS} @ 10V^1$	4.5	-4.1	A
IDM	Pulsed Drain Current <sup>2</sup>	18	-18	A
EAS	Single Pulse Avalanche Energy <sup>3</sup>	22.6	33	mJ
IAS	Avalanche Current	8	-7.5	A
$P_D @ T_A=25^\circ\text{C}$	Total Power Dissipation <sup>4</sup>	1.3		W
TSTG	Storage Temperature Range	-55 to 150		$^\circ\text{C}$
$T_J$	Operating Junction Temperature Range	-55 to 150		$^\circ\text{C}$
$R_{\theta JA}$	Thermal Resistance Junction-Ambient <sup>1</sup>	85		$^\circ\text{C}/\text{W}$
$R_{\theta JC}$	Thermal Resistance Junction-Case <sup>1</sup>	65		$^\circ\text{C}/\text{W}$

## 30V N+P-Channel Enhancement Mode MOSFET

### N-Channel Electrical Characteristics (T<sub>J</sub>=25°C, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
V(BR)DSS	Drain-Source Breakdown Voltage	ID=250μA, VGS=0V	30	-	-	V
IDSS	Zero Gate Voltage Drain Current	VDS=30V, VGS=0V	-	-	1.0	μA
IGSS	Gate-Body Leakage Current	VDS=0V, VGS=±20V	-	-	±100	nA
VGS(th)	Gate Threshold Voltage	VDS=VGS, ID=250μA	1.0	1.5	2.5	V
RDS(ON)	Static Drain-Source ON-Resistance	VGS=10V, ID= 5A	-	20	28	mΩ
		VGS=4.5V, ID=3A	-	30	40	mΩ
Ciss	Input Capacitance	VGS=0V, VDS = 15V, f=1MHz	-	388	-	pF
Coss	Output Capacitance		-	57	-	pF
Crss	Reverse Transfer Capacitance		-	45	-	pF
Qg	Total Gate Charge	VGS=0 to 10V VDS=15V, ID=3A	-	9	-	nC
Qgs	Gate Source Charge		-	1.5	-	nC
Qgd	Gate Drain("Miller") Charge		-	2	-	nC
td(on)	Turn-On DelayTime	VGS=10V, VDD=15V ID= 3A, RGEN=3Ω	-	2	-	ns
tr	Turn-On Rise Time		-	6	-	ns
td(off)	Turn-Off DelayTime		-	61	-	ns
tf	Turn-Off Fall Time		-	34	-	ns
IS	Maximum Continuous Drain to Source Diode Forward Current		-	-	5	A
ISM	Maximum Pulsed Drain to Source Diode Forward Current		-	-	20	A
VSD	Drain to Source Diode Forward Voltage	VGS=0V, IS=5A	-	-	1.2	V
trr	Body Diode Reverse Recovery Time	IF=3A, di/dt=100A/us	-	6	-	ns
Qrr	Body Diode Reverse Recovery Charge		-	2	-	nC

#### Note :

- 1、 The data tested by surface mounted on a 1 inch<sup>2</sup> FR-4 board with 2OZ copper.
- 2、 The data tested by pulsed , pulse width ≅ 300us , duty cycle ≅ 2%
- 3、 The power dissipation is limited by 150°C junction temperature
- 4、 The data is theoretically the same as I<sub>D</sub> and I<sub>DM</sub> , in real applications , should be limited by total power dissipation.

## 30V N+P-Channel Enhancement Mode MOSFET

### P-Channel Electrical Characteristics ( $T_J=25^{\circ}\text{C}$ , unless otherwise noted)

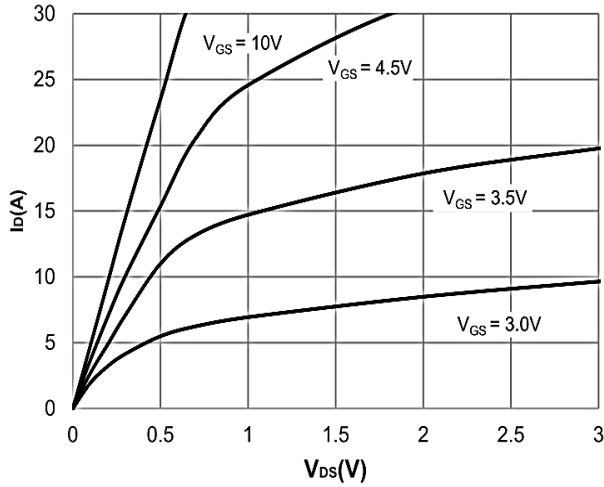
Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
V(BR)DSS	Drain-Source Breakdown Voltage	$I_D=-250\mu\text{A}$ , $V_{GS}=0\text{V}$	-30	-	-	V
IDSS	Zero Gate Voltage Drain Current	$V_{DS}=-30\text{V}$ , $V_{GS}=0\text{V}$	-	-	-1.0	$\mu\text{A}$
IGSS	Gate-Body Leakage Current	$V_{DS}=0\text{V}$ , $V_{GS}=\pm 20\text{V}$	-	-	$\pm 100$	nA
VGS(th)	Gate Threshold Voltage	$V_{DS}=V_{GS}$ , $I_D=-250\mu\text{A}$	-1.0	-1.8	-2.5	V
RDS(ON)	Static Drain-Source ON-Resistance <sup>(3)</sup>	$V_{GS}=-10\text{V}$ , $I_D=-5\text{A}$	-	42	50	m $\Omega$
		$V_{GS}=-4.5\text{V}$ , $I_D=-3\text{A}$	-	60	75	m $\Omega$
Ciss	Input Capacitance	$V_{GS}=0\text{V}$ , $V_{DS}=-15\text{V}$ , $f=1\text{MHz}$	-	540	-	pF
Coss	Output Capacitance		-	75	-	pF
Crss	Reverse Transfer Capacitance		-	57	-	pF
Qg	Total Gate Charge	$V_{GS}=0$ to $-10\text{V}$ , $V_{DS}=-15\text{V}$ , $I_D=-2\text{A}$	-	11	-	nC
Qgs	Gate Source Charge		-	2	-	nC
Qgd	Gate Drain("Miller") Charge		-	2	-	nC
td(on)	Turn-On Delay Time	$V_{GS}=-10\text{V}$ , $V_{DD}=-15\text{V}$ $I_D=-2\text{A}$ , $R_{GEN}=3\Omega$	-	3	-	ns
tr	Turn-On Rise Time		-	2	-	ns
td(off)	Turn-Off Delay Time		-	26	-	ns
tf	Turn-Off Fall Time		-	15	-	ns
IS	Maximum Continuous Drain to Source Diode Forward Current		-	-	-4.1	A
ISM	Maximum Pulsed Drain to Source Diode Forward Current		-	-	-16	A
VSD	Drain to Source Diode Forward Voltage	$V_{GS}=0\text{V}$ , $I_S=-4.1\text{A}$	-	-	-1.2	V
trr	Body Diode Reverse Recovery Time	$I_F=-2\text{A}$ , $di/dt=100\text{A}/\mu\text{s}$	-	9	-	ns
Qrr	Body Diode Reverse Recovery Charge		-	3	-	nC

**Note :**

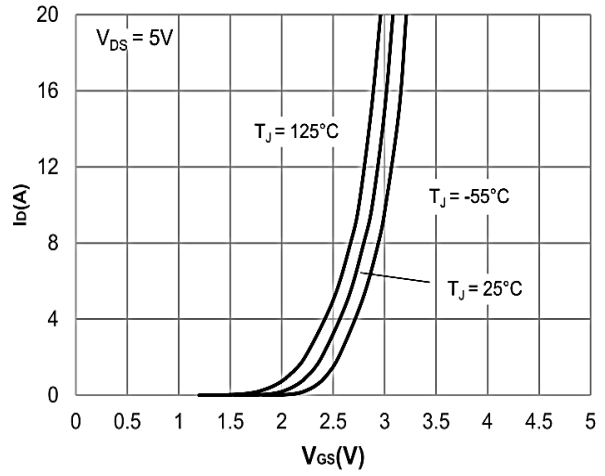
- 1、The data tested by surface mounted on a 1 inch FR-4 board with 2OZ copper.
- 2、The data tested by pulsed , pulse width  $\leq 300\mu\text{s}$  , duty cycle  $\leq 2\%$
- 3、The power dissipation is limited by  $150^{\circ}\text{C}$  junction temperature
- 4、The data is theoretically the same as  $I_D$  and  $I_{DM}$  , in real applications , should be limited by total power dissipation.

**30V N+P-Channel Enhancement Mode MOSFET**

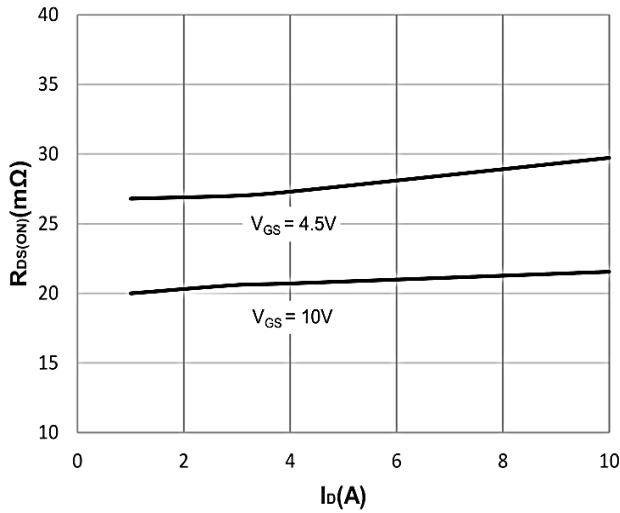
**Typical Characteristics**



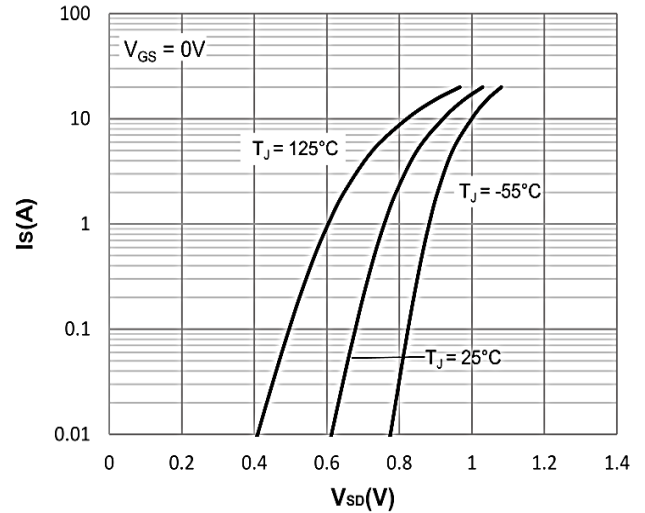
**Figure 1: Output Characteristics**



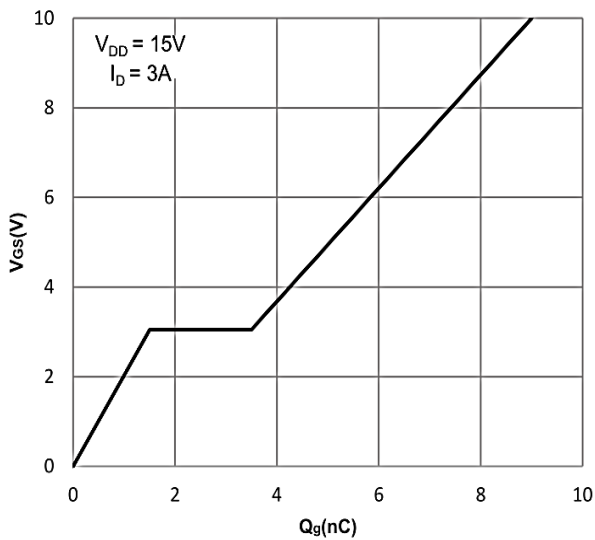
**Figure 2: Typical Transfer Characteristics**



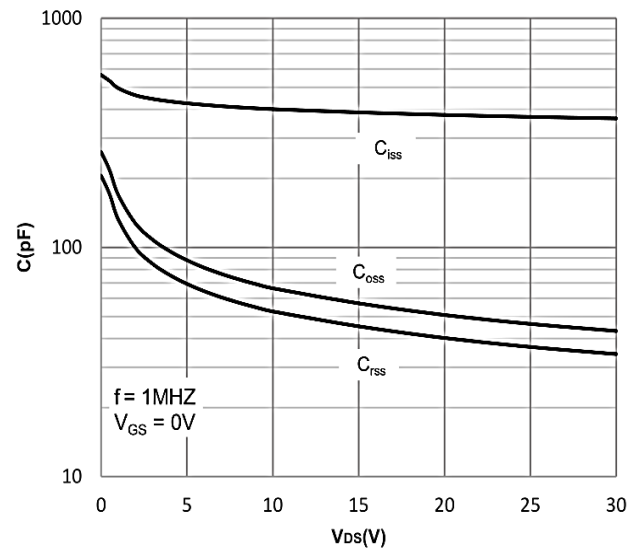
**Figure 3: On-resistance vs. Drain Current**



**Figure 4: Body Diode Characteristics**

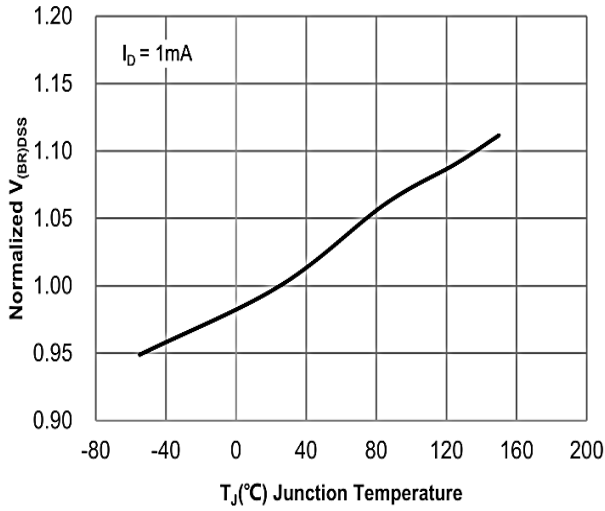


**Figure 5: Gate Charge Characteristics**

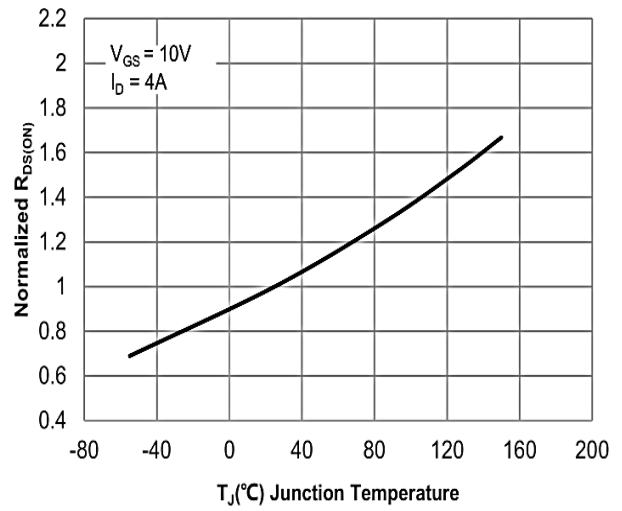


**Figure 6: Capacitance Characteristics**

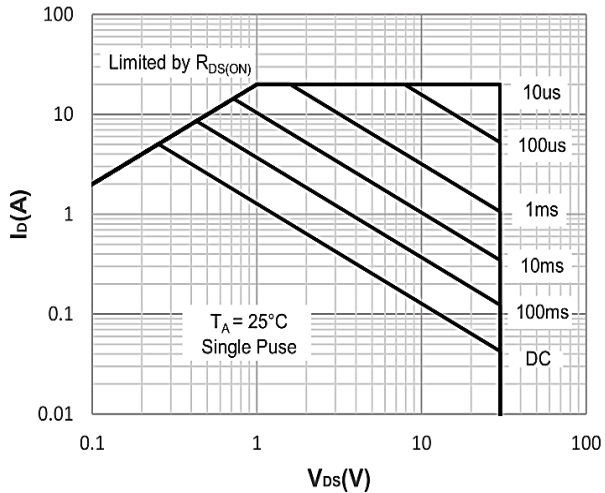
**30V N+P-Channel Enhancement Mode MOSFET**



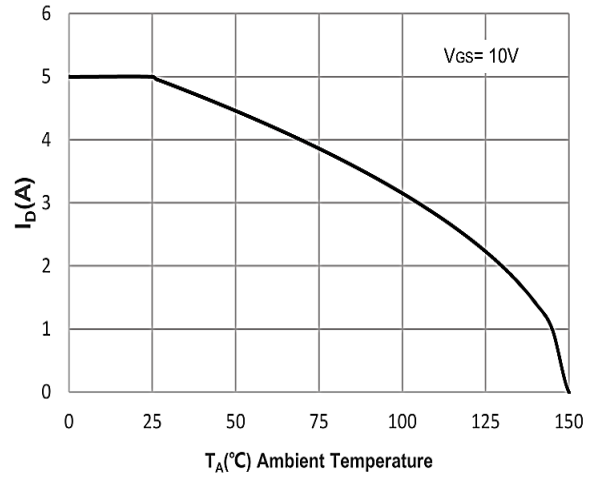
**Figure 7: Normalized Breakdown Voltage vs. Junction Temperature**



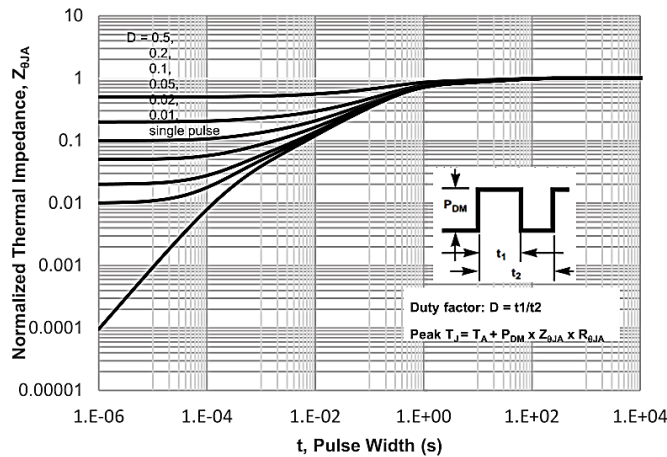
**Figure 8: Normalized on Resistance vs. Junction Temperature**



**Figure 9: Maximum Safe Operating Area vs. Case Temperature**



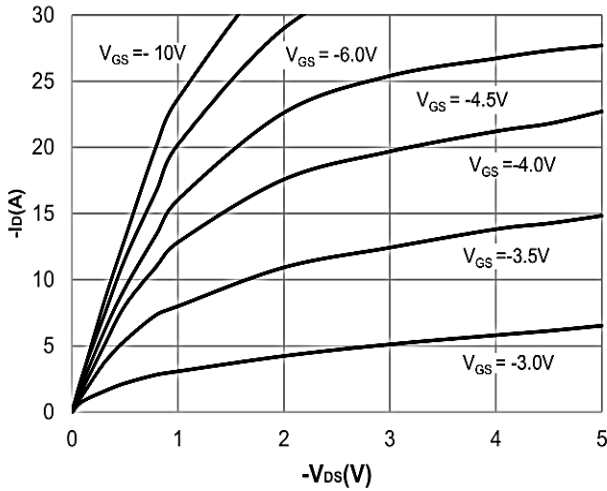
**Figure 10: Maximum Continuous Drain Current vs. Ambient Temperature**



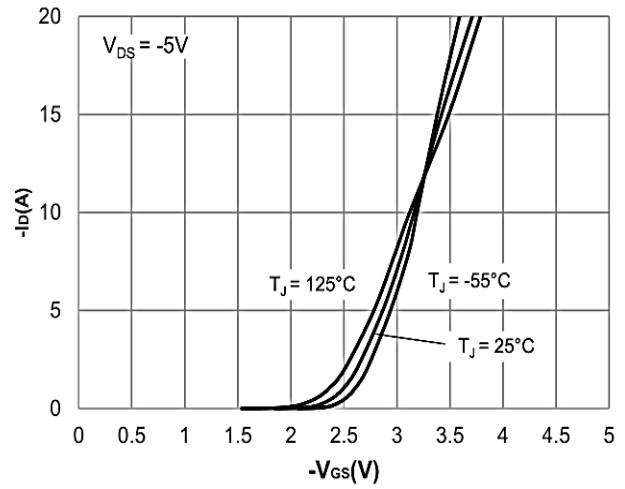
**Figure.11: Maximum Effective Transient Thermal Impedance, Junction-to-Ca**

**30V N+P-Channel Enhancement Mode MOSFET**

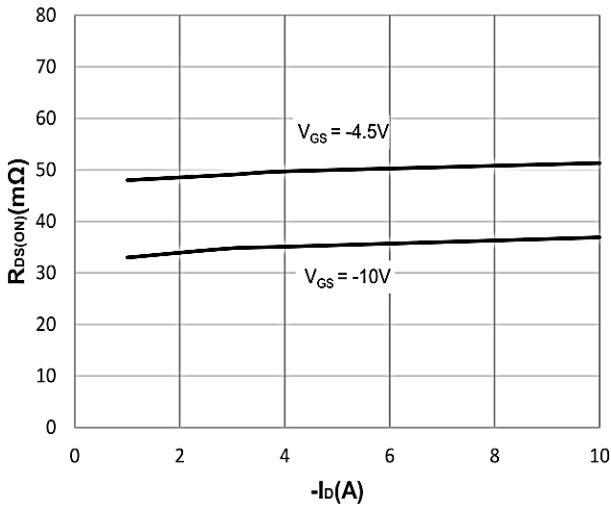
**P-Channel Typical Characteristics**



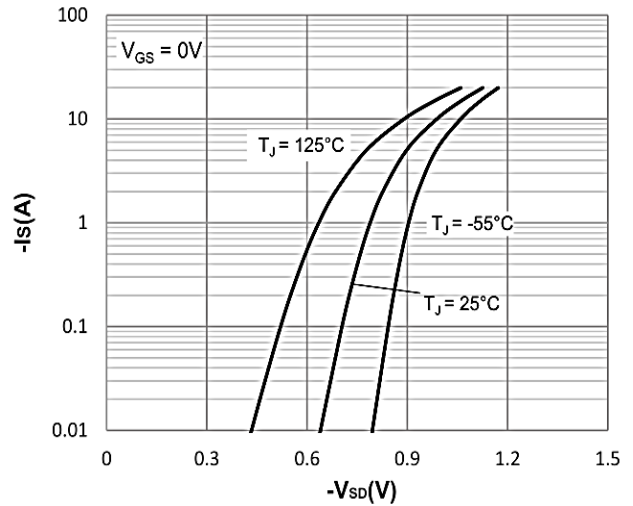
**Figure 1: Output Characteristics**



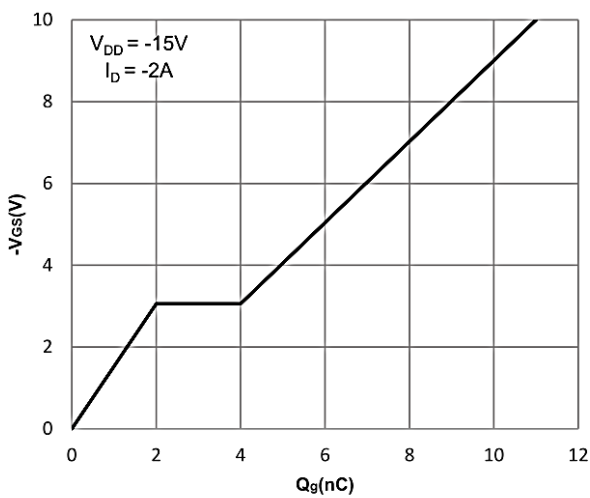
**Figure 2: Typical Transfer Characteristics**



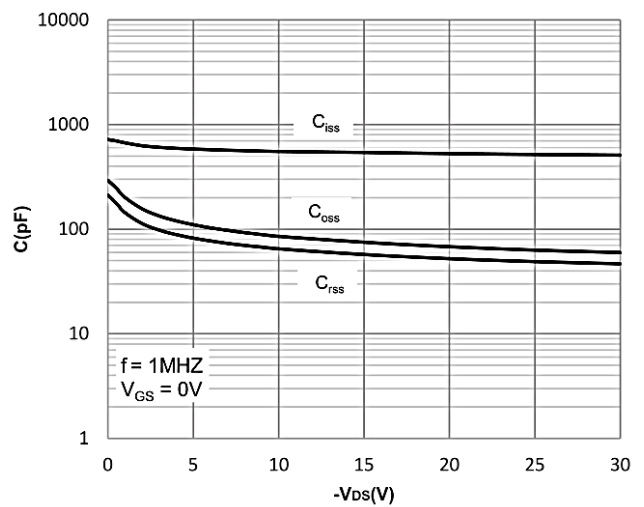
**Figure 3: On-resistance vs. Drain Current**



**Figure 4: Body Diode Characteristics**



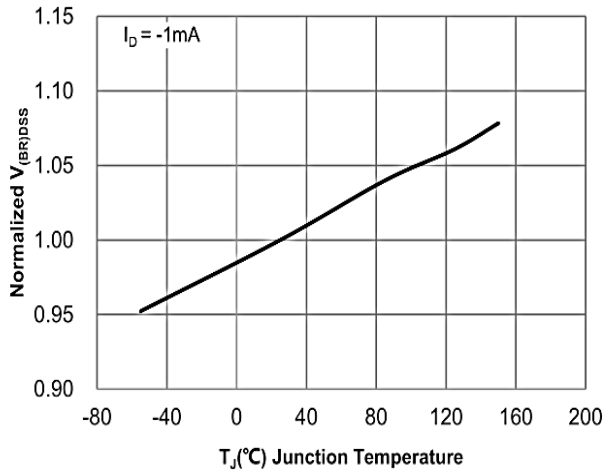
**Figure 5: Gate Charge Characteristics**



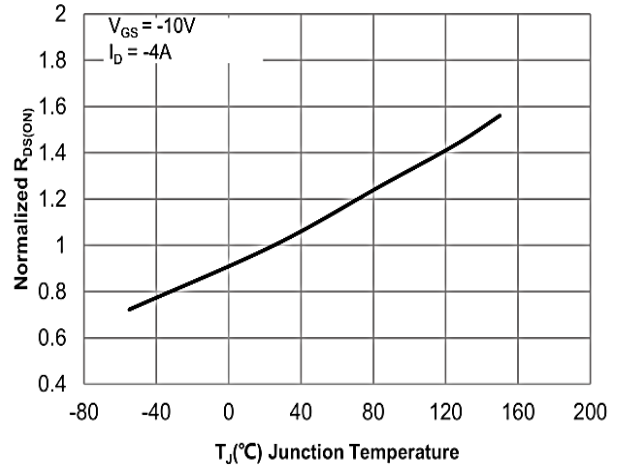
**Figure 6: Capacitance Characteristics**



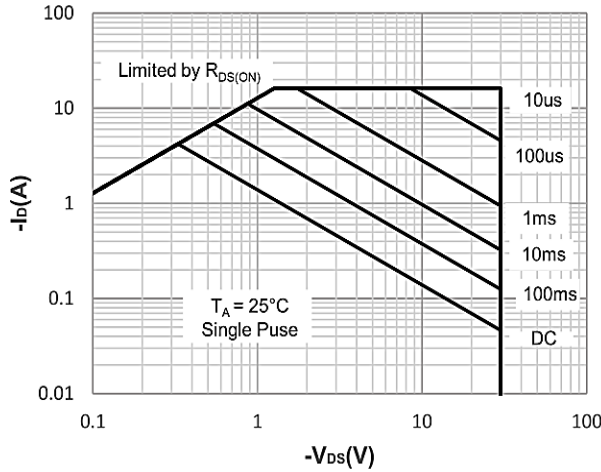
**30V N+P-Channel Enhancement Mode MOSFET**



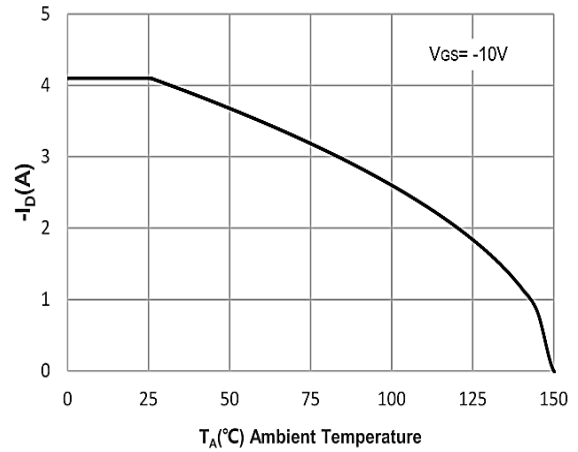
**Figure 7: Normalized Breakdown Voltage vs. Junction Temperature**



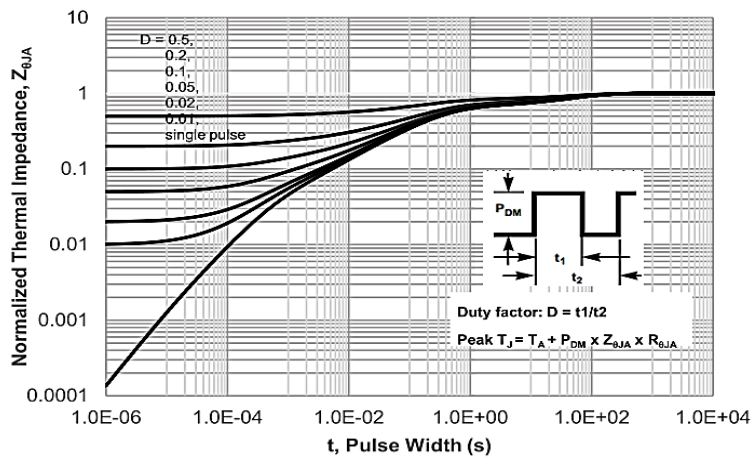
**Figure 8: Normalized on Resistance vs. Junction Temperature**



**Figure 9: Maximum Safe Operating Area vs. Case Temperature**

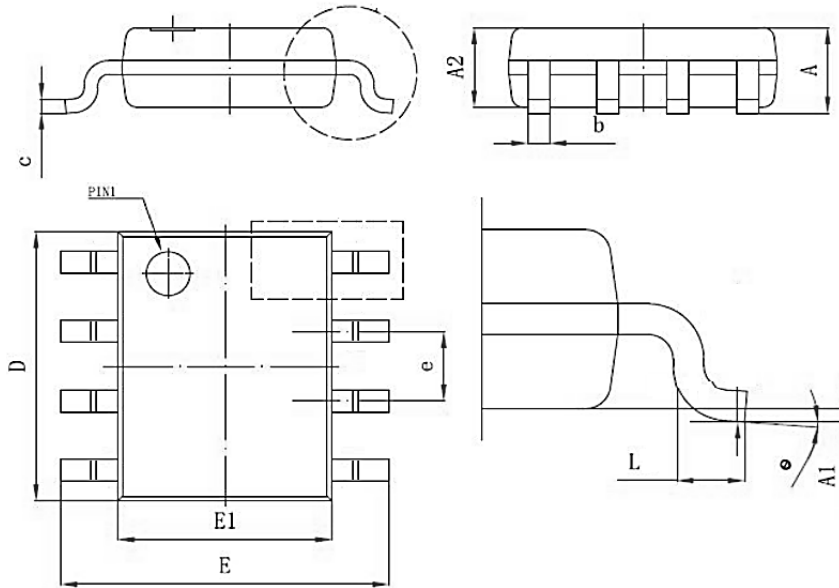


**Figure 10: Maximum Continuous Drain Current vs. Ambient Temperature**



**Figure.11: Maximum Effective Transient Thermal Impedance, Junction-to-Ca**

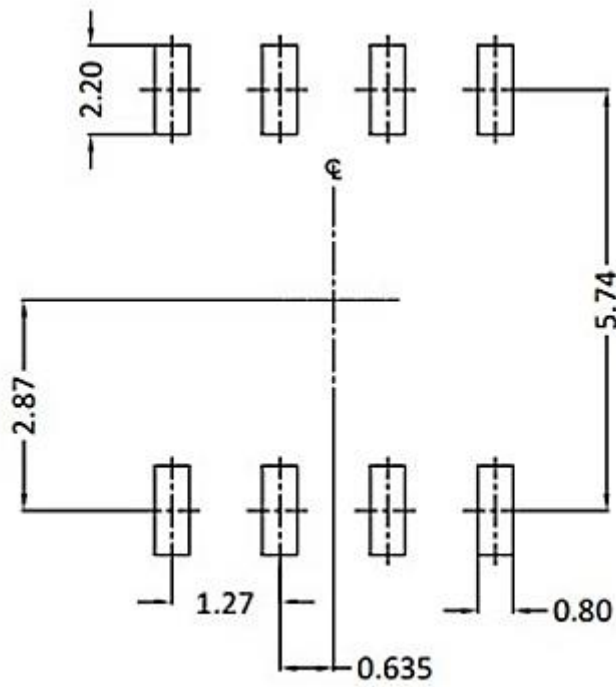
Package Mechanical Data-SOP-8L



Symbol	Dim in mm		
	Min	Typ	Max
A	1.35	1.55	1.75
A1	0.02	0.15	0.25
A2	1.425	1.45	1.475
b	0.3	0.4	0.5
c	0.15	0.2	0.25
D	4.8	5	5.2
E	5.8	6	6.2
E1	3.8	4	4.2
e	1.27BSC		
L	0.4		1.27
θ	0°		8°



Recommended Minimum Pads



Dimensions in (mm)

**30V N+P-Channel Enhancement Mode MOSFET****Attention**

1, Any and all APM Microelectronics products described or contained herein do not have specifications that can handle applications that require extremely high levels of reliability, such as life support systems, aircraft's control systems, or other applications whose failure can be reasonably expected to result in serious physical and/or material damage. Consult with your APM Microelectronics representative nearest you before using any APM Microelectronics products described or contained herein in such applications.

2, APM Microelectronics assumes no responsibility for equipment failures that result from using products at values that exceed, even momentarily, rated values (such as maximum ratings, operating condition ranges, or other parameters) listed in products specifications of any and all APM Microelectronics products described or contained herein.

3, Specifications of any and all APM Microelectronics products described or contained here instipulate the performance, characteristics, and functions of the described products in the independent state, and are not guarantees of the performance, characteristics, and functions of the described products as mounted in the customer's products or equipment. To verify symptoms and states that cannot be evaluated in an independent device, the customer should always evaluate and test devices mounted in the customer's products or equipment.

4, APM Microelectronics Semiconductor CO., LTD. strives to supply high quality high reliability products. However, any and all semiconductor products fail with some probability. It is possible that these probabilistic failures could give rise to accidents or events that could endanger human lives that could give rise to smoke or fire, or that could cause damage to other property. When designing equipment, adopt safety measures so that these kinds of accidents or events cannot occur. Such measures include but are not limited to protective circuits and error prevention circuits for safe design, redundant design, and structural design.

5, In the event that any or all APM Microelectronics products (including technical data, services) described or contained herein are controlled under any of applicable local export control laws and regulations, such products must not be exported without obtaining the export license from the authorities concerned in accordance with the above law.

6, No part of this publication may be reproduced or transmitted in any form or by any means, electronic or mechanical, including photocopying and recording, or any information storage or retrieval system, or otherwise, without the prior written permission of APM Microelectronics Semiconductor CO., LTD.

7, Information (including circuit diagrams and circuit parameters) herein is for example only; it is not guaranteed for volume production. APM Microelectronics believes information herein is accurate and reliable, but no guarantees are made or implied regarding its use or any infringements of intellectual property rights or other rights of third parties.

8, Any and all information described or contained herein are subject to change without notice due to product/technology improvement, etc. When designing equipment, refer to the "Delivery Specification" for the APM Microelectronics product that you intend to use.

**30V N+P-Channel Enhancement Mode MOSFET**

Edition	Date	Change
REV1.0	2022/12/10	Initial release
REV1.1	2024/3/1	Reduce RDS(on)

**Copyright Attribution“APM-Microelectronic”**