



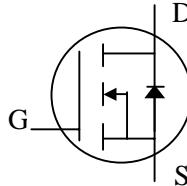
N-channel Enhancement-mode Power MOSFET

Simple Drive Requirement

Low On-resistance

Fast Switching Performance

RoHS-compliant, halogen-free

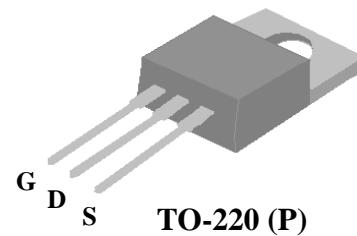


BV_{DSS}	100V
$R_{DS(ON)}$	28mΩ
I_D	38A

Description

Advanced Power MOSFETs from APEC provide the designer with the best combination of fast switching, low on-resistance and cost-effectiveness.

The AP50T10GP-HF-3 is in the TO-220 through-hole package which is used in commercial applications where a low PCB footprint or an attached heatsink is required. This device is well suited for low voltage applications such as DC/DC converters.



Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	100	V
V_{GS}	Gate-Source Voltage	± 20	V
I_D at $T_C=25^\circ\text{C}$	Continuous Drain Current	38	A
I_D at $T_C=100^\circ\text{C}$	Continuous Drain Current	24	A
I_{DM}	Pulsed Drain Current ¹	120	A
P_D at $T_C=25^\circ\text{C}$	Total Power Dissipation	89.2	W
P_D at $T_A=25^\circ\text{C}$	Total Power Dissipation	2	W
T_{STG}	Storage Temperature Range	-55 to 150	°C
T_J	Operating Junction Temperature Range	-55 to 150	°C

Thermal Data

Symbol	Parameter	Value	Units
R_{thj-c}	Maximum Thermal Resistance, Junction-case	1.4	°C/W
R_{thj-a}	Maximum Thermal Resistance, Junction-ambient	62	°C/W

Ordering Information

AP50T10GP-HF-3TB RoHS-compliant halogen-free TO-220, shipped in tubes (50pcs/tube)



Electrical Specifications at $T_j=25^\circ\text{C}$ (unless otherwise specified)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}$, $I_{\text{D}}=250\mu\text{A}$	100	-	-	V
$R_{\text{DS}(\text{ON})}$	Static Drain-Source On-Resistance ²	$V_{\text{GS}}=10\text{V}$, $I_{\text{D}}=24\text{A}$	-	-	28	$\text{m}\Omega$
		$V_{\text{GS}}=5\text{V}$, $I_{\text{D}}=16\text{A}$	-	-	45	$\text{m}\Omega$
$V_{\text{GS}(\text{th})}$	Gate Threshold Voltage	$V_{\text{DS}}=V_{\text{GS}}$, $I_{\text{D}}=250\mu\text{A}$	1	-	3	V
g_{fs}	Forward Transconductance	$V_{\text{DS}}=10\text{V}$, $I_{\text{D}}=24\text{A}$	-	30	-	S
I_{DSS}	Drain-Source Leakage Current	$V_{\text{DS}}=80\text{V}$, $V_{\text{GS}}=0\text{V}$	-	-	25	uA
I_{GSS}	Gate-Source Leakage	$V_{\text{GS}}=\pm 20\text{V}$, $V_{\text{DS}}=0\text{V}$	-	-	± 100	nA
Q_{g}	Total Gate Charge ²	$I_{\text{D}}=24\text{A}$	-	42	67	nC
Q_{gs}	Gate-Source Charge		-	7	-	nC
Q_{gd}	Gate-Drain ("Miller") Charge		-	18	-	nC
$t_{\text{d}(\text{on})}$	Turn-on Delay Time ²	$V_{\text{DS}}=50\text{V}$	-	10	-	ns
t_{r}	Rise Time	$I_{\text{D}}=24\text{A}$	-	30	-	ns
$t_{\text{d}(\text{off})}$	Turn-off Delay Time	$R_{\text{G}}=1\Omega$	-	27	-	ns
t_{f}	Fall Time	$V_{\text{GS}}=10\text{V}$	-	8	-	ns
C_{iss}	Input Capacitance	$V_{\text{GS}}=0\text{V}$	-	1840	2940	pF
C_{oss}	Output Capacitance	$V_{\text{DS}}=25\text{V}$	-	190	-	pF
C_{rss}	Reverse Transfer Capacitance	$f=1.0\text{MHz}$	-	130	-	pF
R_{g}	Gate Resistance	$f=1.0\text{MHz}$	-	1.7	-	Ω

Source-Drain Diode

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
V_{SD}	Forward On Voltage ²	$I_{\text{S}}=24\text{A}$, $V_{\text{GS}}=0\text{V}$	-	-	1.3	V
t_{rr}	Reverse Recovery Time ²	$I_{\text{S}}=10\text{A}$, $V_{\text{GS}}=0\text{V}$	-	50	-	ns
Q_{rr}	Reverse Recovery Charge	$dI/dt=100\text{A}/\mu\text{s}$	-	100	-	nC

Notes:

1. Pulse width limited by maximum junction temperature.
2. Pulse test

THIS PRODUCT IS SENSITIVE TO ELECTROSTATIC DISCHARGE, PLEASE HANDLE WITH CAUTION.

USE OF THIS PRODUCT AS A CRITICAL COMPONENT IN LIFE SUPPORT OR OTHER SIMILAR SYSTEMS IS NOT AUTHORIZED.

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Typical Electrical Characteristics

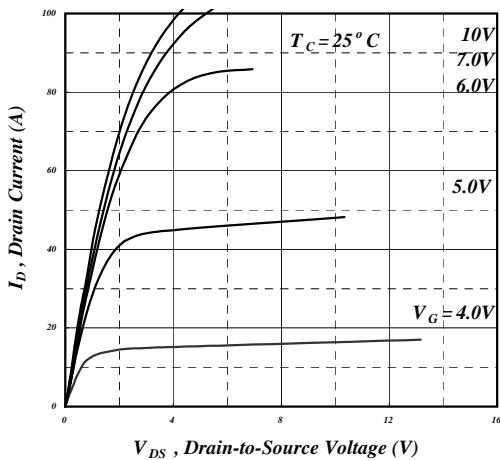


Fig 1. Typical Output Characteristics

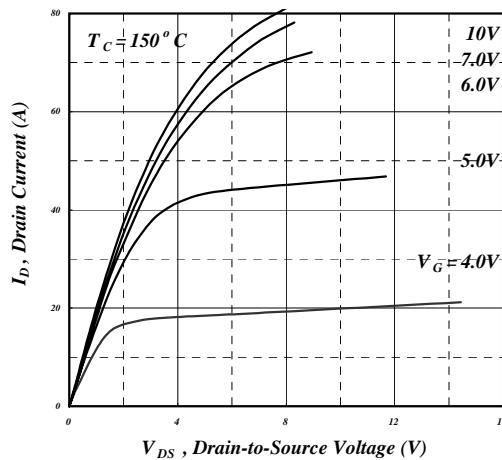


Fig 2. Typical Output Characteristics

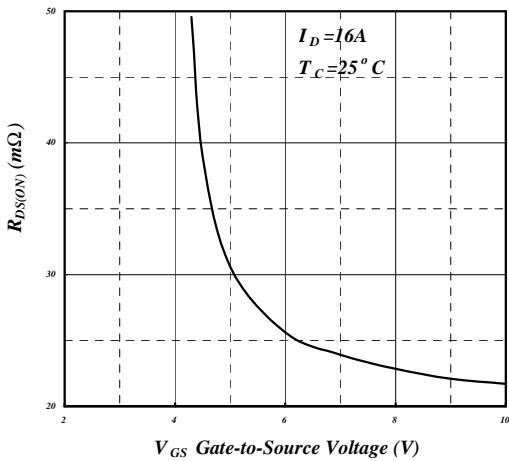


Fig 3. Normalised BVDSS
vs. Junction Temperature

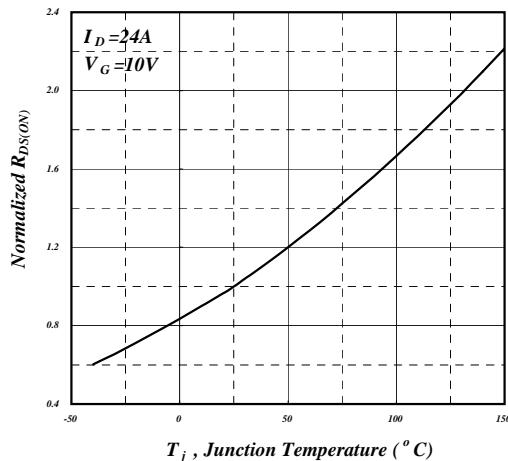


Fig 4. Normalized On-Resistance
vs. Junction Temperature

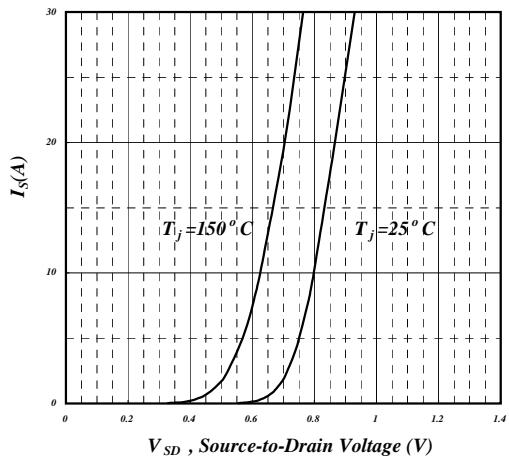


Fig 5. Forward Characteristic of
Reverse Diode

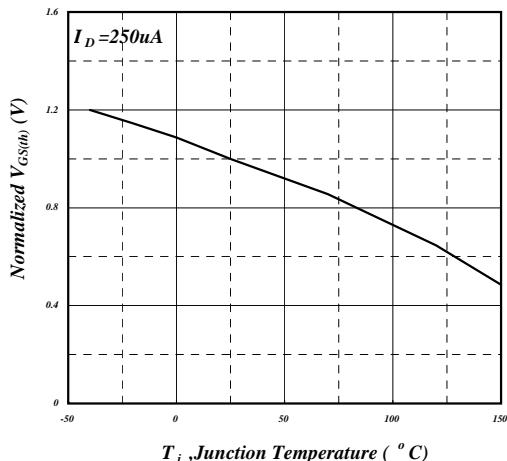


Fig 6. Gate Threshold Voltage vs.
Junction Temperature



Typical Electrical Characteristics (cont.)

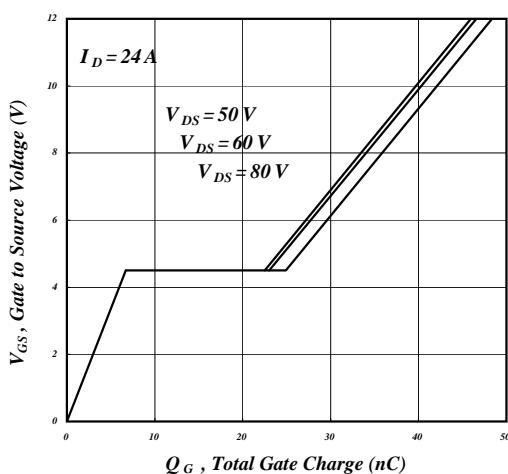


Fig 7. Gate Charge Characteristics

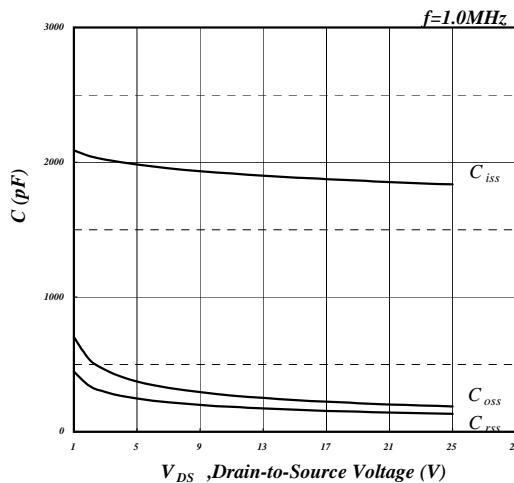


Fig 8. Typical Capacitance Characteristics

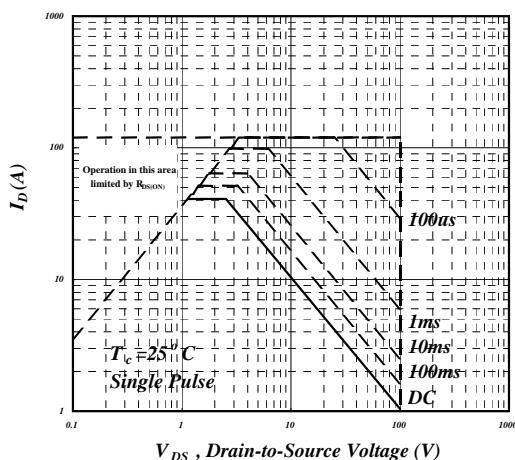


Fig 9. Maximum Safe Operating Area

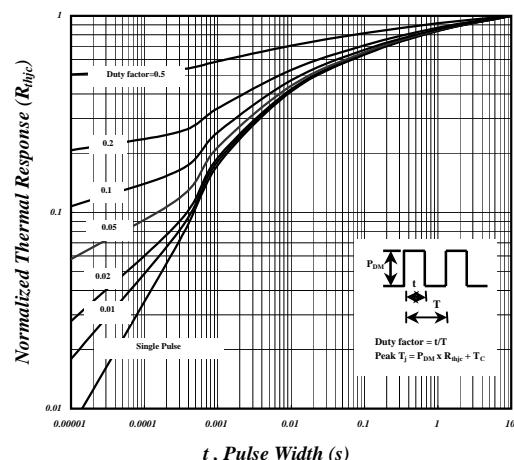


Fig 10. Effective Transient Thermal Impedance

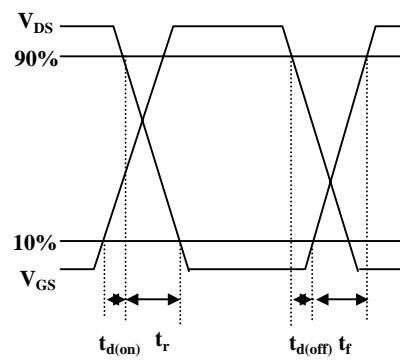


Fig 11. Switching Time Waveforms

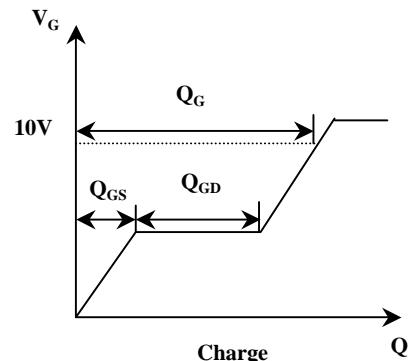
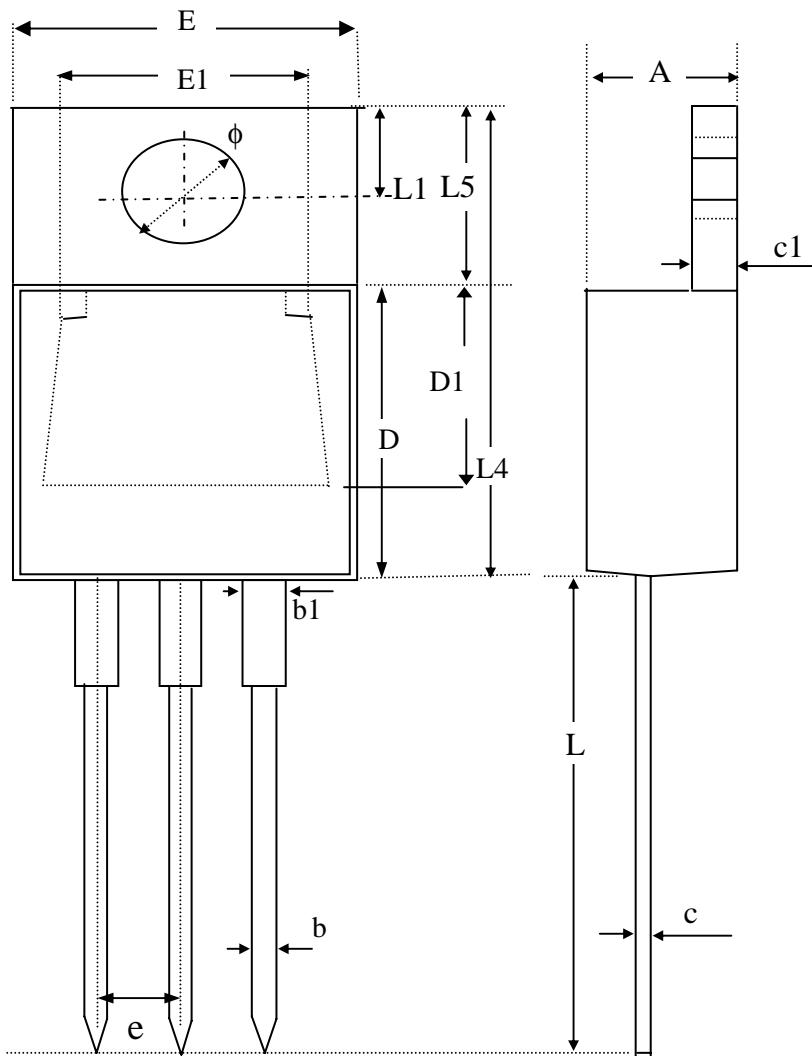


Fig 12. Gate Charge Waveform



Package Dimensions: TO-220



SYMBOLS	Millimeters		
	MIN	NOM	MAX
A	4.40	4.60	4.80
b	0.76	0.88	1.00
D	8.60	8.80	9.00
c	0.36	0.43	0.50
E	9.80	10.10	10.40
L4	14.70	15.00	15.30
L5	6.20	6.40	6.60
D1	5.10 REF.		
c1	1.25	1.35	1.45
b1	1.17	1.32	1.47
L	13.25	13.75	14.25
e	2.54 REF.		
L1	2.60	2.75	2.89
φ	3.71	3.84	3.96
E1	7.4 REF,		

1. All Dimensions Are in Millimeters.

2. Dimension Does Not Include Mold Protrusions.

Marking Information: TO-220

