



**Advanced Power
Electronics Corp.**

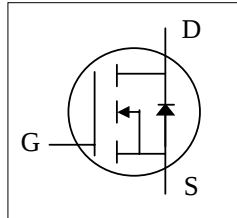
AP60T06GJ-HF

Halogen-Free Product

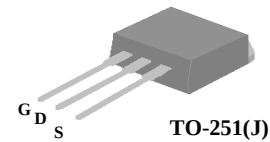
N-CHANNEL ENHANCEMENT MODE

POWER MOSFET

- ▼ Simple Drive Requirement
- ▼ Lower On-resistance
- ▼ Fast Switching Characteristic
- ▼ RoHS Compliant & Halogen-Free



BV_{DS}	55V
$R_{DS(ON)}$	12m Ω
I_D	46A



Description

Advanced Power MOSFETs from APEC provide the designer with the best combination of fast switching, ruggedized device design, low on-resistance and cost-effectiveness.

The TO-251 package is widely preferred for all commercial-industrial through-hole applications and suited for low voltage applications such as DC/DC converters.

Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	55	V
V_{GS}	Gate-Source Voltage	± 20	V
$I_D@T_C=25^\circ\text{C}$	Continuous Drain Current, V_{GS} @ 10V	46	A
$I_D@T_C=100^\circ\text{C}$	Continuous Drain Current, V_{GS} @ 10V	29	A
I_{DM}	Pulsed Drain Current ¹	160	A
$P_D@T_C=25^\circ\text{C}$	Total Power Dissipation	44.6	W
$P_D@T_A=25^\circ\text{C}$	Total Power Dissipation	1.13	W
T_{STG}	Storage Temperature Range	-55 to 150	$^\circ\text{C}$
T_J	Operating Junction Temperature Range	-55 to 150	$^\circ\text{C}$

Thermal Data

Symbol	Parameter	Value	Units
Rthj-c	Maximum Thermal Resistance, Junction-case	2.8	$^\circ\text{C}/\text{W}$
Rthj-a	Maximum Thermal Resistance, Junction-ambient	110	$^\circ\text{C}/\text{W}$

Data and specifications subject to change without notice

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AP60T06GJ-HF

Electrical Characteristics@T_j=25°C(unless otherwise specified)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =250uA	55	-	-	V
R _{DS(ON)}	Static Drain-Source On-Resistance ²	V _{GS} =10V, I _D =30A	-	-	12	mΩ
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _D =250uA	2	-	5	V
g _{fs}	Forward Transconductance	V _{DS} =10V, I _D =20A	-	26	-	S
I _{DSS}	Drain-Source Leakage Current	V _{DS} =44V, V _{GS} =0V	-	-	25	uA
I _{GSS}	Gate-Source Leakage	V _{GS} = ±20V, V _{DS} =0V	-	-	±100	nA
Q _g	Total Gate Charge ²	I _D =20A	-	28	45	nC
Q _{gs}	Gate-Source Charge	V _{DS} =48V	-	6.5	-	nC
Q _{gd}	Gate-Drain ("Miller") Charge	V _{GS} =10V	-	13	-	nC
t _{d(on)}	Turn-on Delay Time ²	V _{DS} =30V	-	11.5	-	ns
t _r	Rise Time	I _D =20A	-	42	-	ns
t _{d(off)}	Turn-off Delay Time	R _G =3.3Ω	-	20	-	ns
t _f	Fall Time	V _{GS} =10V	-	8	-	ns
C _{iss}	Input Capacitance	V _{GS} =0V	-	1380	2200	pF
C _{oss}	Output Capacitance	V _{DS} =25V	-	240	-	pF
C _{rss}	Reverse Transfer Capacitance	f=1.0MHz	-	140	-	pF
R _g	Gate Resistance	f=1.0MHz	-	1	2	Ω

Source-Drain Diode

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
V _{SD}	Forward On Voltage ²	I _S =30A, V _{GS} =0V	-	-	1.3	V
t _{rr}	Reverse Recovery Time ²	I _S =20A, V _{GS} =0V	-	38	-	ns
Q _{rr}	Reverse Recovery Charge	di/dt=100A/μs	-	55	-	nC

Notes:

1.Pulse width limited by Max. junction temperature.

2.Pulse test

THIS PRODUCT IS SENSITIVE TO ELECTROSTATIC DISCHARGE, PLEASE HANDLE WITH CAUTION.

USE OF THIS PRODUCT AS A CRITICAL COMPONENT IN LIFE SUPPORT OR OTHER SIMILAR SYSTEMS IS NOT AUTHORIZED.

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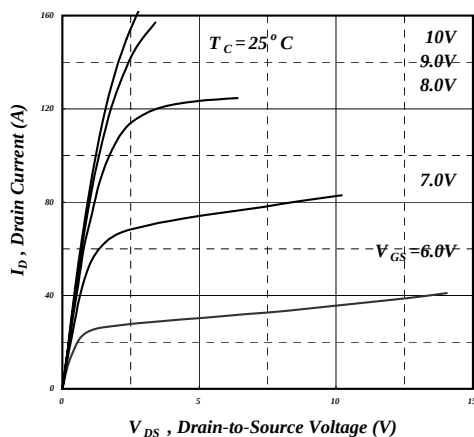


Fig 1. Typical Output Characteristics

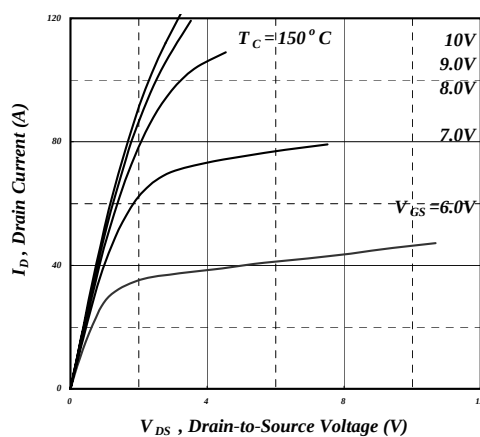


Fig 2. Typical Output Characteristics

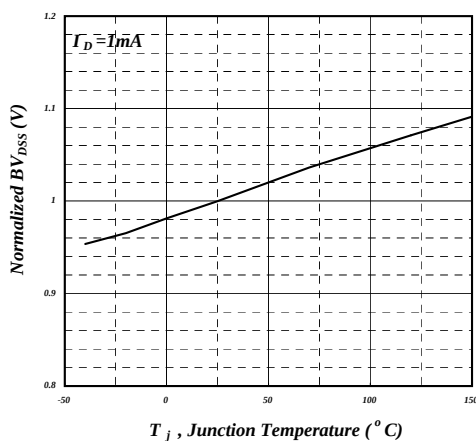


Fig 3. Normalized BV_{DSS} v.s. Junction Temperature

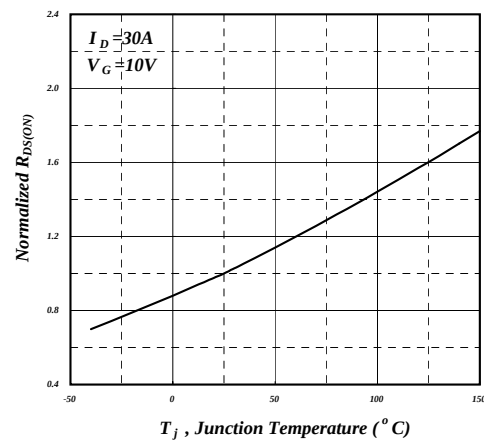


Fig 4. Normalized On-Resistance v.s. Junction Temperature

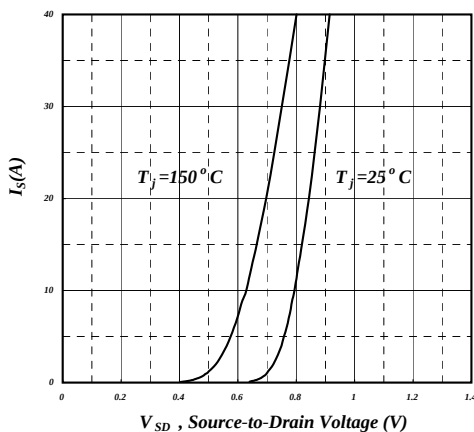


Fig 5. Forward Characteristic of Reverse Diode

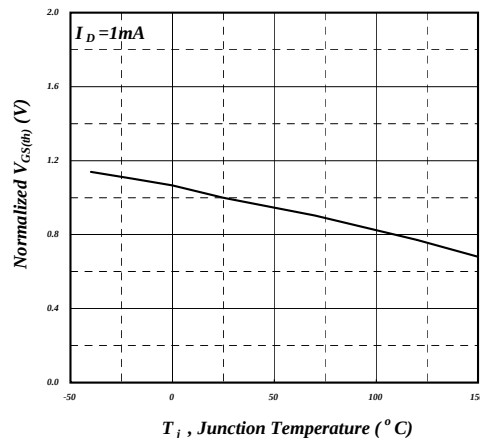


Fig 6. Gate Threshold Voltage v.s. Junction Temperature

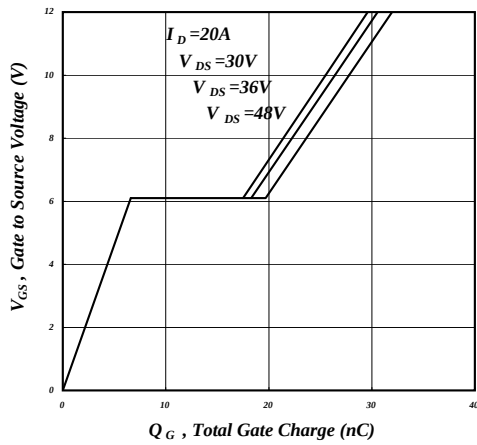


Fig 7. Gate Charge Characteristics

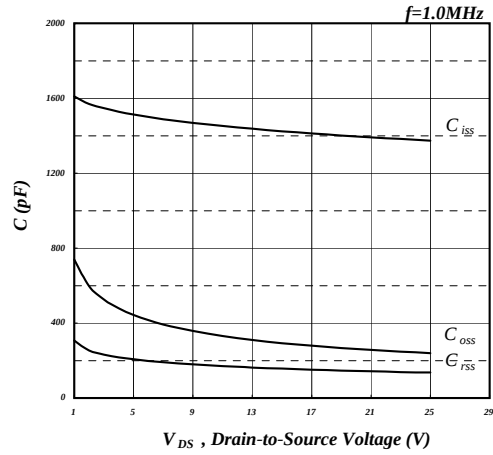


Fig 8. Typical Capacitance Characteristics

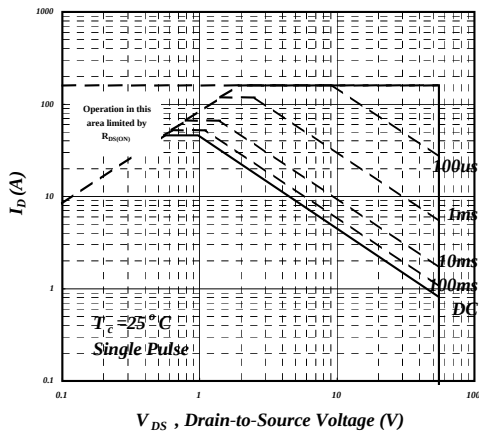


Fig 9. Maximum Safe Operating Area

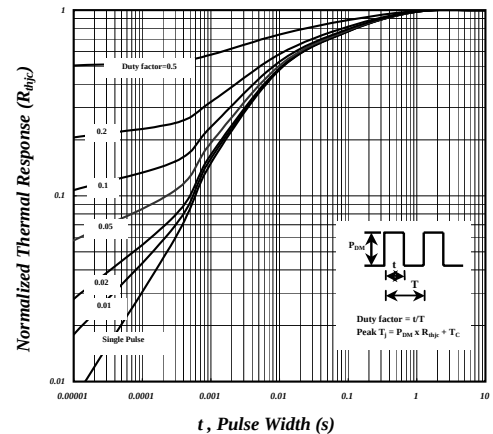


Fig 10. Effective Transient Thermal Impedance

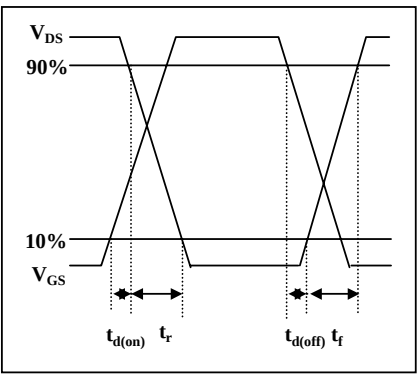


Fig 11. Switching Time Waveform

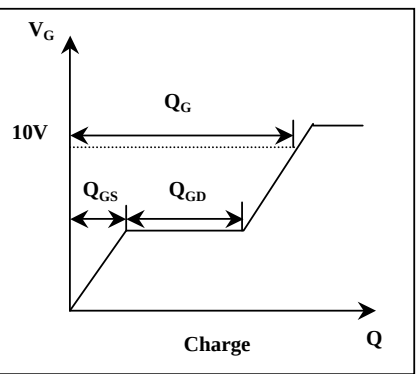


Fig 12. Gate Charge Waveform