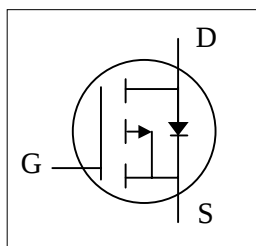




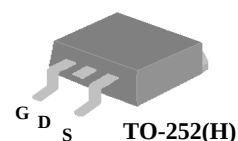
- ▼ Lower On-resistance
- ▼ Simple Drive Requirement
- ▼ Fast Switching Characteristic



BV_{DSS}	-40V
$R_{DS(ON)}$	12.3m Ω
I_D	-60A

Description

Advanced Power MOSFETs from APEC provide the designer with the best combination of fast switching, ruggedized device design, low on-resistance and cost-effectiveness.



The TO-252 package is widely preferred for commercial-industrial surface mount applications and suited for low voltage applications such as DC/DC converters.

Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	-40	V
V_{GS}	Gate-Source Voltage	+20	V
$I_D@T_C=25^{\circ}C$	Continuous Drain Current, V_{GS} @ 10V	-60	A
$I_D@T_C=100^{\circ}C$	Continuous Drain Current, V_{GS} @ 10V	-38	A
I_{DM}	Pulsed Drain Current ¹	-240	A
$P_D@T_C=25^{\circ}C$	Total Power Dissipation	69	W
T_{STG}	Storage Temperature Range	-55 to 150	$^{\circ}C$
T_J	Operating Junction Temperature Range	-55 to 150	$^{\circ}C$

Thermal Data

Symbol	Parameter	Value	Units
R_{thj-c}	Maximum Thermal Resistance Junction-case	1.8	$^{\circ}C/W$
R_{thj-a}	Maximum Thermal Resistance, Junction-ambient (PCB mount) ³	62.5	$^{\circ}C/W$



AP6677GH

Electrical Characteristics@T_j=25°C(unless otherwise specified)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =-250uA	-40	-	-	V
R _{DS(ON)}	Static Drain-Source On-Resistance ²	V _{GS} =-10V, I _D =-30A	-	-	12.3	mΩ
		V _{GS} =-4.5V, I _D =-20A	-	-	18	mΩ
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _D =-250uA	-1	-	-3	V
g _{fs}	Forward Transconductance	V _{DS} =-10V, I _D =-30A	-	46	-	S
I _{DSS}	Drain-Source Leakage Current	V _{DS} =-32V, V _{GS} =0V	-	-	-25	uA
I _{GSS}	Gate-Source Leakage	V _{GS} = ±20V, V _{DS} =0V	-	-	±100	nA
Q _g	Total Gate Charge ²	I _D =-30A	-	44	70	nC
Q _{gs}	Gate-Source Charge	V _{DS} =-32V	-	6	-	nC
Q _{gd}	Gate-Drain ("Miller") Charge	V _{GS} =-4.5V	-	28	-	nC
t _{d(on)}	Turn-on Delay Time ²	V _{DS} =-20V	-	11	-	ns
t _r	Rise Time	I _D =-30A	-	60	-	ns
t _{d(off)}	Turn-off Delay Time	R _G =3.3Ω, V _{GS} =-10V	-	60	-	ns
t _f	Fall Time	R _D =0.67Ω	-	120	-	ns
C _{iss}	Input Capacitance	V _{GS} =0V	-	3160	5050	pF
C _{oss}	Output Capacitance	V _{DS} =-25V	-	560	-	pF
C _{rss}	Reverse Transfer Capacitance	f=1.0MHz	-	380	-	pF

Source-Drain Diode

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
V _{SD}	Forward On Voltage ²	I _S =-30A, V _{GS} =0V	-	-	-1.2	V
t _{rr}	Reverse Recovery Time ²	I _S =-10A, V _{GS} =0V,	-	42	-	ns
Q _{rr}	Reverse Recovery Charge	dI/dt=-100A/μs	-	46	-	nC

Notes:

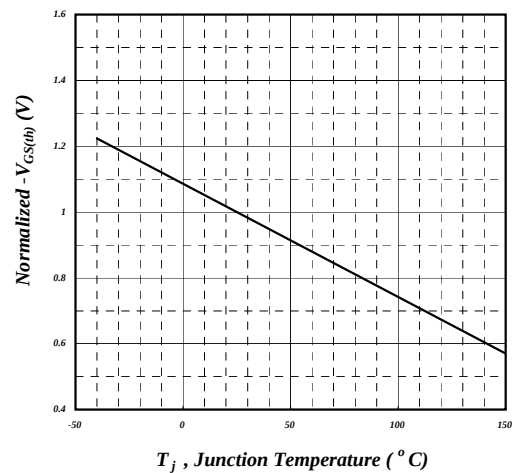
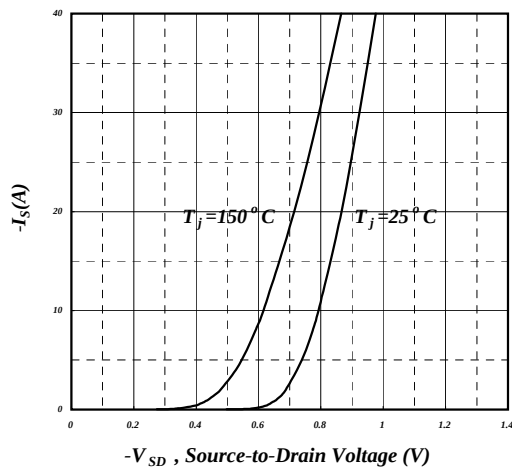
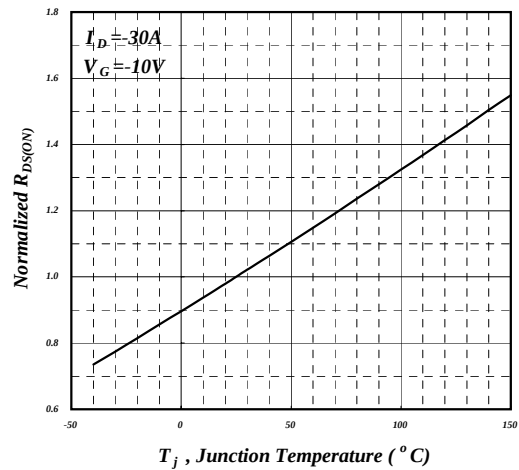
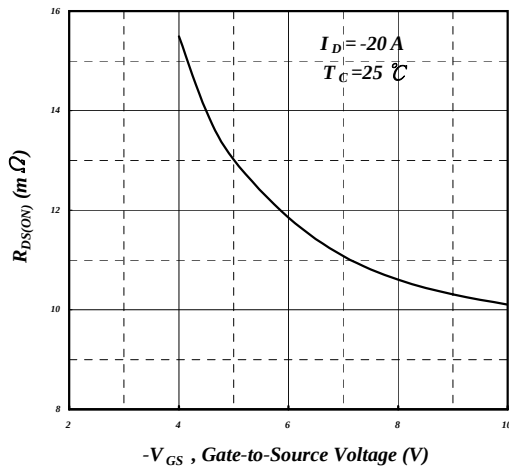
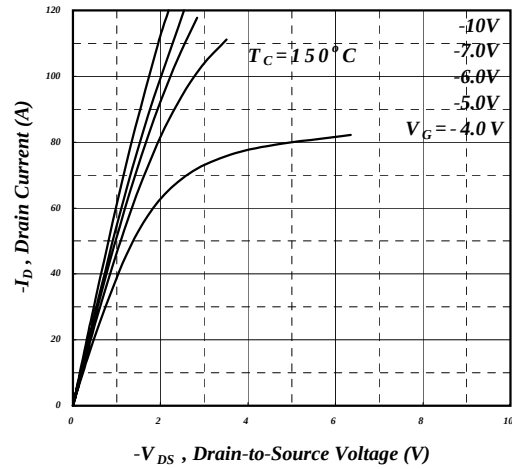
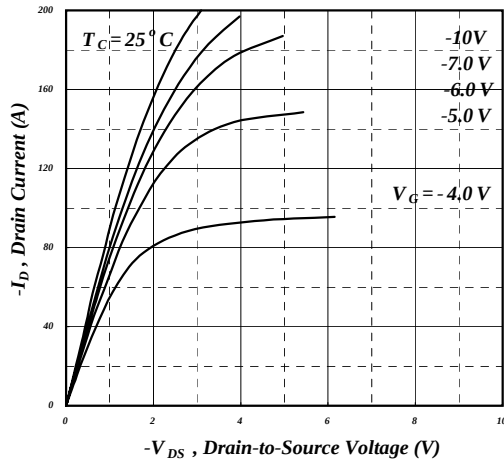
- 1.Pulse width limited by Max. junction temperature.
- 2.Pulse test
- 3.Surface mounted on 1 in² copper pad of FR4 board

THIS PRODUCT IS SENSITIVE TO ELECTROSTATIC DISCHARGE, PLEASE HANDLE WITH CAUTION.

USE OF THIS PRODUCT AS A CRITICAL COMPONENT IN LIFE SUPPORT OR OTHER SIMILAR SYSTEMS IS NOT AUTHORIZED.

APEC DOES NOT ASSUME ANY LIABILITY ARISING OUT OF THE APPLICATION OR USE OF ANY PRODUCT OR CIRCUIT DESCRIBED HEREIN; NEITHER DOES IT CONVEY ANY LICENSE UNDER ITS PATENT RIGHTS, NOR THE RIGHTS OF OTHERS.

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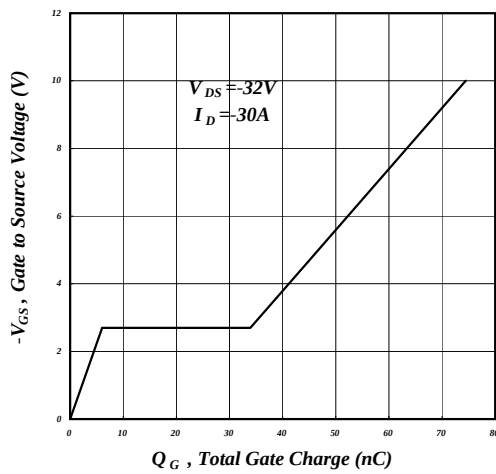


Fig 7. Gate Charge Characteristics

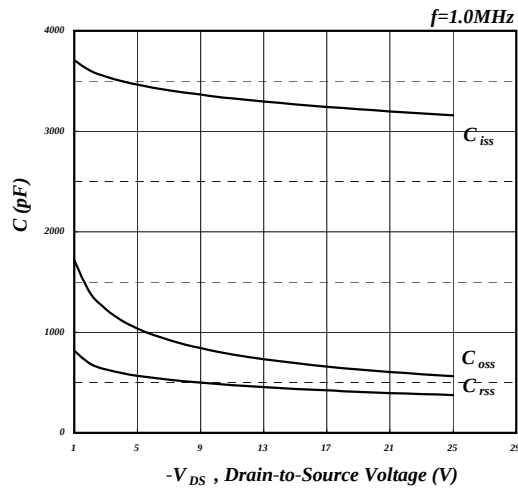


Fig 8. Typical Capacitance Characteristics

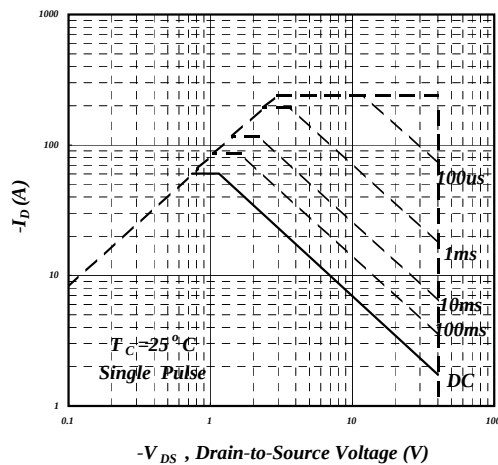


Fig 9. Maximum Safe Operating Area

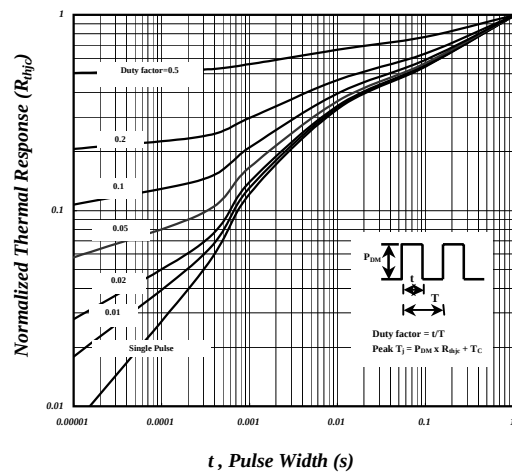


Fig 10. Effective Transient Thermal Impedance

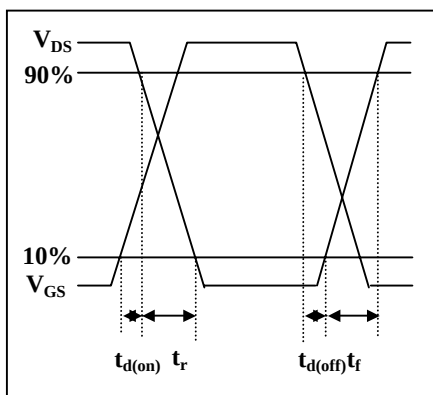


Fig 11. Switching Time Waveform

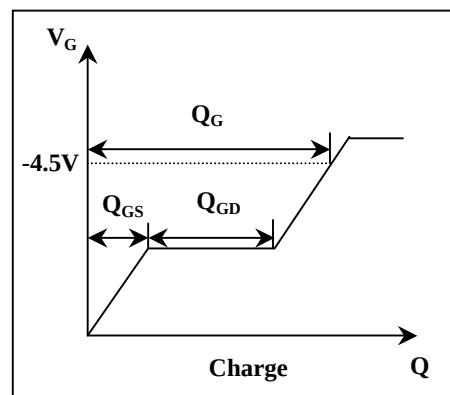
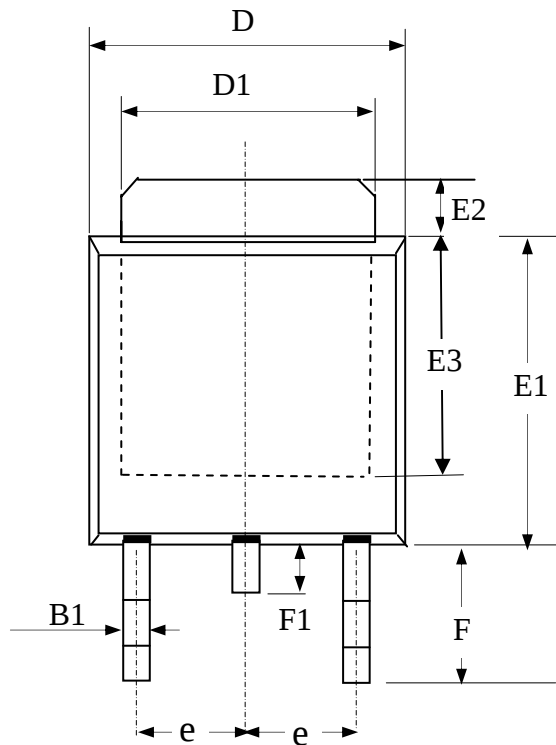


Fig 12. Gate Charge Waveform



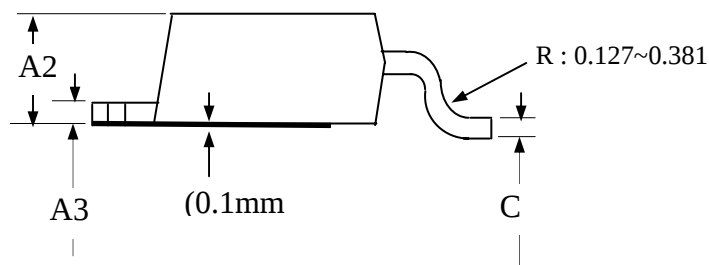
Package Outline : TO-252



SYMBOLS	Millimeters		
	MIN	NOM	MAX
A2	1.80	2.30	2.80
A3	0.40	0.50	0.60
B1	0.40	0.70	1.00
D	6.00	6.50	7.00
D1	4.80	5.35	5.90
E3	3.50	4.00	4.50
F	2.20	2.63	3.05
F1	0.50	0.85	1.20
E1	5.10	5.70	6.30
E2	0.50	1.10	1.80
e	--	2.30	--
C	0.35	0.50	0.65

1.All Dimensions Are in Millimeters.

2.Dimension Does Not Include Mold Protrusions.



Part Marking Information & Packing : TO-252

Laser Marking

