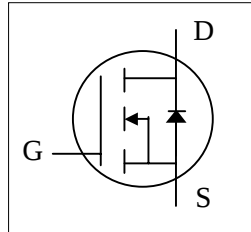




- ▼ Low Gate Charge
- ▼ Simple Drive Requirement
- ▼ Fast Switching

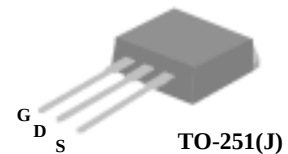
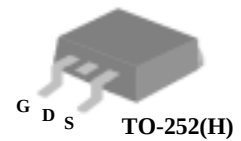


BV_{DSS}	25V
$R_{DS(ON)}$	$6m\Omega$
I_D	75A

Description

AP88LS02 used advanced design and process to achieve low gate charge, lower on-resistance and fast switching performance.

The TO-252 package is universally preferred for all commercial-industrial surface mount applications and suited for low voltage applications such as DC/DC converters. The through-hole version (AP85L02J-A) is available for low-profile applications.



Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	25	V
V_{GS}	Gate-Source Voltage	± 20	V
$I_D@T_C=25^\circ C$	Continuous Drain Current, V_{GS} @ 4.5V	75	A
$I_D@T_C=100^\circ C$	Continuous Drain Current, V_{GS} @ 4.5V	64	A
I_{DM}	Pulsed Drain Current ¹	350	A
$P_D@T_C=25^\circ C$	Total Power Dissipation	96	W
	Linear Derating Factor	0.75	W/ $^\circ C$
T_{STG}	Storage Temperature Range	-55 to 150	$^\circ C$
T_J	Operating Junction Temperature Range	-55 to 150	$^\circ C$

Thermal Data

Symbol	Parameter	Value	Units
R_{thj-c}	Thermal Resistance Junction-case Max.	1.3	$^\circ C/W$
R_{thj-a}	Thermal Resistance Junction-ambient Max.	110	$^\circ C/W$



AP85L02H/J-A

Electrical Characteristics@T_j=25°C(unless otherwise specified)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =250uA	25	-	-	V
$\Delta BV_{DSS}/\Delta T_j$	Breakdown Voltage Temperature Coefficient	Reference to 25°C, I _D =1mA	-	0.02	-	V/°C
R _{DS(ON)}	Static Drain-Source On-Resistance ²	V _{GS} =10V, I _D =45A	-	-	6	mΩ
		V _{GS} =4.5V, I _D =30A	-	-	8	mΩ
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _D =250uA	1	-	3	V
g _{fs}	Forward Transconductance	V _{DS} =10V, I _D =30A	-	29	-	S
I _{DSS}	Drain-Source Leakage Current (T _j =25°C)	V _{DS} =25V, V _{GS} =0V	-	-	1	uA
	Drain-Source Leakage Current (T _j =150°C)	V _{DS} =20V, V _{GS} =0V	-	-	100	uA
I _{GSS}	Gate-Source Leakage	V _{GS} =±20V	-	-	±100	nA
Q _g	Total Gate Charge ²	I _D =30A	-	28	45	nC
Q _{gs}	Gate-Source Charge	V _{DS} = 20V	-	5	-	nC
Q _{gd}	Gate-Drain ("Miller") Charge	V _{GS} =4.5V	-	19	-	nC
t _{d(on)}	Turn-on Delay Time ²	V _{DS} =15V	-	10	-	ns
t _r	Rise Time	I _D =30A	-	80	-	ns
t _{d(off)}	Turn-off Delay Time	R _G =3.3Ω, V _{GS} =10V	-	37	-	ns
t _f	Fall Time	R _D =0.5Ω	-	85	-	ns
C _{iss}	Input Capacitance	V _{GS} =0V	-	2070	3310	pF
C _{oss}	Output Capacitance	V _{DS} =25V	-	990	-	pF
C _{rss}	Reverse Transfer Capacitance	f=1.0MHz	-	300	-	pF

Source-Drain Diode

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
V _{SD}	Forward On Voltage ²	I _S =45A, V _{GS} =0V	-	-	1.3	V
t _{rr}	Reverse Recovery Time ²	I _S =30A, V _{GS} =0V,	-	50	-	ns
Q _{rr}	Reverse Recovery Charge	dI/dt=100A/μs	-	51	-	nC

Notes:

- 1.Pulse width limited by safe operating area.
- 2.Pulse width ≤300us , duty cycle ≤2%.

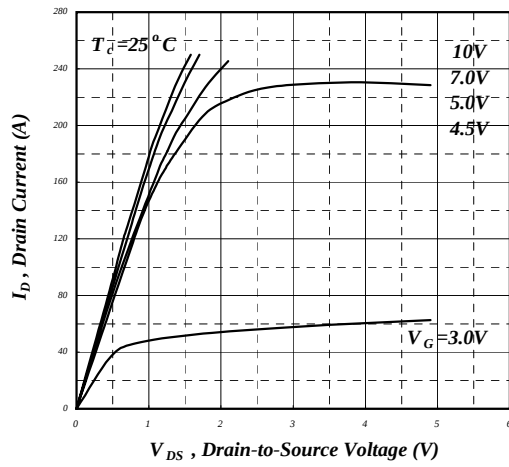


Fig 1. Typical Output Characteristics

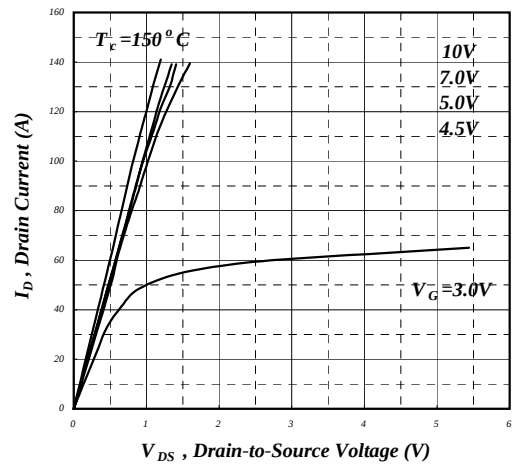


Fig 2. Typical Output Characteristics

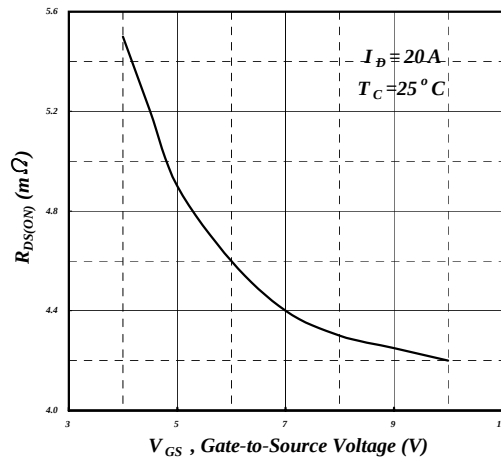


Fig 3. On-Resistance v.s. Gate Voltage

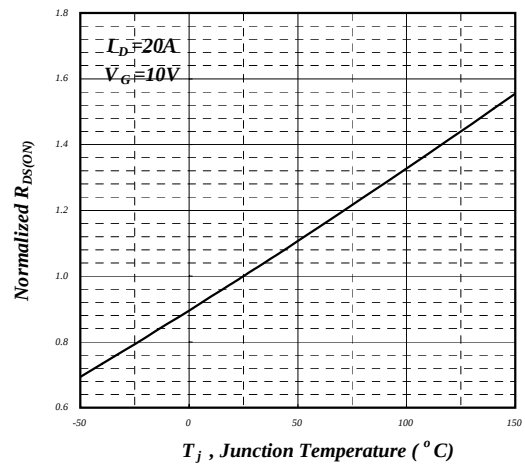


Fig 4. Normalized On-Resistance v.s. Junction Temperature

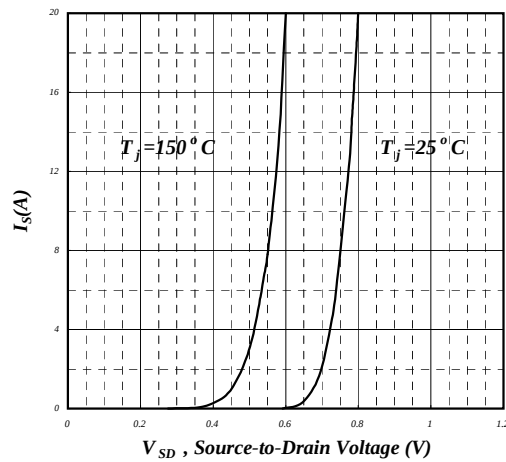


Fig 5. Forward Characteristic of Reverse Diode

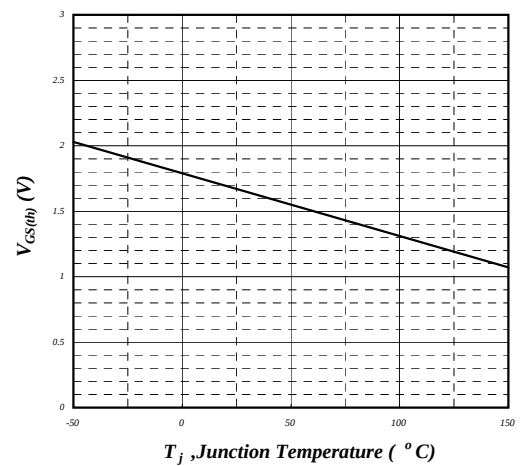


Fig 6. Gate Threshold Voltage v.s. Junction Temperature



AP85L02H/J-A

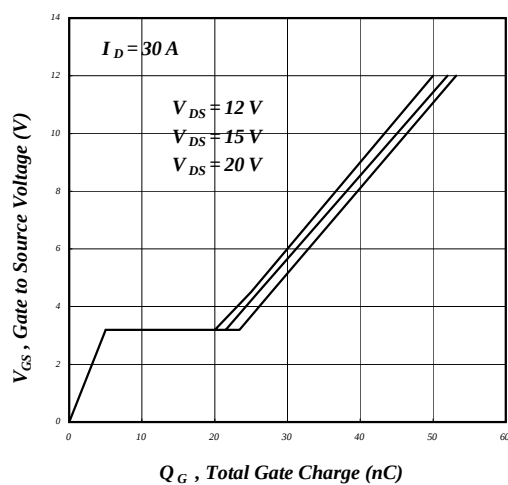


Fig 7. Gate Charge Characteristics

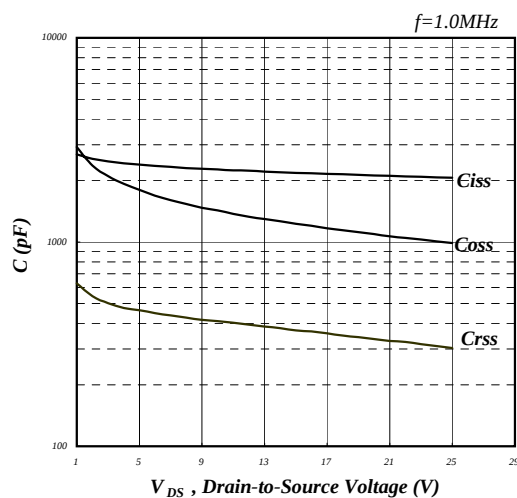


Fig 8. Typical Capacitance Characteristics

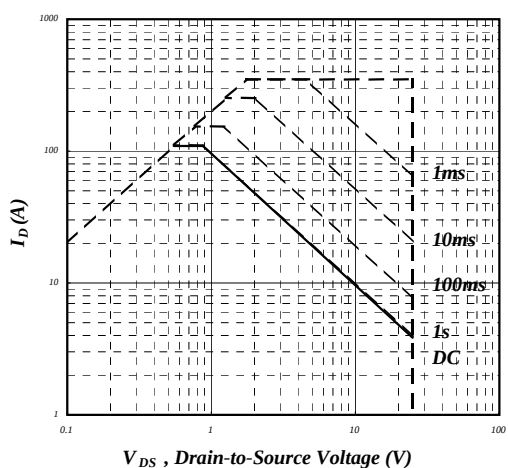


Fig 9. Maximum Safe Operating Area

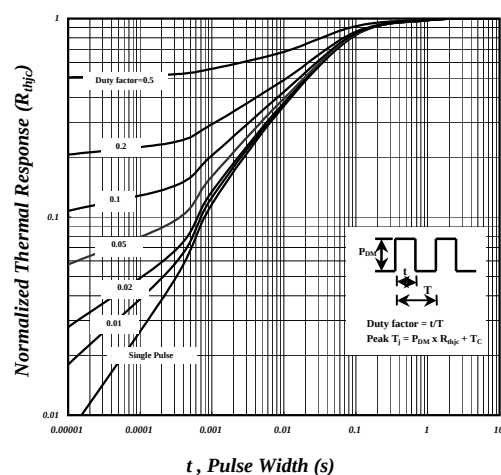


Fig 10. Effective Transient Thermal Impedance

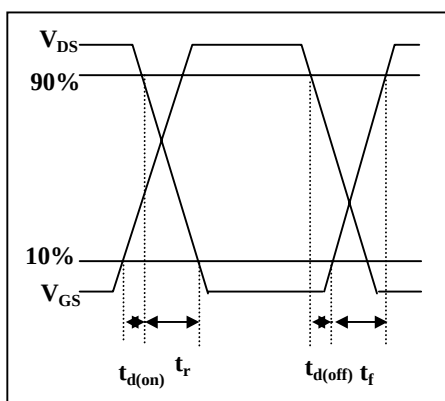


Fig 11. Switching Time Waveform

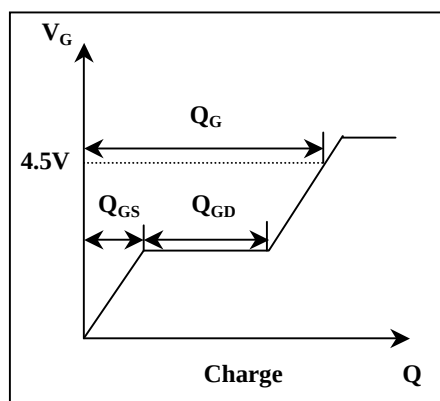


Fig 12. Gate Charge Waveform