



**Advanced Power
Electronics Corp.**

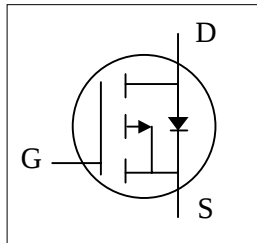
AP9575AGJ

RoHS-compliant Product

P-CHANNEL ENHANCEMENT MODE

POWER MOSFET

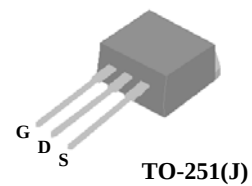
- ▼ Higher Gate-Source Voltage
- ▼ Simple Drive Requirement
- ▼ Fast Switching Characteristic



BV_{DSS}	-60V
$R_{DS(ON)}$	64m Ω
I_D	-17A

Description

The TO-251 package is universally preferred for all commercial-industrial applications and suited for low voltage applications such as DC/DC converters.



Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	-60	V
V_{GS}	Gate-Source Voltage	± 30	V
$I_D@T_C=25^\circ\text{C}$	Continuous Drain Current, V_{GS} @ 10V	-17	A
$I_D@T_C=100^\circ\text{C}$	Continuous Drain Current, V_{GS} @ 10V	-11	A
I_{DM}	Pulsed Drain Current ¹	-60	A
$P_D@T_C=25^\circ\text{C}$	Total Power Dissipation	36	W
	Linear Derating Factor	0.29	W/ $^\circ\text{C}$
T_{STG}	Storage Temperature Range	-55 to 150	$^\circ\text{C}$
T_J	Operating Junction Temperature Range	-55 to 150	$^\circ\text{C}$

Thermal Data

Symbol	Parameter	Value	Units
Rthj-c	Thermal Resistance Junction-case Max.	3.5	$^\circ\text{C}/\text{W}$
Rthj-a	Thermal Resistance Junction-ambient Max.	110	$^\circ\text{C}/\text{W}$



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Electrical Characteristics@T_j=25°C(unless otherwise specified)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =-250uA	-60	-	-	V
R _{DS(ON)}	Static Drain-Source On-Resistance ²	V _{GS} =-10V, I _D =-12A	-	-	64	mΩ
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _D =-250uA	-1	-	-3	V
g _{fs}	Forward Transconductance	V _{DS} =-10V, I _D =-9A	-	8.8	-	S
I _{DSS}	Drain-Source Leakage Current (T _j =25°C)	V _{DS} =-60V, V _{GS} =0V	-	-	-10	uA
	Drain-Source Leakage Current (T _j =150°C)	V _{DS} =-48V, V _{GS} =0V	-	-	-25	uA
I _{GSS}	Gate-Source Leakage	V _{GS} = ±30V	-	-	±100	nA
Q _g	Total Gate Charge ²	I _D =-9A	-	25	40	nC
Q _{gs}	Gate-Source Charge	V _{DS} =-48V	-	3.4	-	nC
Q _{gd}	Gate-Drain ("Miller") Charge	V _{GS} =-10V	-	10	-	nC
t _{d(on)}	Turn-on Delay Time ²	V _{DS} =-30V	-	11	-	ns
t _r	Rise Time	I _D =-9A	-	38	-	ns
t _{d(off)}	Turn-off Delay Time	R _G =3.3Ω, V _{GS} =-10V	-	26	-	ns
t _f	Fall Time	R _D =3.3Ω	-	64	-	ns
C _{iss}	Input Capacitance	V _{GS} =0V	-	970	1550	pF
C _{oss}	Output Capacitance	V _{DS} =-25V	-	150	-	pF
C _{rss}	Reverse Transfer Capacitance	f=1.0MHz	-	115	-	pF

Source-Drain Diode

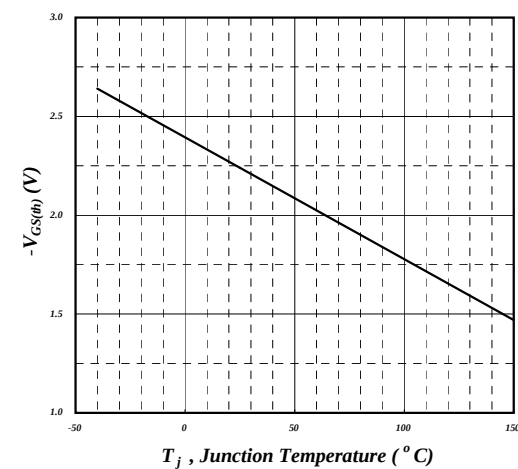
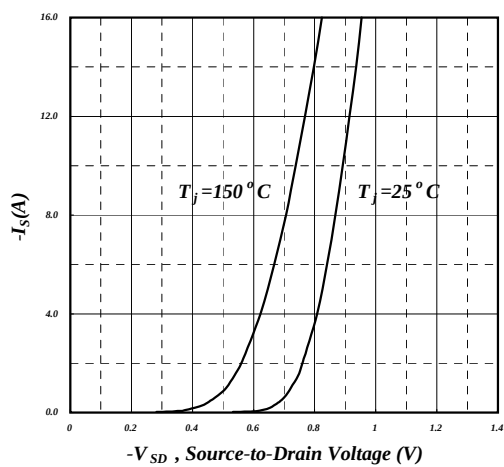
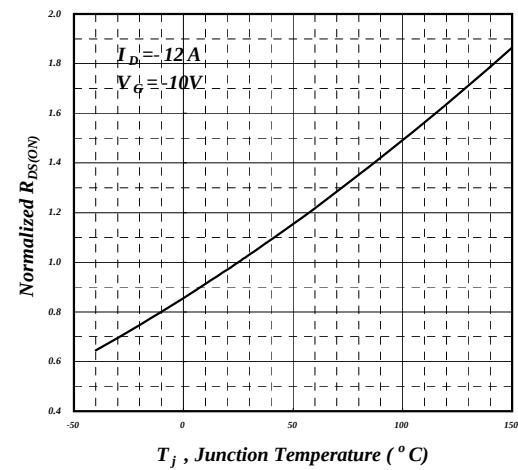
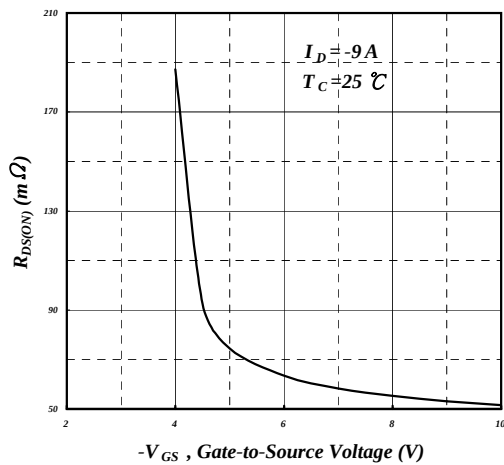
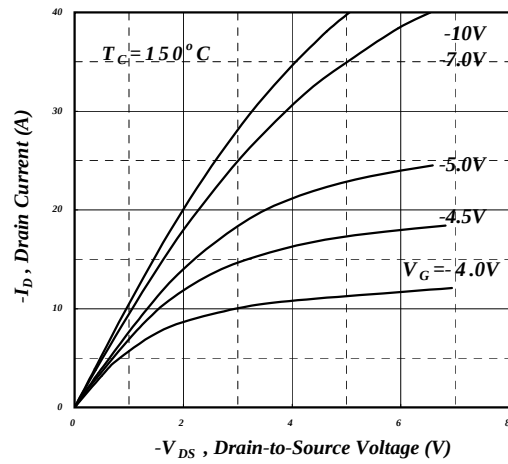
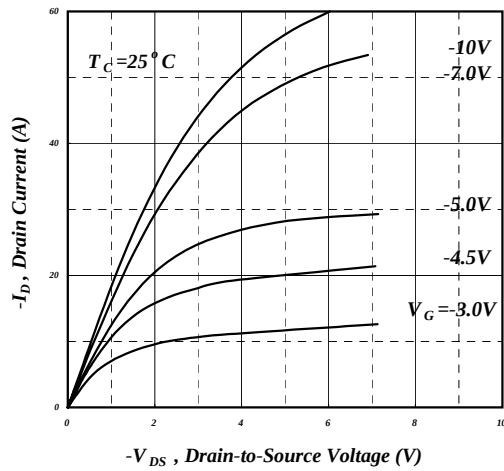
Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
V _{SD}	Forward On Voltage ²	I _S =-9A, V _{GS} =0V	-	-	-1.2	V
t _{rr}	Reverse Recovery Time ²	I _S =-9A, V _{GS} =0V,	-	35	-	ns
Q _{rr}	Reverse Recovery Charge	dI/dt=-100A/μs	-	50	-	nC

Notes:

- 1.Pulse width limited Max. junction temperature.
- 2.Pulse test

THIS PRODUCT IS AN ELECTROSTATIC SENSITIVE, PLEASE HANDLE WITH CAUTION.

THIS PRODUCT HAS BEEN QUALIFIED FOR CONSUMER MARKET. APPLICATIONS OR USES AS CRITERIAL COMPONENT IN LIFE SUPPORT DEVICE OR SYSTEM ARE NOT AUTHORIZED.



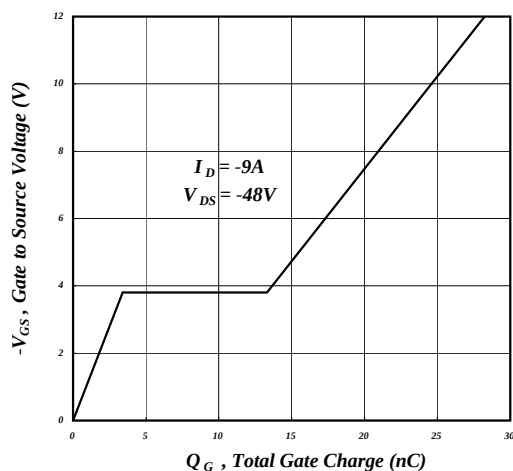


Fig 7. Gate Charge Characteristics

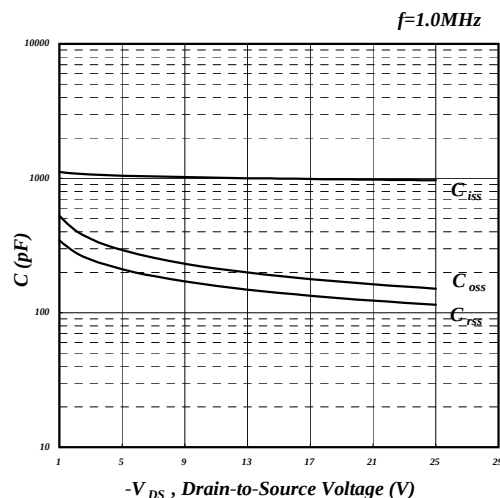


Fig 8. Typical Capacitance Characteristics

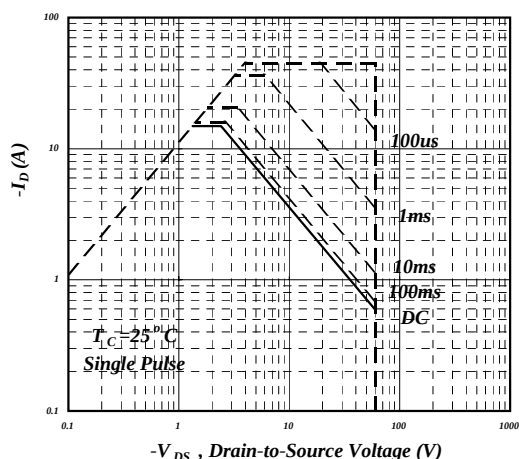


Fig 9. Maximum Safe Operating Area

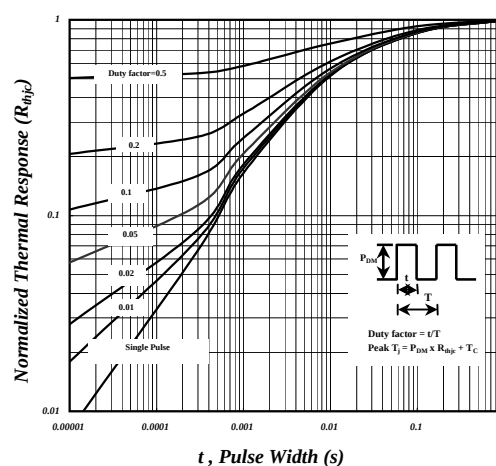


Fig 10. Effective Transient Thermal Impedance

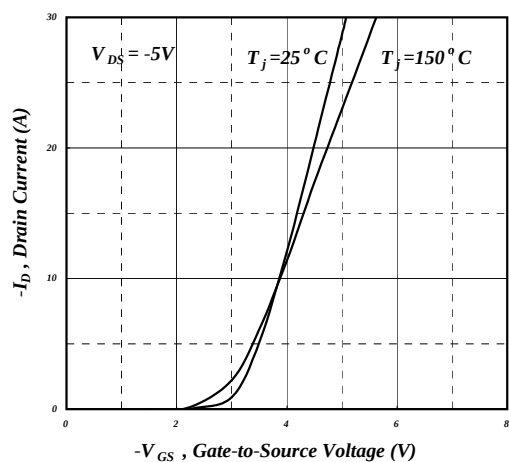


Fig 11. Transfer Characteristics

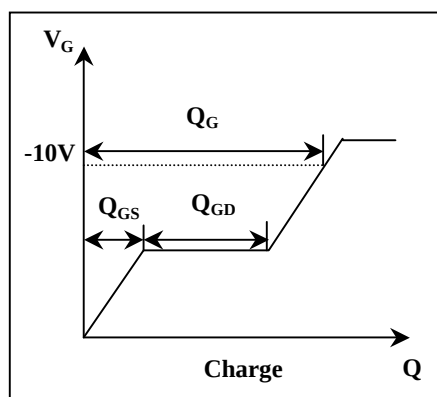
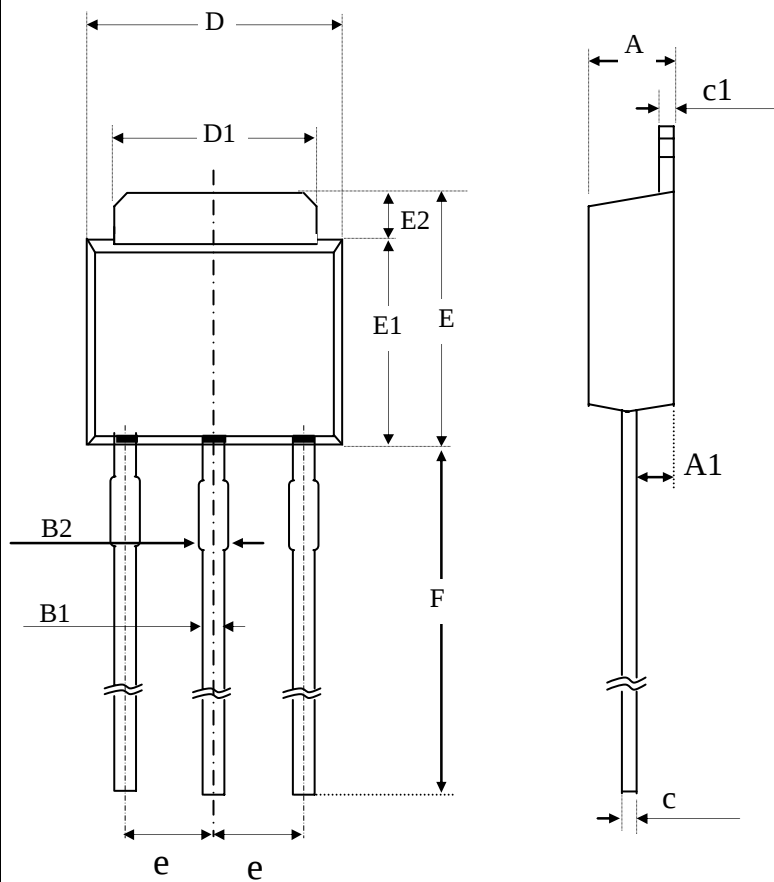


Fig 12. Gate Charge Waveform



ADVANCED POWER ELECTRONICS CORP.



SYMBOLS	Millimeters		
	MIN	NOM	MAX
	Original	Original	Original
A	2.10	2.30	2.50
A1	0.60	1.20	1.80
B1	0.40	0.60	0.80
B2	0.60	0.95	1.25
c	0.40	0.50	0.65
c1	0.40	0.55	0.70
D	6.00	6.50	7.00
D1	4.80	5.40	5.90
E1	5.00	5.50	6.00
E2	1.20	1.70	2.20
e	----	2.30	----
F	7.00	---	16.70

1.All Dimensions Are in Millimeters.

2.Dimension Does Not Include Mold Protrusions.

Part Marking Information & Packing : TO-251

