ADSL CPE Hybrid

APC78106

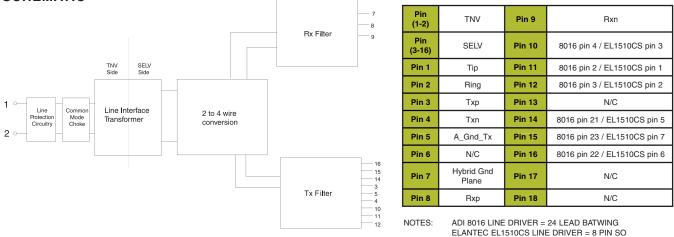
ARCIBIOS

OPERATING TEMPERATURE -40°C TO +85°C

FEATURES

- APC78106 is designed to interface with Alcatel's MTK20150 and MTK20141 ADSL transceiver chip sets to optimize customer premises equipment (CPE) designs. The Hybrid contains every component required between the analog front end chip and the connector including
- the line interface transformer and common mode chokes, line protection circuitry, transmit and receive filters (excluding line driver and parallel capacitors).
- The hybrid was designed jointly by APC and Alcatel to optimize the transmission capabilities of the "Dynamite" chip set. It meets or exceeds the requirements of the standards as specified in TS101 388, T1.413 and ETR328. It also provides BS EN60950 / IEC60950 / TS 001 / AS/NZS 3260 / CAN/ CSA C22.2 Np. 950-95 / UL1950 and CB Certificate supplementary insulation and UL Component Recognition (File No. E175058).

SCHEMATIC



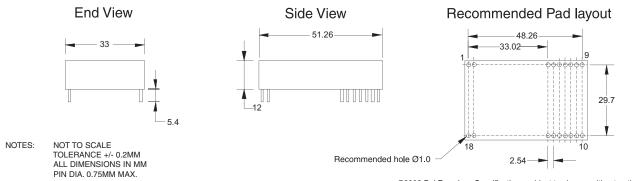
1. ALL TRACES BETWEEN APC PINS 10 AND 14 AND ADI LINE DRIVER PINS 4 AND 21 RESPECTIVELY, MUST BE KEPT SYMMETRICAL, PARALLEL AND EQUIDISTANT. 2. TWO 4.7NF PARALLEL CAPACITORS SHOULD BE PLACED BETWEEN ADI LINE DRIVER PIN 4 AND AGT (A_GND_TX) AND BETWEEN ADI LINE DRIVER PIN 21 AND AGT (A_GND_TX). THESE NEED TO BE PLACED AS CLOSE TO THE ADI LINE DRIVER AS POSSIBLE.

SAFETY NOTE:

THE CIRCUIT ON THE LC SIDE OF THE TRANSFORMER IS A SELV CIRCUIT AND THE CIRCUIT ON THE LINE SIDE OF THE TRANSFORMER IS A TNV CIRCUIT. - CIRCUITRY DESIGNATED AS SELV SHALL ONLY BE CONNECTED TO OTHER SELV CIRCUITS (UNDER NORMAL AND SINGLE FAULT CONDITIONS) IN END INSTALLATION

- DO NOT PLACE HYBRID AGAINST OTHER COMPONENT PARTS ATTAINING EXCESSIVE TEMPERATURES.

MECHANICAL



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