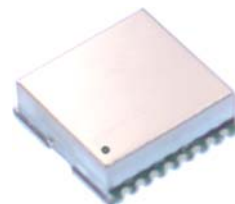


Features

- +1 dBm Output Level at 2482MHz
- 2nd Harmonic : < -20 dBc
- Spurious Level : < -70 dBc
- Lock Time : < 10ms
- 30 mA Current Consumption
- Strip-line Resonator

Description

The plerow™ PLL synthesizer module was designed for use in wireless and wireline systems in a wide range of frequency from 50 MHz to 6 GHz. ASB's PLL provides exceptionally low spurious and phase noise performance with fast locking time and low current consumption. All products are available in a surface-mount type package.



Specifications

Parameter	Unit	Min.	Typical	Max.
Frequency Range	MHz		2482	
Output Power	dBm	-1	+1	+3
Supply Voltage	V	3.1	3.3	3.5
Current Consumption	mA		26	30
Channel Step Size	kHz		-	
2 nd Harmonics	dBc		-25	-20
Spurious Level	dBc		-70	-65
Lock Time	ms		3	10
Reference Frequency	MHz		-	
Reference Input Level	dBm		-	
Phase Noise (C / N)	@ 1 kHz	dBc/Hz	-73	-65
	@ 10 kHz		-101	-95
	@ 100 kHz		-118	-115
Output Impedance	Ω		50	
Operating Temp. Range	°C	-40	+25	+85
Package Type & Size	mm	SMT, 19.0W×19.0L×5.8H		

1) Measurement conditions are as follows: T = 25°C, V_{CC} = 5.0 V, Freq. = 2482MHz, 50 ohm system.

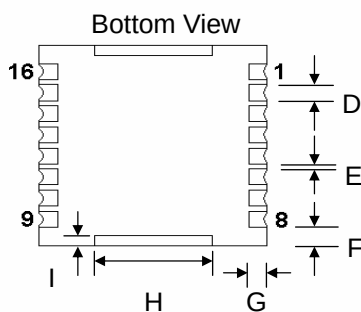
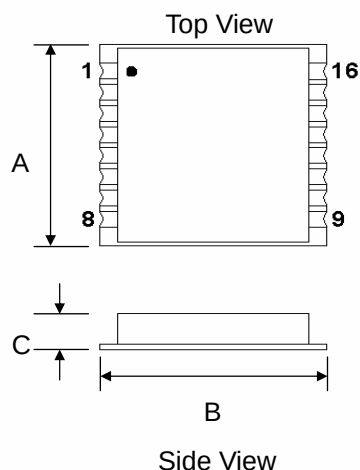
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Outline Drawing



Pin Configuration	
1	Ground
2	Ground
3	Ground
4	Ground
9	VCC (VCO)
13	RF OUT
15	VCP (PLL)
16	LOCK DETECT
Others	Ground

Dimension (mm)	
A	19.0
B	19.0
C	5.8
D	1.5
E	0.5
F	1.75
G	1.35
H	15.0
I	0.9
Tolerance: ± 0.2	