

Absolute Maximum Ratings

Symbol	Parameter		Rating	Unit
Common Ratings (T _A =25°C Unless Otherwise Noted)				
V _{DSS}	Drain-Source Voltage		30	V
V _{GSS}	Gate-Source Voltage		±20	
T _J	Maximum Junction Temperature		150	°C
T _{STG}	Storage Temperature Range		-55 to 150	°C
I _S	Diode Continuous Forward Current		40	A
I _{DP}	300μs Pulse Drain Current Tested	T _C =25°C	200	A
		T _C =100°C	130	
I _D	Continuous Drain Current	T _C =25°C	*75	A
		T _C =100°C	50	
P _D	Maximum Power Dissipation	T _C =25°C	50	W
		T _C =100°C	20	
R _{θJC}	Thermal Resistance-Junction to Case		2.5	°C/W
R _{θJA}	Thermal Resistance-Junction to Ambient		50	°C/W
E _{AS}	Drain-Source Avalanche Energy, L=0.5mH		300	mJ

Notes:

* Current limited by bond wire.

Electrical Characteristics ($T_A = 25^{\circ}\text{C}$ Unless Otherwise Noted)

Symbol	Parameter	Test Condition	APM3004NU			Unit
			Min.	Typ.	Max.	
Static Characteristics						
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _{DS} =250μA	30			V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} =24V, V _{GS} =0V			1	μA
			T _J =85°C		30	
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _{DS} =250μA	1.3	1.8	2.5	V
I _{GSS}	Gate Leakage Current	V _{GS} =±20V, V _{DS} =0V			±100	nA
R _{DS(ON)} ^a	Drain-Source On-state Resistance	V _{GS} =10V, I _{DS} =40A		3.3	4	mΩ
		V _{GS} =4.5V, I _{DS} =20A		4.5	5.9	
Diode Characteristics						
V _{SD} ^a	Diode Forward Voltage	I _{SD} =30A, V _{GS} =0V		0.8	1.1	V
t _{rr}	Reverse Recovery Time	I _{DS} =40A, dI _{SD} /dt=100A/μs		37		ns
Q _{rr}	Reverse Recovery Charge			26		nC

Electrical Characteristics (Cont.) (T_A = 25°C Unless Otherwise Noted)

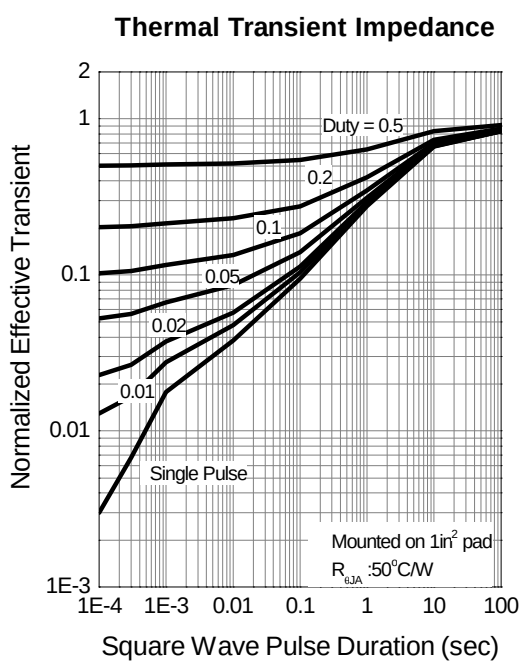
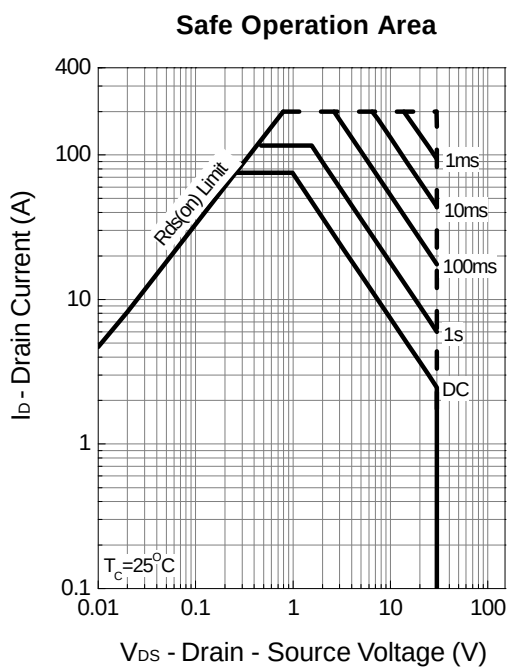
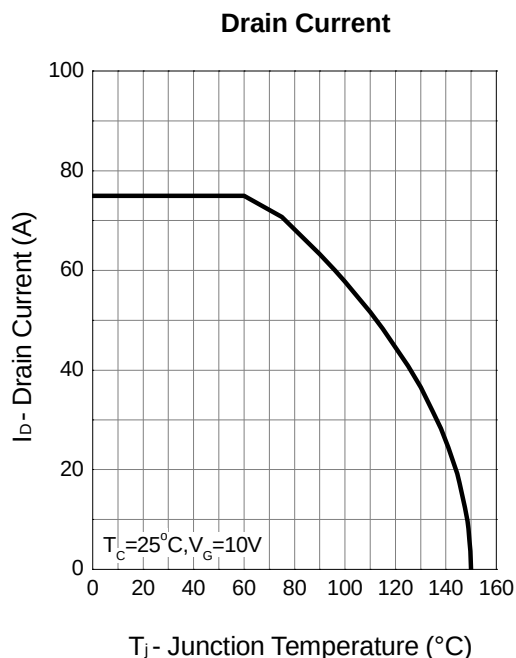
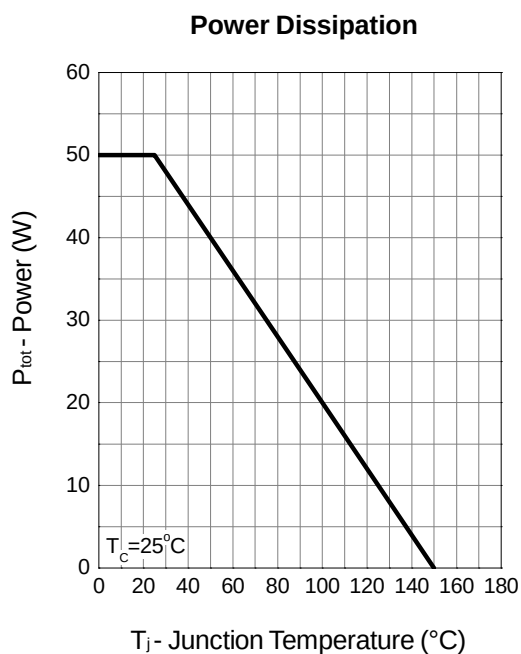
Symbol	Parameter	Test Condition	APM3004NU			Unit
			Min.	Typ.	Max.	
Dynamic Characteristics ^b						
R _G	Gate Resistance	V _{GS} =0V,V _{DS} =0V,F=1MHz		1.6		Ω
C _{iss}	Input Capacitance	V _{GS} =0V, V _{DS} =15V, Frequency=1.0MHz		5420		pF
C _{oss}	Output Capacitance			925		
C _{rss}	Reverse Transfer Capacitance			665		
t _{d(ON)}	Turn-on Delay Time	V _{DD} =15V, R _L =15Ω, I _{DS} =1A, V _{GEN} =10V, R _G =6Ω		22	41	ns
t _r	Turn-on Rise Time			21	39	
t _{d(OFF)}	Turn-off Delay Time			117	212	
t _f	Turn-off Fall Time			52	95	
Gate Charge Characteristics ^b						
Q _g	Total Gate Charge	V _{DS} =15V, V _{GS} =4.5V, I _{DS} =40A		50	70	nC
Q _{gs}	Gate-Source Charge			18		
Q _{gd}	Gate-Drain Charge			24		

Notes:

a : Pulse test ; pulse width≤300μs, duty cycle≤2%.

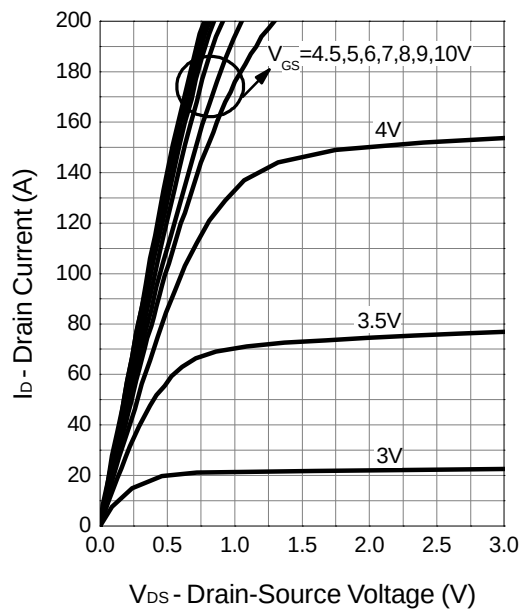
b : Guaranteed by design, not subject to production testing.

Typical Characteristics

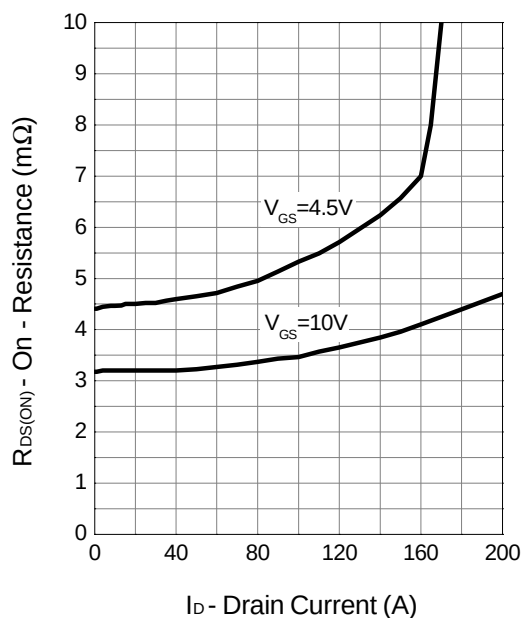


Typical Characteristics (Cont.)

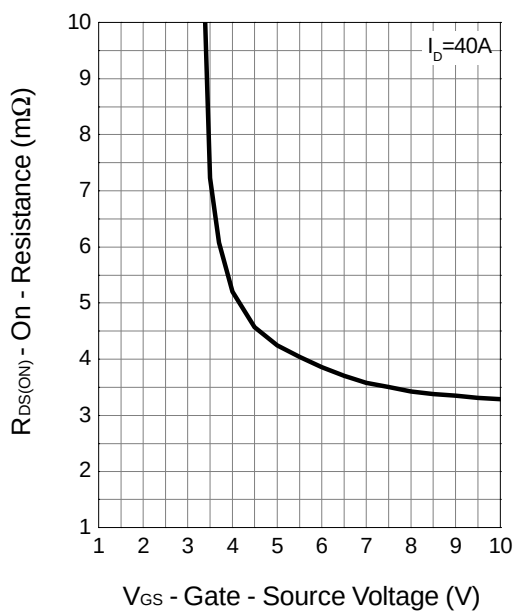
Output Characteristics



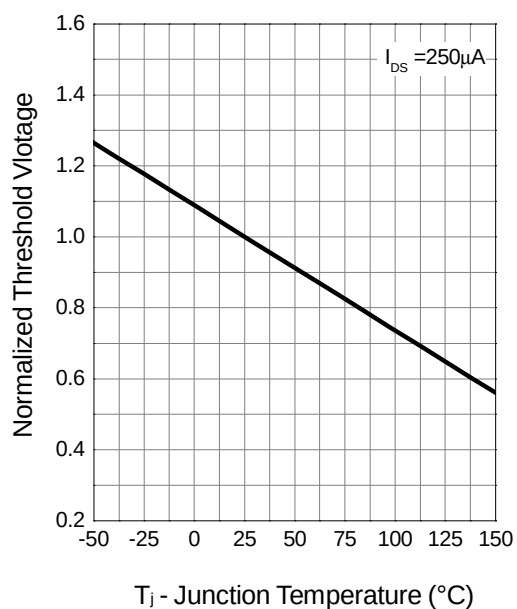
Drain-Source On Resistance



Drain-Source On Resistance

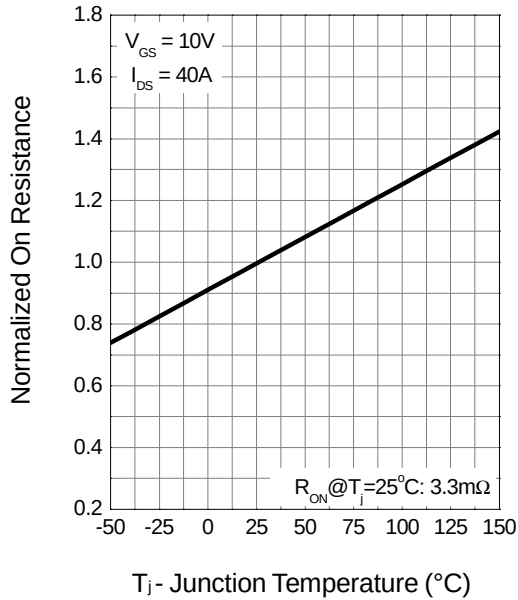


Gate Threshold Voltage

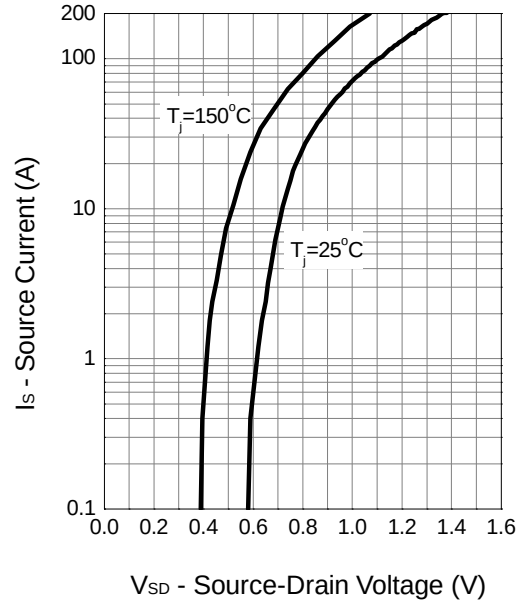


Typical Characteristics (Cont.)

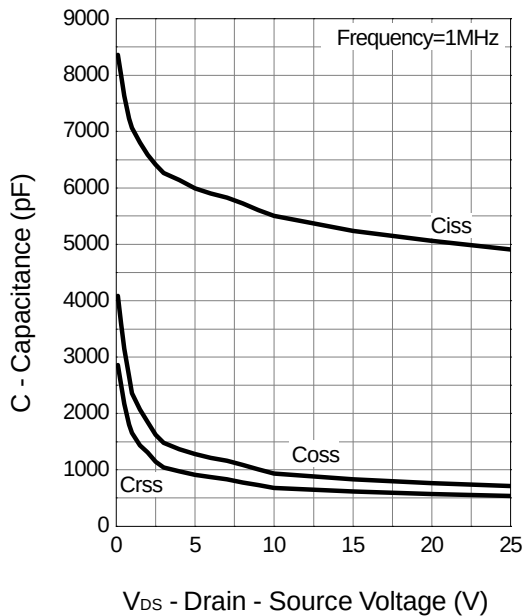
Drain-Source On Resistance



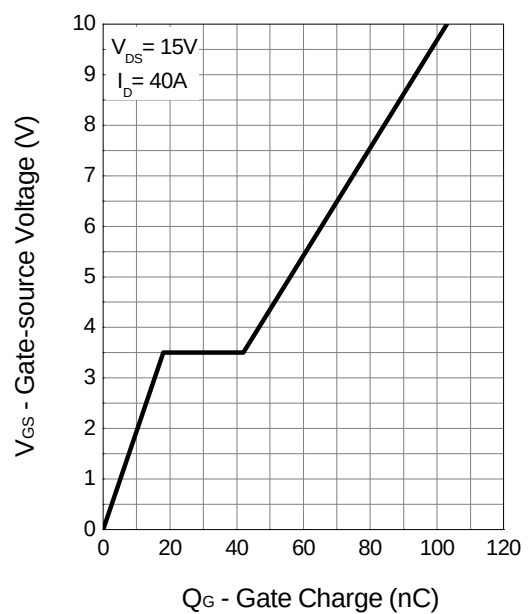
Source-Drain Diode Forward



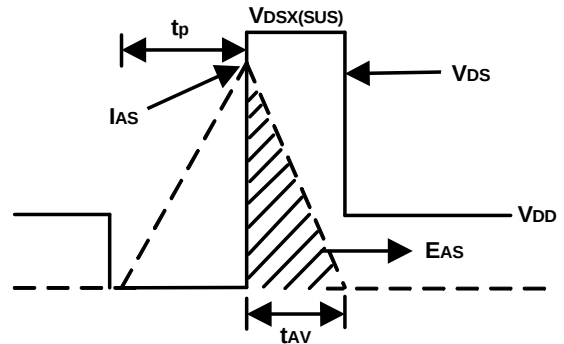
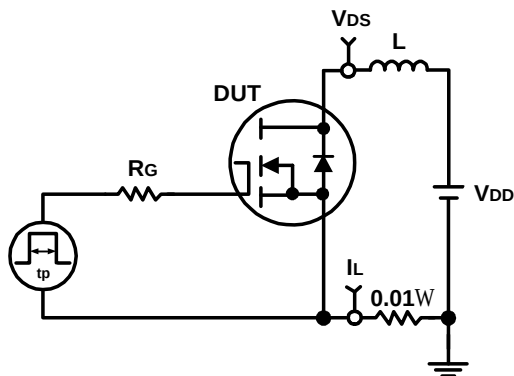
Capacitance



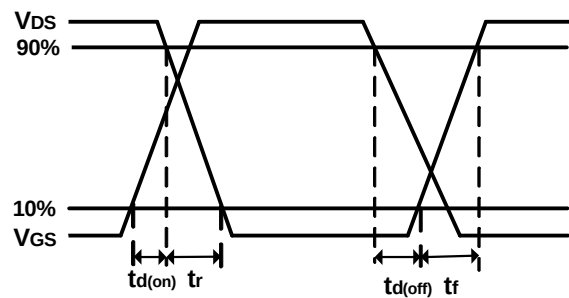
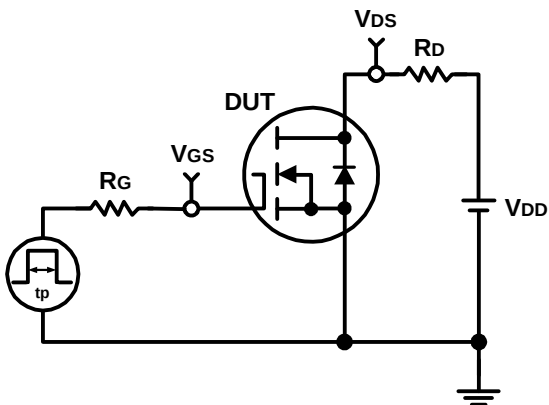
Gate Charge



Avalanche Test Circuit and Waveforms

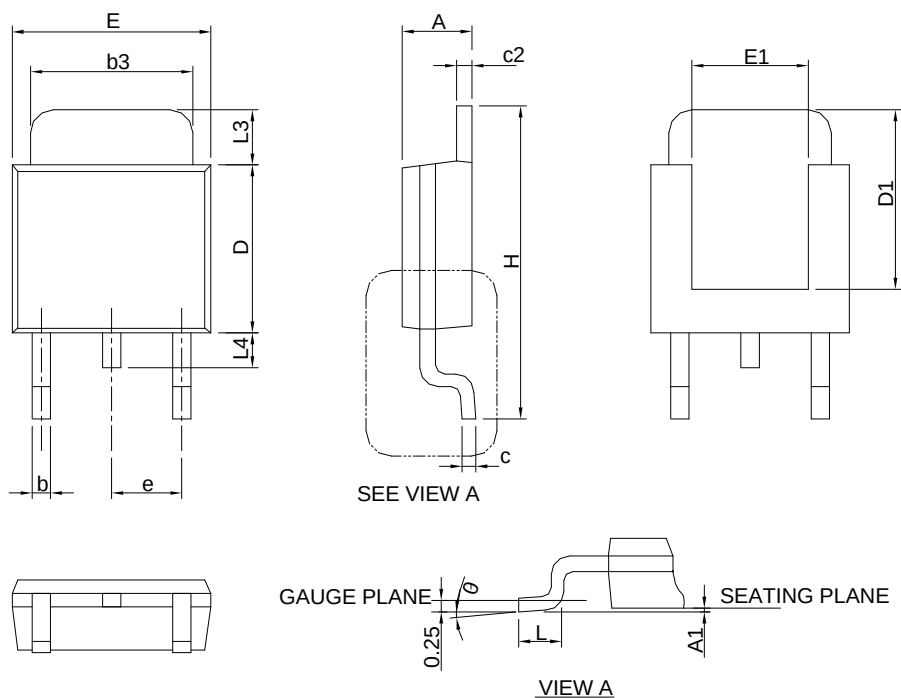


Avalanche Test Circuit and Waveforms



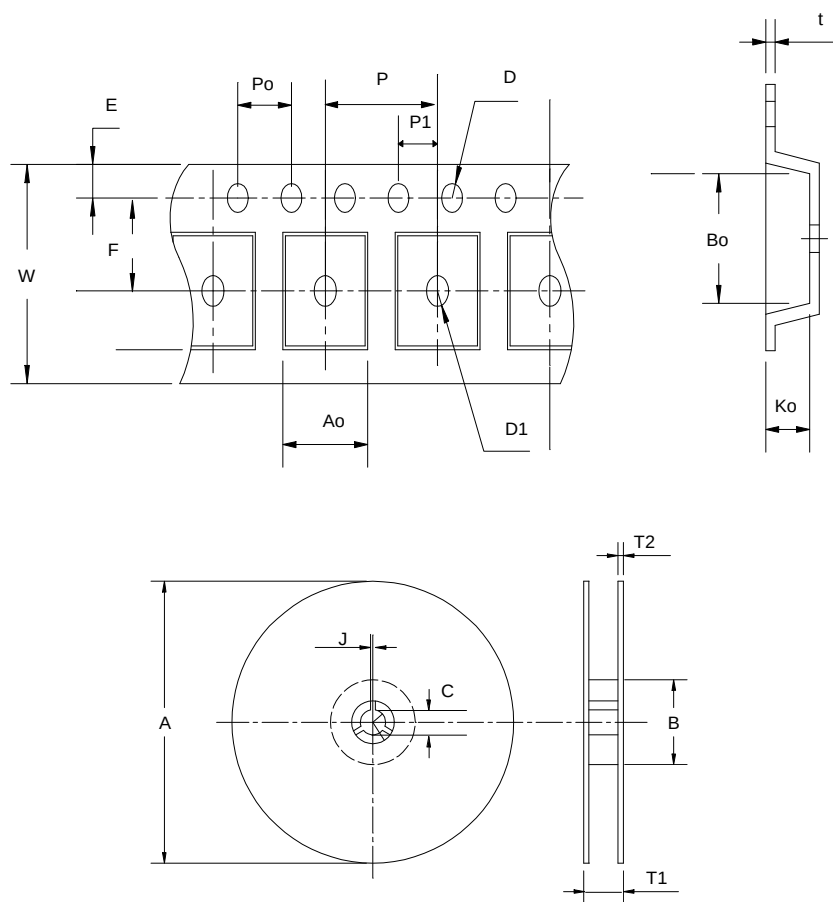
Package Information

TO252



SYMBOL	TO252			
	MILLIMETERS		INCHES	
	MIN.	MAX.	MIN.	MAX.
A	2.18	2.39	0.086	0.094
A1		0.13		0.005
b	0.50	0.89	0.020	0.035
b3	4.95	5.46	0.195	0.215
c	0.46	0.61	0.018	0.024
c2	0.46	0.89	0.018	0.035
D	5.33	6.22	0.210	0.245
D1	4.57		0.180	
E	6.35	6.73	0.250	0.265
E1	3.81		0.150	
e	2.29 BSC		0.090 BSC	
H	9.40	10.41	0.370	0.410
L	0.90	1.78	0.035	0.070
L3	0.89	2.03	0.035	0.080
L4		1.02		0.040
θ	0°	8°	0°	8°

Carrier Tape



Application	A	B	C	J	T1	T2	W	P	E
TO-252	330 ±3	100 ±2	13 ±0.5	2 ±0.5	16.4 +0.3 -0.2	2.5±0.5	16+ 0.3 - 0.1	8 ±0.1	1.75±0.1
	F	D	D1	P0	P1	A0	Bo	Ko	t
	7.5 ±0.1	1.5 +0.1	1.5±0.25	4.0 ±0.1	2.0 ±0.1	6.8 ±0.1	10.4±0.1	2.5±0.1	0.3±0.05

(mm)

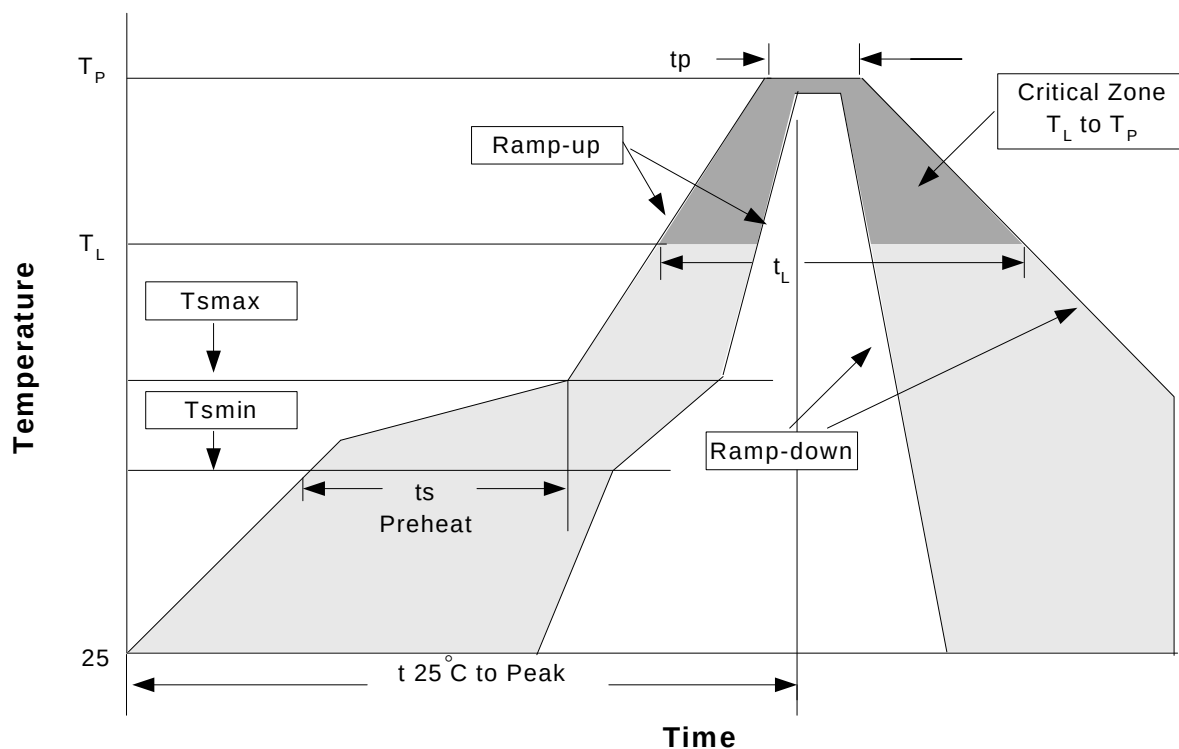
Cover Tape Dimensions

Application	Carrier Width	Cover Tape Width	Devices Per Reel
TO- 252	16	13.3	2500

Physical Specifications

Terminal Material	Solder-Plated Copper (Solder Material : 90/10 or 63/37 SnPb), 100%Sn
Lead Solderability	Meets EIA Specification RSI86-91, ANSI/J-STD-002 Category 3.

Reflow Condition (IR/Convection or VPR Reflow)



Classification Reflow Profiles

Profile Feature	Sn-Pb Eutectic Assembly	Pb-Free Assembly
Average ramp-up rate (T_L to T_P)	3°C/second max.	3°C/second max.
Preheat - Temperature Min (T_{smin}) - Temperature Max (T_{smax}) - Time (min to max) (t_s)	100°C 150°C 60-120 seconds	150°C 200°C 60-180 seconds
Time maintained above: - Temperature (T_L) - Time (t_L)	183°C 60-150 seconds	217°C 60-150 seconds
Peak/Classification Temperature (T_p)	See table 1	See table 2
Time within 5°C of actual Peak Temperature (t_p)	10-30 seconds	20-40 seconds
Ramp-down Rate	6°C/second max.	6°C/second max.
Time 25°C to Peak Temperature	6 minutes max.	8 minutes max.

Notes: All temperatures refer to topside of the package .Measured on the body surface.

Classification Reflow Profiles(Cont.)

Table 1. SnPb Eutectic Process – Package Peak Reflow Temperatures

Package Thickness	Volume mm ³ <350	Volume mm ³ 350
<2.5 mm	240 +0/-5°C	225 +0/-5°C
≥2.5 mm	225 +0/-5°C	225 +0/-5°C

Table 2. Pb-free Process – Package Classification Reflow Temperatures

Package Thickness	Volume mm ³ <350	Volume mm ³ 350-2000	Volume mm ³ >2000
<1.6 mm	260 +0°C*	260 +0°C*	260 +0°C*
1.6 mm – 2.5 mm	260 +0°C*	250 +0°C*	245 +0°C*
≥2.5 mm	250 +0°C*	245 +0°C*	245 +0°C*

*Tolerance: The device manufacturer/supplier **shall** assure process compatibility up to and including the stated classification temperature (this means Peak reflow temperature +0°C. For example 260°C+0°C) at the rated MSL level.

Reliability Test Program

Test item	Method	Description
SOLDERABILITY	MIL-STD-883D-2003	245°C, 5 SEC
HOLT	MIL-STD-883D-1005.7	1000 Hrs Bias @125°C
PCT	JESD-22-B,A102	168 Hrs, 100%RH, 121°C
TST	MIL-STD-883D-1011.9	-65°C~150°C, 200 Cycles

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