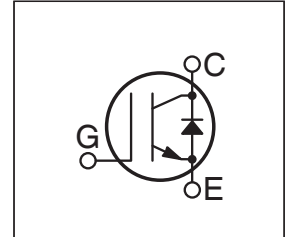
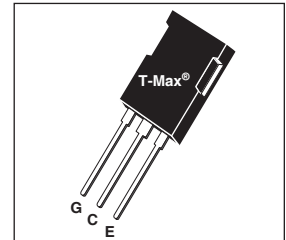


## POWER MOS 7® IGBT

The POWER MOS 7® IGBT is a new generation of high voltage power IGBTs. Using Punch Through Technology this IGBT is ideal for many high frequency, high voltage switching applications and has been optimized for high frequency switchmode power supplies.

- Low Conduction Loss
- Low Gate Charge
- Ultrafast Tail Current shutoff
- SSOA Rated



### MAXIMUM RATINGS

 All Ratings:  $T_C = 25^\circ\text{C}$  unless otherwise specified.

Symbol	Parameter	APT40GP90B2DQ2(G)	UNIT
$V_{CES}$	Collector-Emitter Voltage	900	Volts
$V_{GE}$	Gate-Emitter Voltage	$\pm 30$	
$I_{C1}$	Continuous Collector Current <sup>(7)</sup> @ $T_C = 25^\circ\text{C}$	101	Amps
$I_{C2}$	Continuous Collector Current @ $T_C = 110^\circ\text{C}$	50	
$I_{CM}$	Pulsed Collector Current <sup>(1)</sup>	160	
SSOA	Switching Safe Operating Area @ $T_J = 150^\circ\text{C}$	160A @ 900V	
$P_D$	Total Power Dissipation	543	Watts
$T_J, T_{STG}$	Operating and Storage Junction Temperature Range	-55 to 150	$^\circ\text{C}$
$T_L$	Max. Lead Temp. for Soldering: 0.063" from Case for 10 Sec.	300	

### STATIC ELECTRICAL CHARACTERISTICS

Symbol	Characteristic / Test Conditions	MIN	TYP	MAX	Units
$V_{(BR)CES}$	Collector-Emitter Breakdown Voltage ( $V_{GE} = 0\text{V}, I_C = 350\mu\text{A}$ )	900			Volts
$V_{GE(TH)}$	Gate Threshold Voltage ( $V_{CE} = V_{GE}, I_C = 1\text{mA}, T_J = 25^\circ\text{C}$ )	3	4.5	6	
$V_{CE(ON)}$	Collector-Emitter On Voltage ( $V_{GE} = 15\text{V}, I_C = 40\text{A}, T_J = 25^\circ\text{C}$ )		3.2	3.9	
	Collector-Emitter On Voltage ( $V_{GE} = 15\text{V}, I_C = 40\text{A}, T_J = 125^\circ\text{C}$ )		2.7		
$I_{CES}$	Collector Cut-off Current ( $V_{CE} = 900\text{V}, V_{GE} = 0\text{V}, T_J = 25^\circ\text{C}$ ) <sup>(2)</sup>			350	$\mu\text{A}$
	Collector Cut-off Current ( $V_{CE} = 900\text{V}, V_{GE} = 0\text{V}, T_J = 125^\circ\text{C}$ ) <sup>(2)</sup>			1500	
$I_{GES}$	Gate-Emitter Leakage Current ( $V_{GE} = \pm 20\text{V}$ )			$\pm 100$	nA



**CAUTION:** These Devices are Sensitive to Electrostatic Discharge. Proper Handling Procedures Should Be Followed.

## DYNAMIC CHARACTERISTICS

APT40GP90B2DQ2(G)

Symbol	Characteristic	Test Conditions	MIN	TYP	MAX	UNIT
$C_{ies}$	Input Capacitance	<b>Capacitance</b> $V_{GE} = 0V, V_{CE} = 25V$ $f = 1 \text{ MHz}$		3300		pF
$C_{oes}$	Output Capacitance			325		
$C_{res}$	Reverse Transfer Capacitance			35		
$V_{GEP}$	Gate-to-Emitter Plateau Voltage	Gate Charge		7.5		V
$Q_g$	Total Gate Charge <sup>③</sup>	$V_{GE} = 15V$		145		nC
$Q_{ge}$	Gate-Emitter Charge	$V_{CE} = 450V$		22		
$Q_{gc}$	Gate-Collector ("Miller") Charge	$I_C = 40A$		55		
SSOA	Switching Safe Operating Area	$T_J = 150^\circ\text{C}, R_G = 4.3\Omega, V_{GE} = 15V, L = 100\mu\text{H}, V_{CE} = 900V$	160			A
$t_{d(on)}$	Turn-on Delay Time	<b>Inductive Switching (25°C)</b> $V_{CC} = 600V$ $V_{GE} = 15V$ $I_C = 40A$ $R_G = 4.3\Omega$ $T_J = +25^\circ\text{C}$		14		ns
$t_r$	Current Rise Time			23		
$t_{d(off)}$	Turn-off Delay Time			90		
$t_f$	Current Fall Time			60		μJ
$E_{on1}$	Turn-on Switching Energy <sup>④</sup>			TBD		
$E_{on2}$	Turn-on Switching Energy (Diode) <sup>⑤</sup>		1350			
$E_{off}$	Turn-off Switching Energy <sup>⑥</sup>			795		
$t_{d(on)}$	Turn-on Delay Time	<b>Inductive Switching (125°C)</b> $V_{CC} = 600V$ $V_{GE} = 15V$ $I_C = 40A$ $R_G = 4.3\Omega$ $T_J = +125^\circ\text{C}$		14		ns
$t_r$	Current Rise Time			23		
$t_{d(off)}$	Turn-off Delay Time			130		
$t_f$	Current Fall Time			90		μJ
$E_{on1}$	Turn-on Switching Energy <sup>④</sup>			TBD		
$E_{on2}$	Turn-on Switching Energy (Diode) <sup>⑤</sup>		2280			
$E_{off}$	Turn-off Switching Energy <sup>⑥</sup>			1245		

## THERMAL AND MECHANICAL CHARACTERISTICS

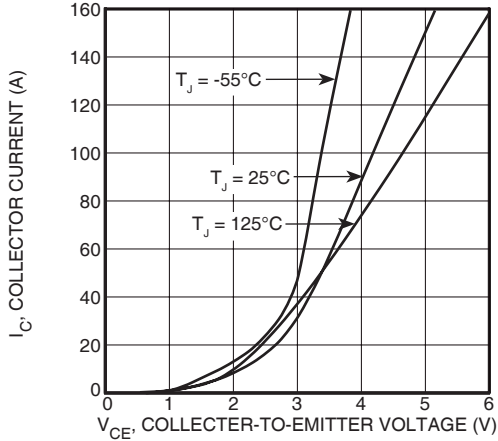
Symbol	Characteristic	MIN	TYP	MAX	UNIT
$R_{\theta JC}$	Junction to Case (IGBT)			.23	°C/W
$R_{\theta JC}$	Junction to Case (DIODE)			.61	
$W_T$	Package Weight		5.9		gm

- ① Repetitive Rating: Pulse width limited by maximum junction temperature.
- ② For Combi devices,  $I_{ces}$  includes both IGBT and FRED leakages
- ③ See MIL-STD-750 Method 3471.
- ④  $E_{on1}$  is the clamped inductive turn-on energy of the IGBT only, without the effect of a commutating diode reverse recovery current adding to the IGBT turn-on loss. Tested in inductive switching test circuit shown in figure 21, but with a Silicon Carbide diode.
- ⑤  $E_{on2}$  is the clamped inductive turn-on energy that includes a commutating diode reverse recovery current in the IGBT turn-on switching loss. (See Figures 21, 22.)
- ⑥  $E_{off}$  is the clamped inductive turn-off energy measured in accordance with JEDEC standard JESD24-1. (See Figures 21, 23.)
- ⑦ Continuous current limited by package lead temperature.

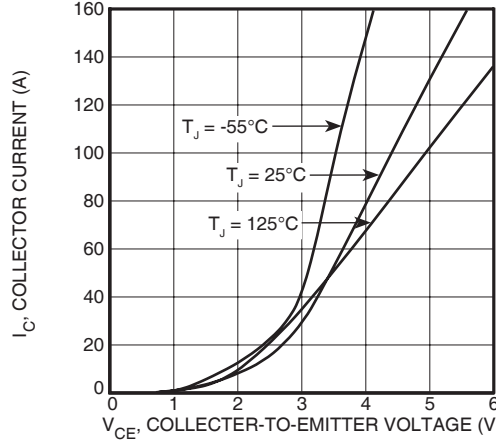
APT Reserves the right to change, without notice, the specifications and information contained h

**TYPICAL PERFORMANCE CURVES**

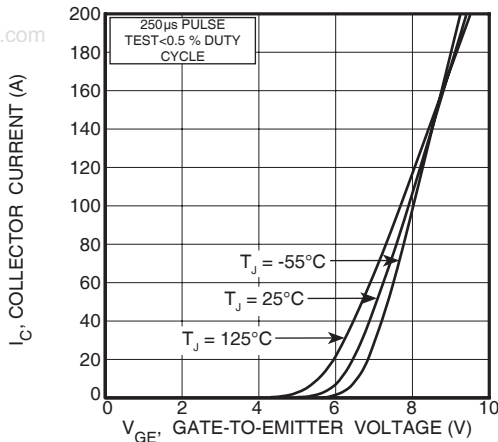
**APT40GP90B2DQ2(G)**



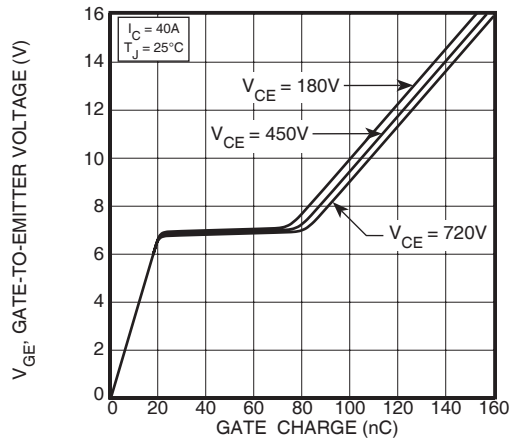
**FIGURE 1, Output Characteristics(T<sub>J</sub> = 25°C)**



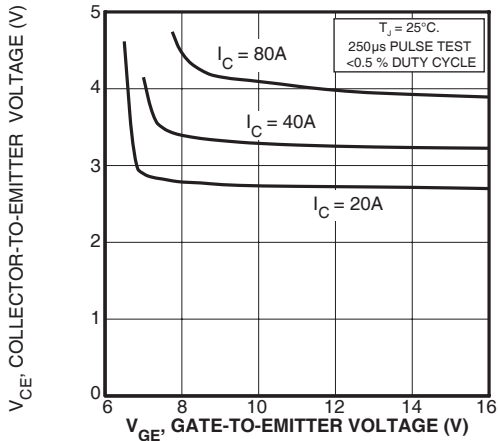
**FIGURE 2, Output Characteristics (T<sub>J</sub> = 125°C)**



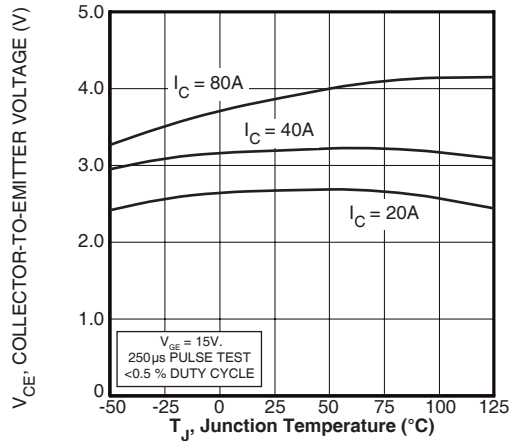
**FIGURE 3, Transfer Characteristics**



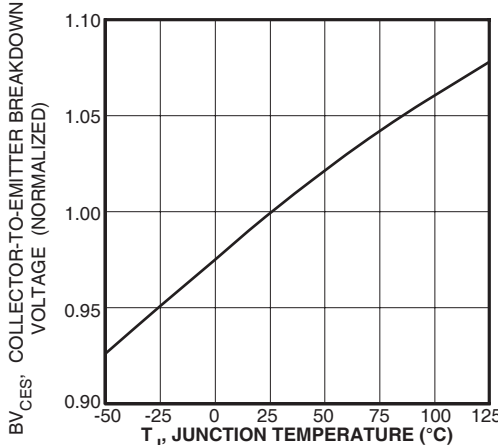
**FIGURE 4, Gate Charge**



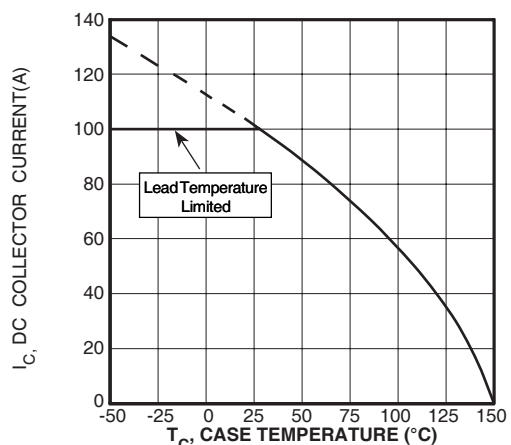
**FIGURE 5, On State Voltage vs Gate-to-Emitter Voltage**



**FIGURE 6, On State Voltage vs Junction Temperature**



**FIGURE 7, Breakdown Voltage vs. Junction Temperature**



**FIGURE 8, DC Collector Current vs Case Temperature**

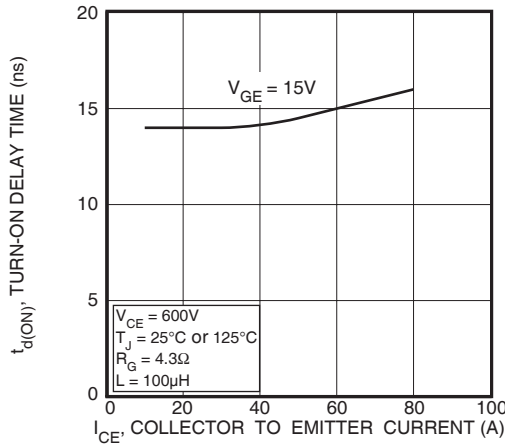


FIGURE 9, Turn-On Delay Time vs Collector Current

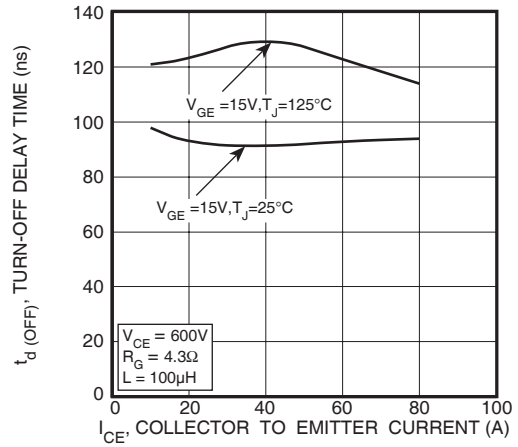


FIGURE 10, Turn-Off Delay Time vs Collector Current

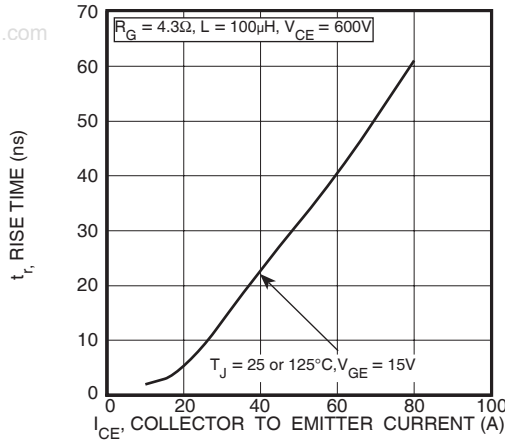


FIGURE 11, Current Rise Time vs Collector Current

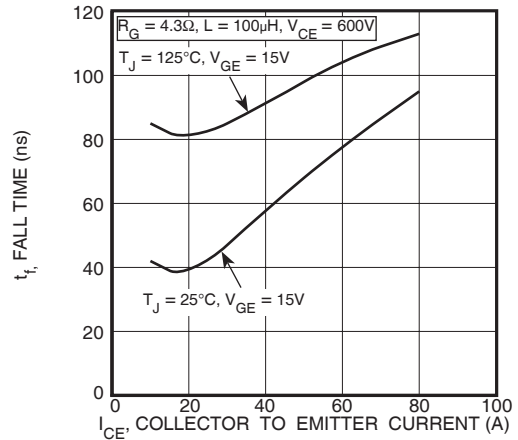


FIGURE 12, Current Fall Time vs Collector Current

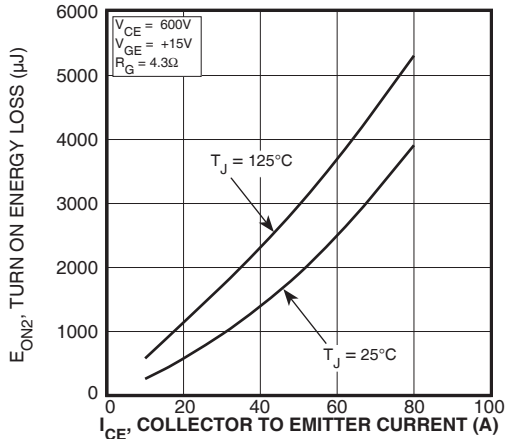


FIGURE 13, Turn-On Energy Loss vs Collector Current

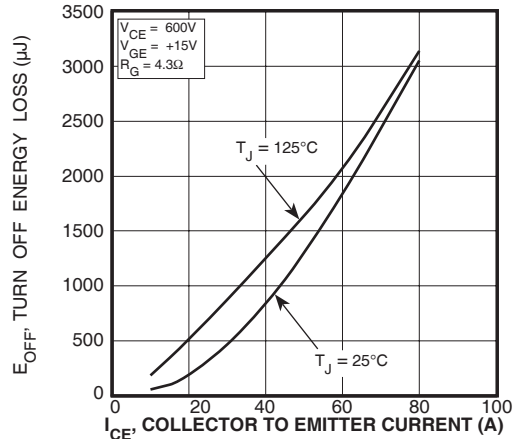


FIGURE 14, Turn Off Energy Loss vs Collector Current

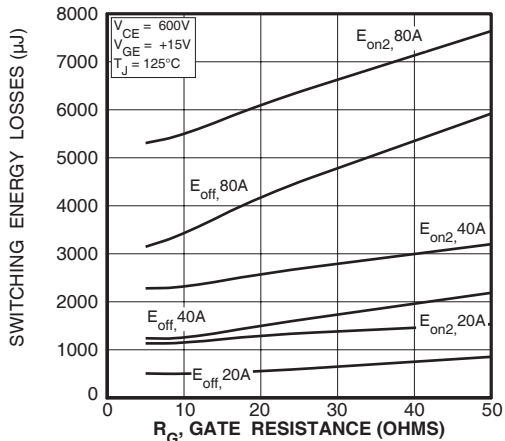


FIGURE 15, Switching Energy Losses vs. Gate Resistance

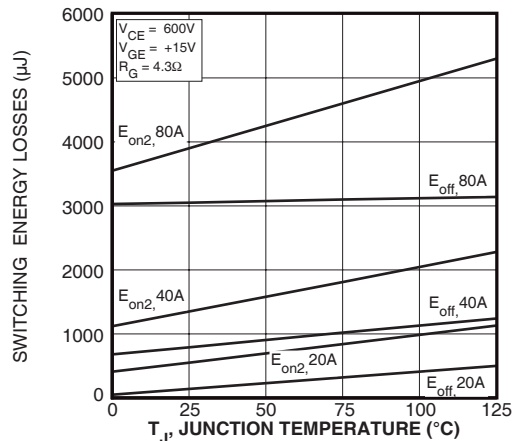


FIGURE 16, Switching Energy Losses vs Junction Temperature

**TYPICAL PERFORMANCE CURVES**

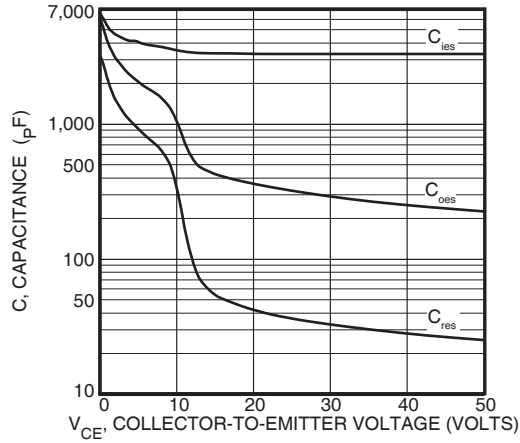


Figure 17, Capacitance vs Collector-To-Emitter Voltage

**APT40GP90B2DQ2(G)**

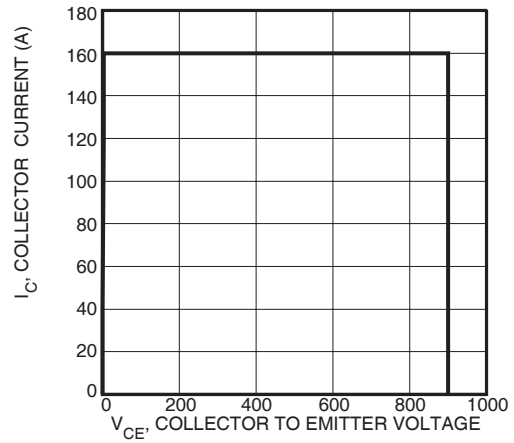


Figure 18, Minimum Switching Safe Operating Area

www.DataSheet4U.com

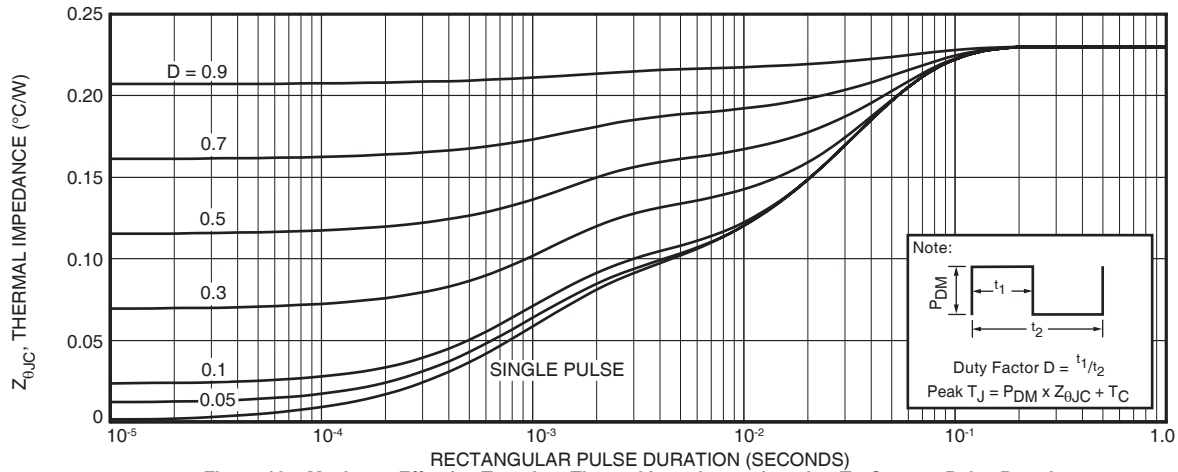


Figure 19a, Maximum Effective Transient Thermal Impedance, Junction-To-Case vs Pulse Duration

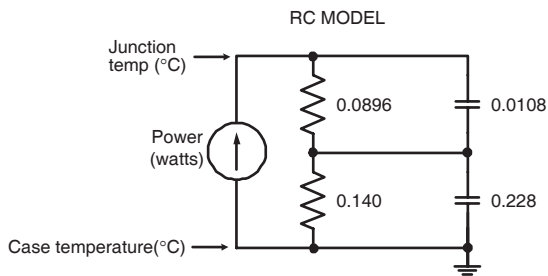


FIGURE 19b, TRANSIENT THERMAL IMPEDANCE MODEL

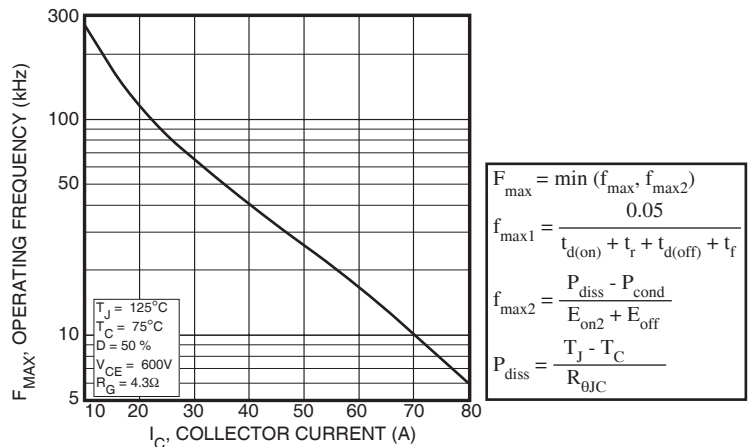


Figure 20, Operating Frequency vs Collector Current

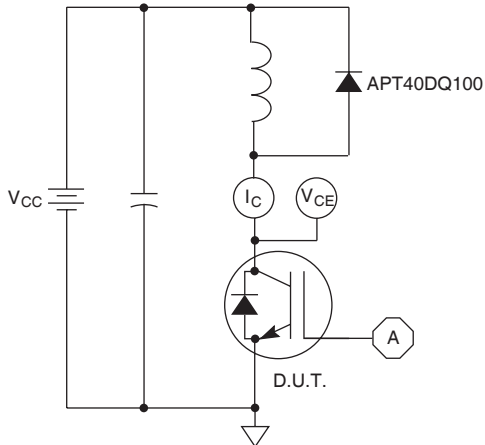


Figure 21, Inductive Switching Test Circuit

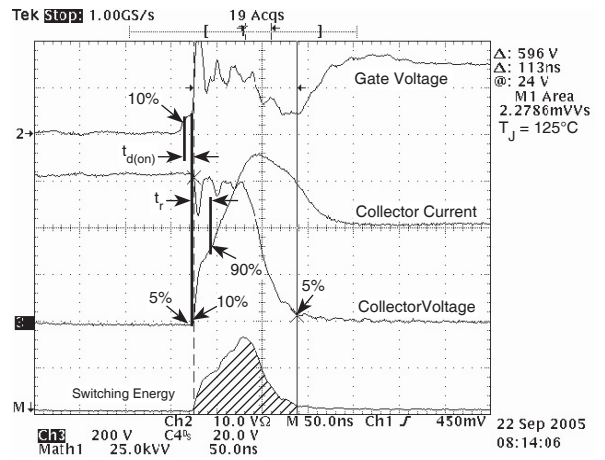


Figure 22, Turn-on Switching Waveforms and Definitions

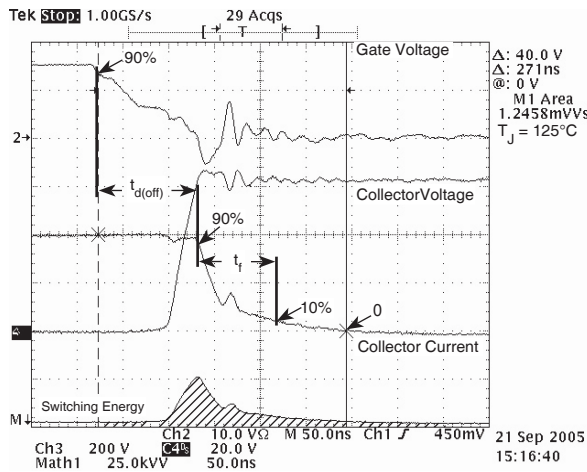
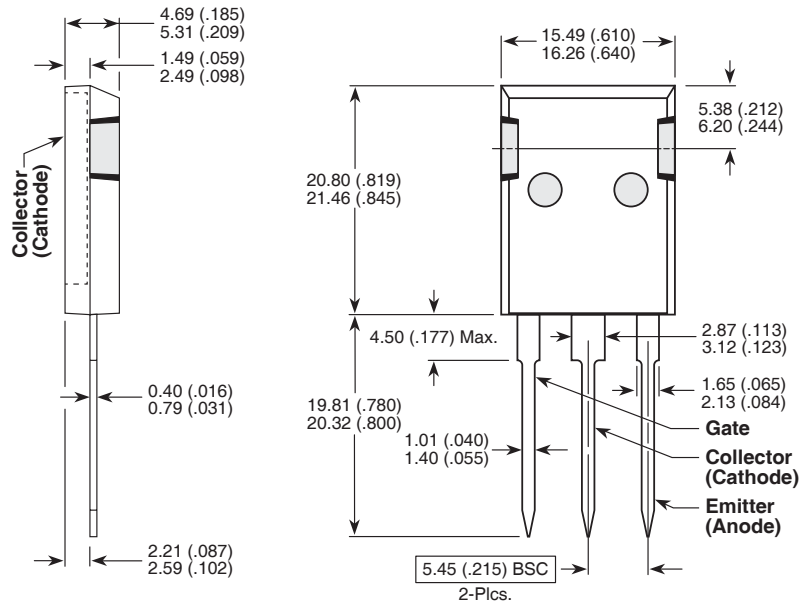


Figure 23, Turn-off Switching Waveforms and Definitions

T-MAX® (B2) Package Outline

Ⓢ1 SAC: Tin, Silver, Copper



Dimensions in Millimeters and (Inches)

APT's products are covered by one or more of U.S. patents 4,895,810 5,045,903 5,089,434 5,182,234 5,019,522

5,262,336 6,503,786 5,256,583 4,748,103 5,283,202 5,231,474 5,434,095 5,528,058 and foreign patents. US and Foreign patents pending. All Rights Reserved.

# ULTRAFAST SOFT RECOVERY ANTI-PARALLEL DIODE

**MAXIMUM RATINGS**

All Ratings:  $T_C = 25^\circ\text{C}$  unless otherwise specified.

Symbol	Characteristic / Test Conditions	APT40GP90B2DQ2		UNIT
$I_F(AV)$	Maximum Average Forward Current ( $T_C = 106^\circ\text{C}$ , Duty Cycle = 0.5)		40	Amps
$I_F(RMS)$	RMS Forward Current (Square wave, 50% duty)		60	
$I_{FSM}$	Non-Repetitive Forward Surge Current ( $T_J = 45^\circ\text{C}$ , 8.3ms)		210	

**STATIC ELECTRICAL CHARACTERISTICS**

Symbol	Characteristic / Test Conditions	MIN	TYP	MAX	UNIT
$V_F$	Forward Voltage		$I_F = 40A$	2.5	Volts
			$I_F = 80A$	3.08	
			$I_F = 40A, T_J = 125^\circ\text{C}$	1.97	

**DYNAMIC CHARACTERISTICS**

Symbol	Characteristic	Test Conditions	MIN	TYP	MAX	UNIT
$t_{rr}$	Reverse Recovery Time	$I_F = 1A, di_F/dt = -100A/\mu s, V_R = 30V, T_J = 25^\circ\text{C}$	-	24		ns
$t_{rr}$	Reverse Recovery Time	$I_F = 40A, di_F/dt = -200A/\mu s, V_R = 667V, T_C = 25^\circ\text{C}$	-	240		
$Q_{rr}$	Reverse Recovery Charge		-	300		nC
$I_{RRM}$	Maximum Reverse Recovery Current		-	3	-	Amps
$t_{rr}$	Reverse Recovery Time	$I_F = 40A, di_F/dt = -200A/\mu s, V_R = 667V, T_C = 125^\circ\text{C}$	-	300		ns
$Q_{rr}$	Reverse Recovery Charge		-	1430		nC
$I_{RRM}$	Maximum Reverse Recovery Current		-	8	-	Amps
$t_{rr}$	Reverse Recovery Time	$I_F = 40A, di_F/dt = -1000A/\mu s, V_R = 667V, T_C = 125^\circ\text{C}$	-	145		ns
$Q_{rr}$	Reverse Recovery Charge		-	2520		nC
$I_{RRM}$	Maximum Reverse Recovery Current		-	28		Amps

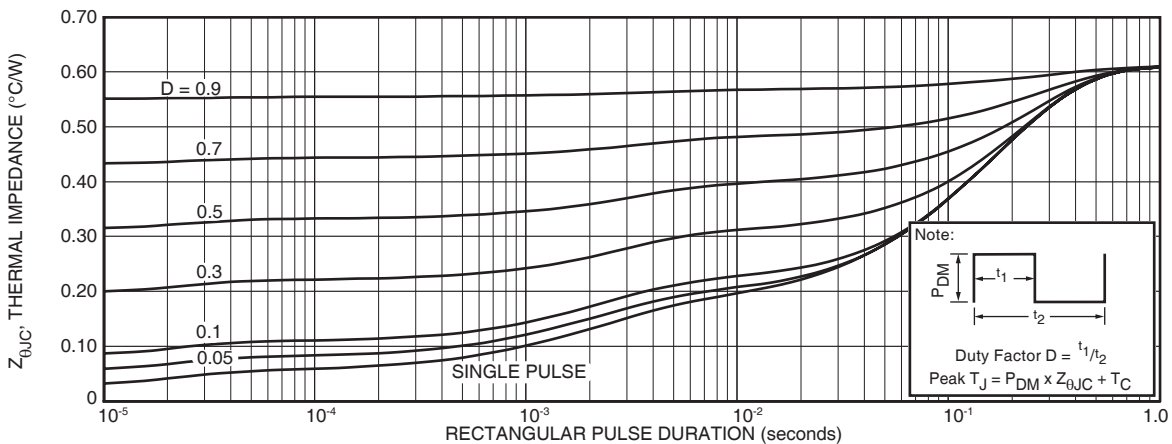


FIGURE 24a. MAXIMUM EFFECTIVE TRANSIENT THERMAL IMPEDANCE, JUNCTION-TO-CASE vs. PULSE DURATION

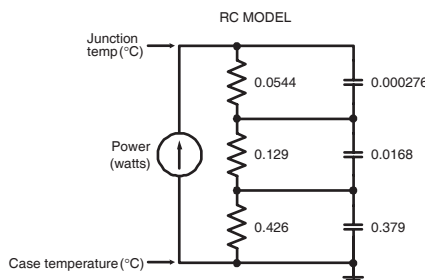


FIGURE 24b, TRANSIENT THERMAL IMPEDANCE MODEL

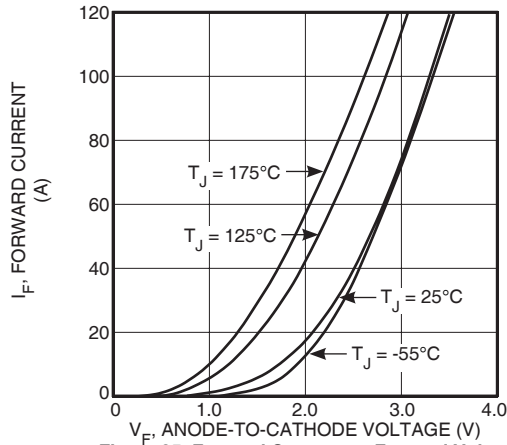


Figure 25. Forward Current vs. Forward Voltage

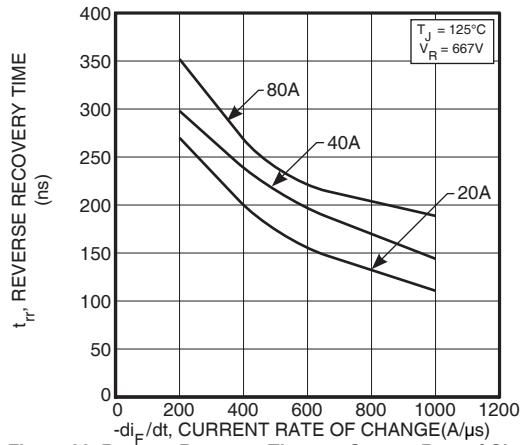


Figure 26. Reverse Recovery Time vs. Current Rate of Change

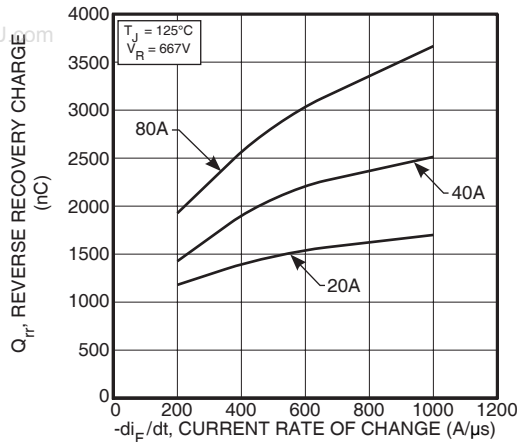


Figure 27. Reverse Recovery Charge vs. Current Rate of Change

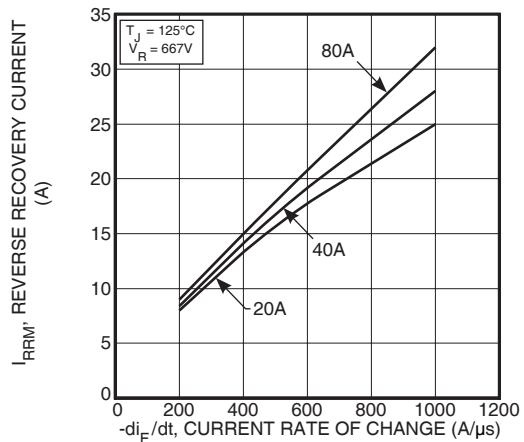


Figure 28. Reverse Recovery Current vs. Current Rate of Change

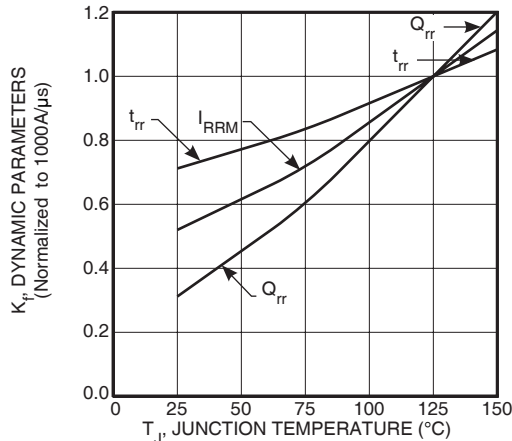


Figure 29. Dynamic Parameters vs. Junction Temperature

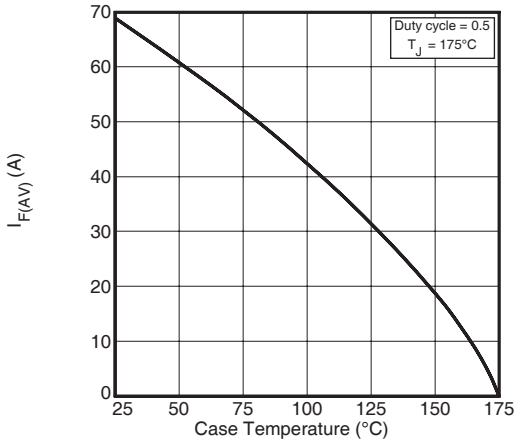


Figure 30. Maximum Average Forward Current vs. Case Temperature

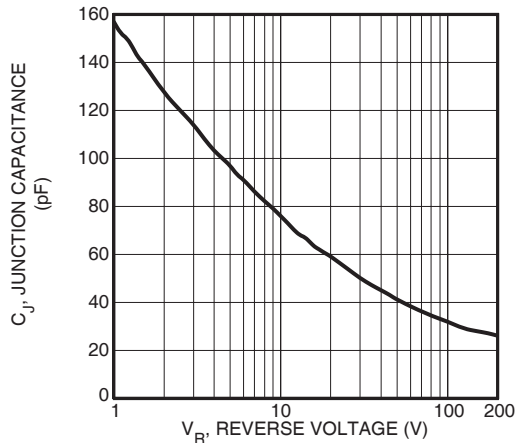


Figure 31. Junction Capacitance vs. Reverse Voltage



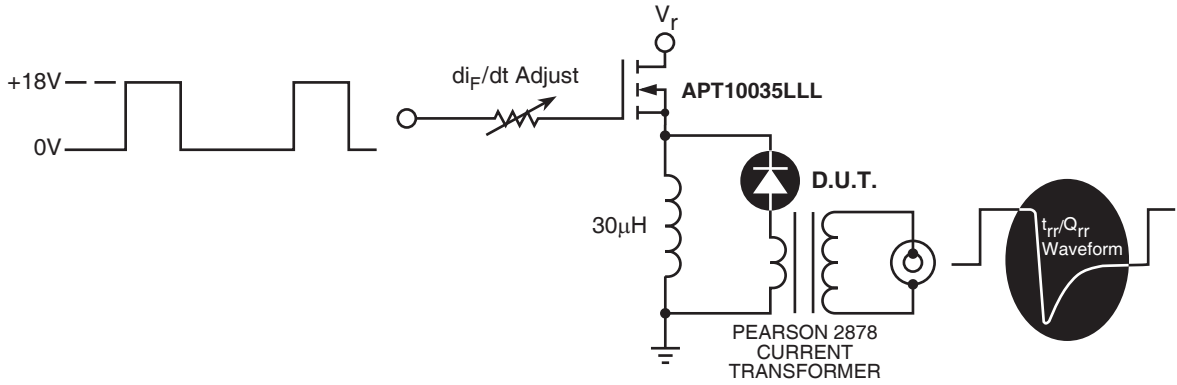


Figure 32. Diode Test Circuit

www.DataSheet4U.com

- 1  $I_F$  - Forward Conduction Current
- 2  $di_F/dt$  - Rate of Diode Current Change Through Zero Crossing.
- 3  $I_{RRM}$  - Maximum Reverse Recovery Current.
- 4  $t_{rr}$  - Reverse Recovery Time, measured from zero crossing where diode current goes from positive to negative, to the point at which the straight line through  $I_{RRM}$  and  $0.25 \cdot I_{RRM}$  passes through zero.
- 5  $Q_{rr}$  - Area Under the Curve Defined by  $I_{RRM}$  and  $t_{rr}$ .

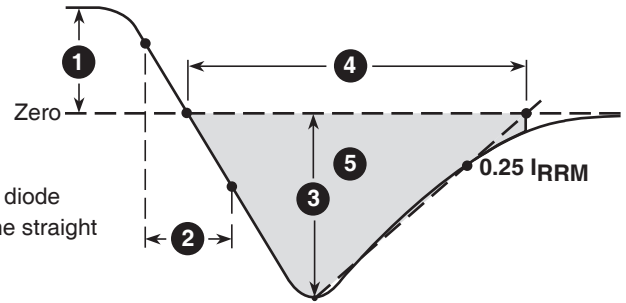
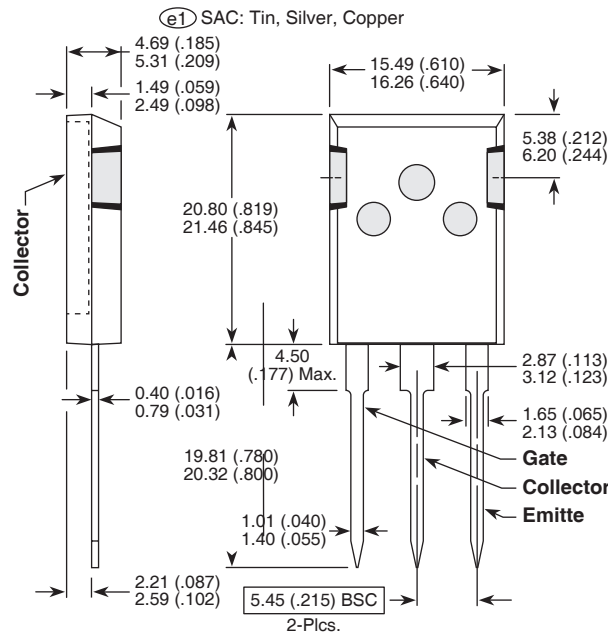


Figure 33. Diode Reverse Recovery Waveform and Definitions

### T-MAX® (B2) Package Outline



Dimensions in Millimeters and (Inches)

APT's products are covered by one or more of U.S. patents 4,895,810 5,045,903 5,089,434 5,182,234 5,019,522 5,262,336 6,503,786 5,256,583 4,748,103 5,283,202 5,231,474 5,434,095 5,528,058 and foreign patents. US and Foreign patents pending. All Rights Reserved.