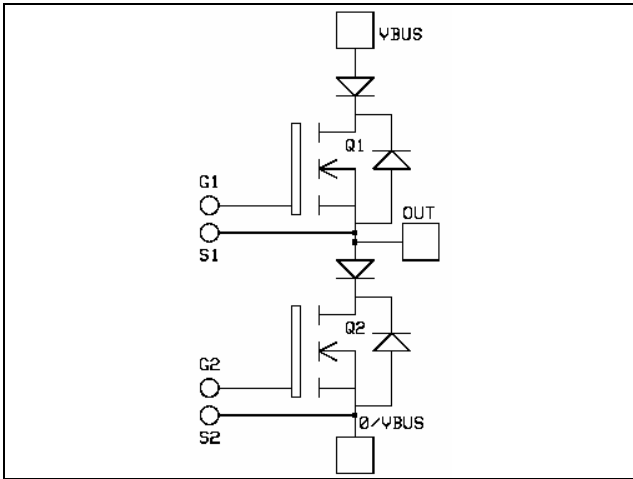


*Phase leg
with Series diodes
MOSFET Power Module*

$V_{DSS} = 1000V$
 $R_{DSon} = 130m\Omega \text{ max @ } T_j = 25^\circ C$
 $I_D = 65A \text{ @ } T_c = 25^\circ C$



Application

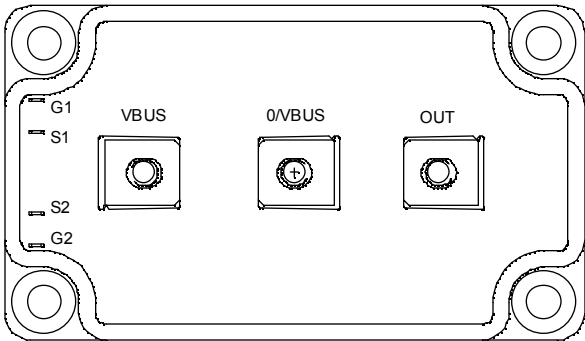
- Zero Current Switching resonant mode

Features

- Power MOS 7[®] MOSFETs
 - Low R_{DSon}
 - Low input and Miller capacitance
 - Low gate charge
 - Fast intrinsic reverse diode
 - Avalanche energy rated
 - Very rugged
- Kelvin source for easy drive
- Very low stray inductance
 - Symmetrical design
 - M5 power connectors
- High level of integration

Benefits

- Outstanding performance at high frequency operation
- Direct mounting to heatsink (isolated package)
- Low junction to case thermal resistance
- Low profile



Absolute maximum ratings

Symbol	Parameter	Max ratings	Unit
V_{DSS}	Drain - Source Breakdown Voltage	1000	V
I_D	Continuous Drain Current	$T_c = 25^\circ C$	65
		$T_c = 80^\circ C$	49
I_{DM}	Pulsed Drain current	240	A
V_{GS}	Gate - Source Voltage	± 30	V
R_{DSon}	Drain - Source ON Resistance	130	$m\Omega$
P_D	Maximum Power Dissipation	$T_c = 25^\circ C$	1250
I_{AR}	Avalanche current (repetitive and non repetitive)	24	A
E_{AR}	Repetitive Avalanche Energy	30	mJ
E_{AS}	Single Pulse Avalanche Energy	1300	

CAUTION: These Devices are sensitive to Electrostatic Discharge. Proper Handling Procedures Should Be Followed.

All ratings @ $T_j = 25^\circ\text{C}$ unless otherwise specified

Electrical Characteristics

Symbol	Characteristic	Test Conditions	Min	Typ	Max	Unit
BV_{DSS}	Drain - Source Breakdown Voltage	$V_{GS} = 0V, I_D = 1.5mA$	1000			V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{GS} = 0V, V_{DS} = 1000V$ $T_j = 25^\circ\text{C}$			600	μA
		$V_{GS} = 0V, V_{DS} = 800V$ $T_j = 125^\circ\text{C}$			2	mA
$R_{DS(on)}$	Drain - Source on Resistance	$V_{GS} = 10V, I_D = 32.5A$			130	$\text{m}\Omega$
$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS} = V_{DS}, I_D = 6mA$	3		5	V
I_{GSS}	Gate - Source Leakage Current	$V_{GS} = \pm 30V, V_{DS} = 0V$			± 450	nA

Dynamic Characteristics

Symbol	Characteristic	Test Conditions	Min	Typ	Max	Unit
C_{iss}	Input Capacitance	$V_{GS} = 0V$ $V_{DS} = 25V$ $f = 1\text{MHz}$		15.2		nF
C_{oss}	Output Capacitance			2.6		
C_{rss}	Reverse Transfer Capacitance			0.44		
Q_g	Total gate Charge	$V_{GS} = 10V$ $V_{Bus} = 500V$ $I_D = 65A$		562		nC
Q_{gs}	Gate - Source Charge			75		
Q_{gd}	Gate - Drain Charge			363		
$T_{d(on)}$	Turn-on Delay Time	Inductive switching @ 125°C $V_{GS} = 15V$ $V_{Bus} = 667V$ $I_D = 65A$ $R_G = 0.5\Omega$		9		ns
T_r	Rise Time			9		
$T_{d(off)}$	Turn-off Delay Time			50		
T_f	Fall Time			24		
E_{on}	Turn-on Switching Energy ❶	Inductive switching @ 25°C $V_{GS} = 15V, V_{Bus} = 667V$ $I_D = 65A, R_G = 0.5\Omega$		2.13		mJ
E_{off}	Turn-off Switching Energy ❷			0.46		
E_{on}	Turn-on Switching Energy ❶	Inductive switching @ 125°C $V_{GS} = 15V, V_{Bus} = 667V$ $I_D = 65A, R_G = 0.5\Omega$		4.5		mJ
E_{off}	Turn-off Switching Energy ❷			0.57		

❶ E_{on} includes diode reverse recovery.

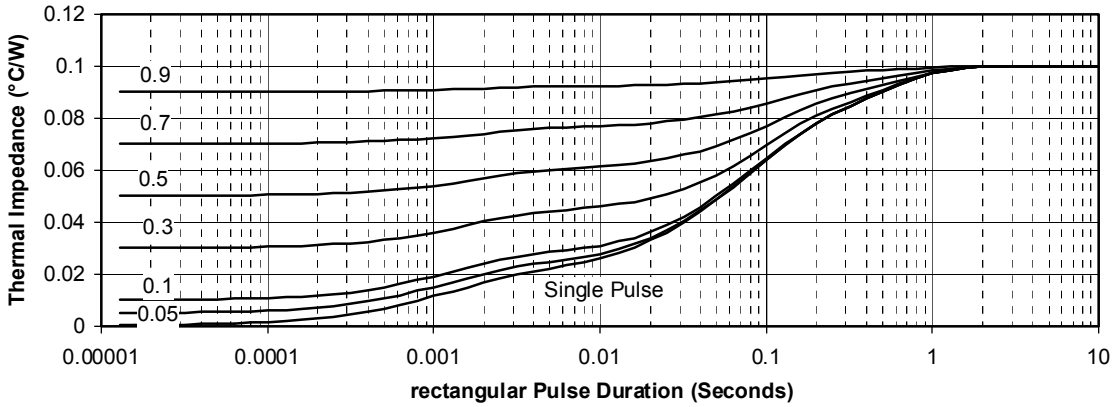
❷ In accordance with JEDEC standard JESD24-1.

Series diode ratings and characteristics

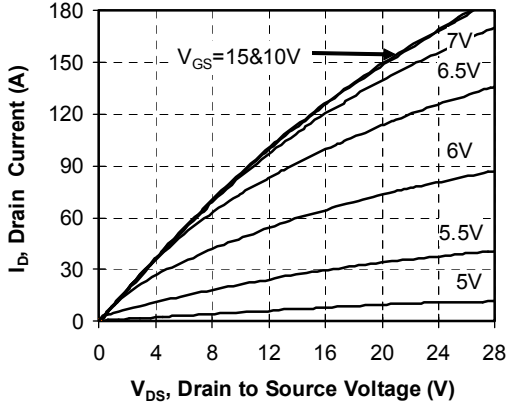
Symbol	Characteristic	Test Conditions	Min	Typ	Max	Unit
V_{RRM}	Maximum Repetitive Reverse Voltage		1000			V
I_{RM}	Maximum Reverse Leakage Current	$V_R = 1000V$ $T_j = 125^\circ\text{C}$			1	mA
$I_{F(AV)}$	Maximum Average Forward Current	50% duty cycle $T_c = 100^\circ\text{C}$		120		A
V_F	Diode Forward Voltage	$I_F = 120A$		1.9	2.5	V
		$I_F = 240A$		2.2		
		$I_F = 120A$ $T_j = 125^\circ\text{C}$		1.7		
t_{rr}	Reverse Recovery Time	$I_F = 120A$ $T_j = 25^\circ\text{C}$		280		ns
		$V_R = 670V$ $di/dt = 400A/\mu\text{s}$ $T_j = 125^\circ\text{C}$		350		
Q_{rr}	Reverse Recovery Charge	$I_F = 120A$ $T_j = 25^\circ\text{C}$		1.5		μC
		$V_R = 670V$ $di/dt = 400A/\mu\text{s}$ $T_j = 125^\circ\text{C}$		7.2		

Typical Performance Curve

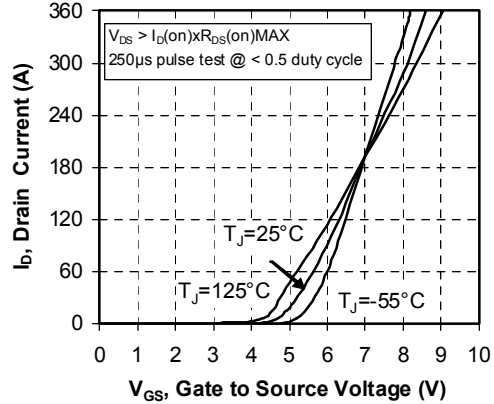
Maximum Effective Transient Thermal Impedance, Junction to Case vs Pulse Duration



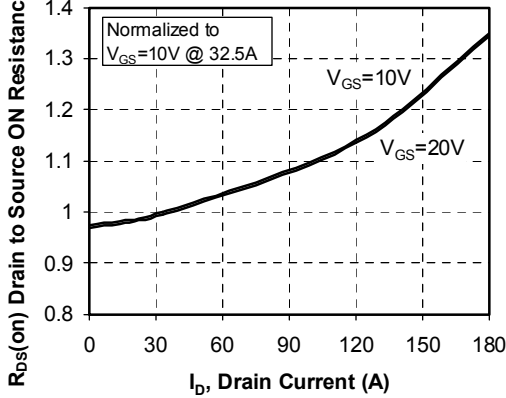
Low Voltage Output Characteristics



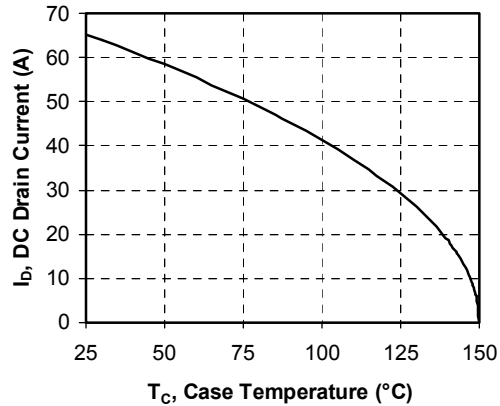
Transfer Characteristics

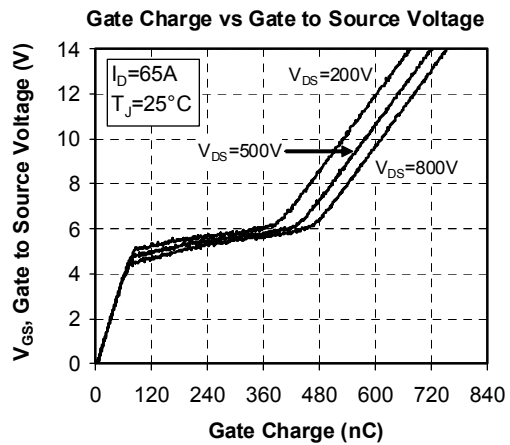
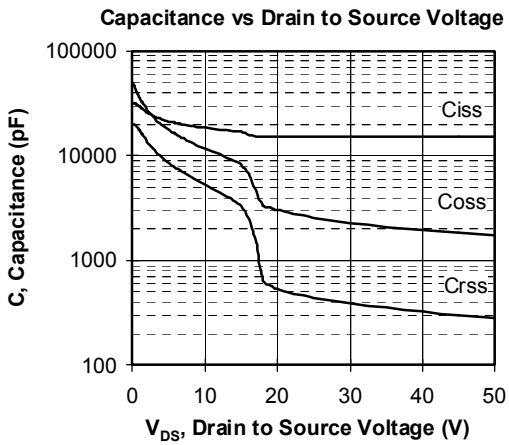
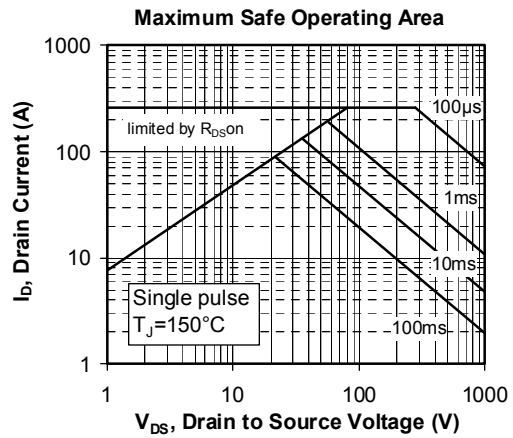
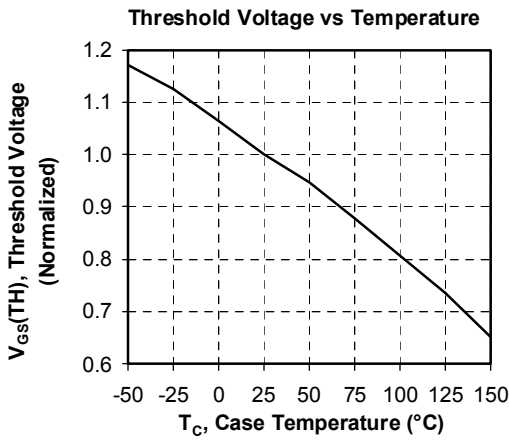
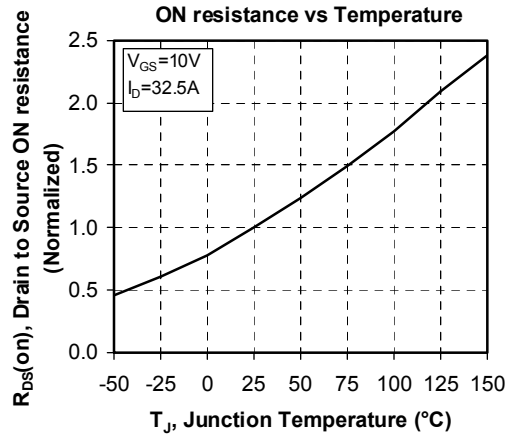
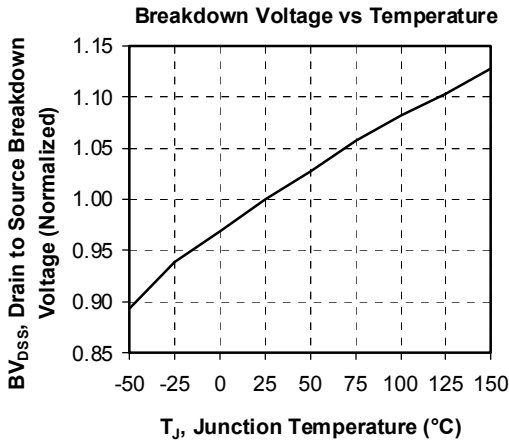


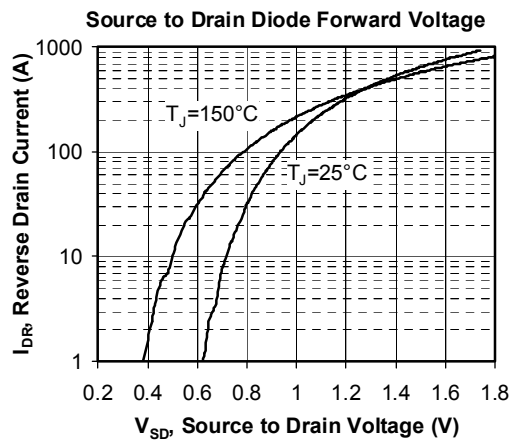
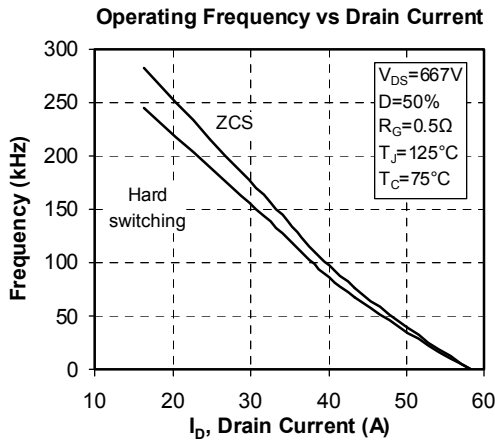
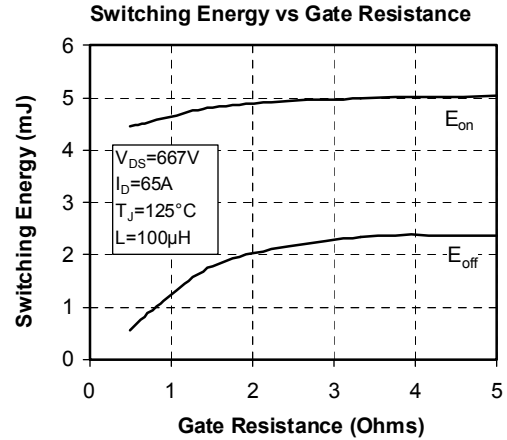
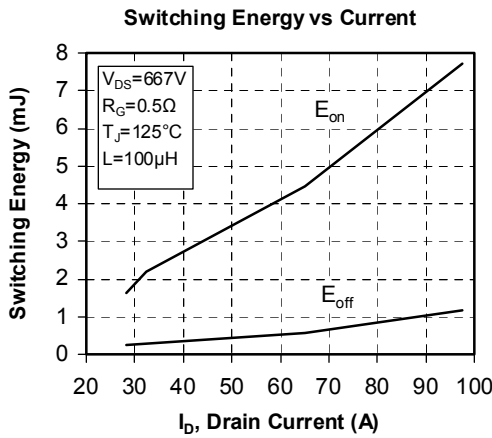
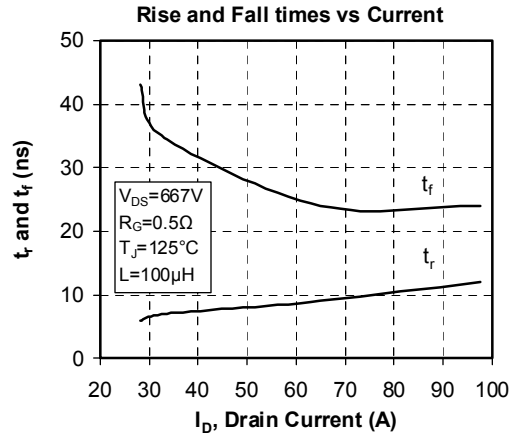
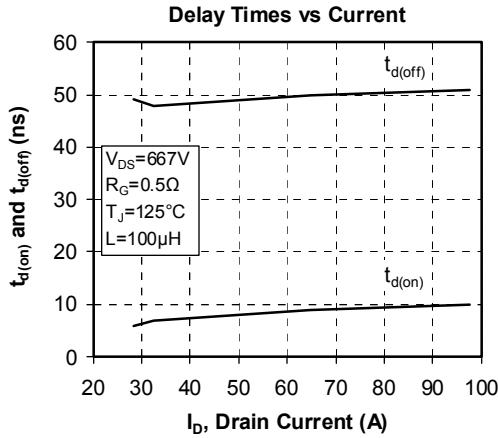
R_DS(on) vs Drain Current



DC Drain Current vs Case Temperature







APT reserves the right to change, without notice, the specifications and information contained herein

APT's products are covered by one or more of U.S. patents 4,895,810 5,045,903 5,089,434 5,182,234 5,019,522 5,262,336 6,503,786 5,256,583 4,748,103 5,283,202 5,231,474 5,434,095 5,528,058 and foreign patents. U.S. and Foreign patents pending. All Rights Reserved.