

Single-Phase Full-Wave Motor Driver with Built-in Hall Sensor

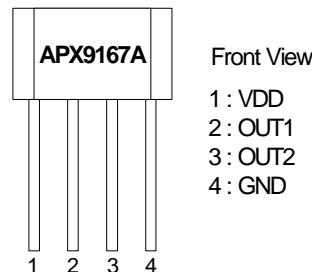
Features

- On-Chip Hall Sensor
- Silent Driver
- Lower Speed Under 7.5V (Typ.) VDD Supply Voltage
- High Sensitivity Hall Effect Sensor IC: ±25G(Type.)
- Built-in Lock Protection and Auto Restart Function
- Built-In Thermal Protection Circuit
- 4 Pin TO92-M4 Package
- Lead Free and Green Devices Available
(RoHS Compliant)

General Description

The APX9167A is an integrated Hall Effect Sensor IC designed for electric commutation of single-phase DC brushless motor applications. In DC fan applications, the APX9167A internal PWM will be enabled under 7.5V (typ.) VDD supply voltage for lower speed application. The device is built-in lock protection. When fan is locked, the device will enter the lock protection mode. It is also with thermal shutdown function. The APX9167A is available in a low cost TO92-M4 package.

Pin Description



Applications

- Brushless DC Fans
- Brushless DC Motors

Ordering and Marking Information

APX9167A		Assembly Material Handling Code Temperature Range Package Code	Package Code E : TO - 92M4 Temperature Range I : -40 to 105 °C Handling Code PB : Plastic Bag Assembly Material G : Halogen and Lead Free Device
APX9167A E :	APX 9167A XXXXX	XXXXXX - Date Code	

Note: ANPEC lead-free products contain molding compounds/die attach materials and 100% matte tin plate termination finish; which are fully compliant with RoHS. ANPEC lead-free products meet or exceed the lead-free requirements of IPC/JEDEC J-STD-020D for MSL classification at lead-free peak reflow temperature. ANPEC defines "Green" to mean lead-free (RoHS compliant) and halogen free (Br or Cl does not exceed 900ppm by weight in homogeneous material and total of Br and Cl does not exceed 1500ppm by weight).

ANPEC reserves the right to make changes to improve reliability or manufacturability without notice, and advise customers to obtain the latest version of relevant information to verify before placing orders.

Absolute Maximum Ratings ($T_A = 25^\circ\text{C}$ unless otherwise noted) (Note 1)

Symbol	Parameter	Rating	Unit
V_{DD}	VDD Pin Supply Voltage (VDD to GND)	-0.3 to 20	V
V_{OUT}	Output Pin (OUT1, OUT2) Output Voltage	-0.3 to 20	V
I_{OUT}	Output Pin (OUT1, OUT2) Maximum Output Current – Continuous	450	
	Hold	1000	mA
	Peak	1200	
T_J	Maximum Junction Temperature	150	$^\circ\text{C}$
T_{STG}	Storage Temperature	-65 to 150	$^\circ\text{C}$
T_{SOR}	Maximum Lead Soldering Temperature, 10 Seconds	260	$^\circ\text{C}$

Note1: Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

Thermal Characteristics

Symbol	Parameter	Typical Value	Unit
R_{THJA}	Thermal Resistance-Junction to Ambient (Note 2) TO-92M4	131	$^\circ\text{C/W}$
P_D	Power Dissipation, $T_A = 25^\circ\text{C}$ TO-92M4	950	mW

Note 3 : The maximum allowable power dissipation at any T_A (ambient temperature) is calculated using: $P_D \text{ (max)} = (T_J - T_A) / \theta_{JA}; T_J = 150^\circ\text{C}$. Exceeding the maximum allowable power dissipation will result in excessive die temperature.

Recommended Operating Conditions

Symbol	Parameter	Range	Unit
V_{DD}	VDD Supply Voltage	2.5 to 17	V
T_A	Operating Ambient Temperature	-40 to 105	$^\circ\text{C}$
T_J	Junction Temperature	-40 to 125	$^\circ\text{C}$

Electrical Characteristics ($T_A = 25^\circ\text{C}$, $V_{DD} = 12\text{V}$ unless otherwise noted)

Symbol	Parameter	Test Conditions	APX9167A			Unit
			Min.	Typ.	Max.	
I_{DD}	VDD Supply Current	$V_{DD}=17\text{V}$, no load		5.5	6.5	mA
V_O	Output Driver Saturation Voltage	$I_{OUT}=400\text{mA}$, Upper and Lower total	-	0.65	0.8	V
V_{TH1}	High Side VDD Threshold Voltage	VDD falling	7	7.5	8	V
V_{HYS}	VTH1 Hysteresis		-	-	0.5	V

Electrical Characteristics(Cont.) ($T_A=25^\circ C$, $V_{DD}=12V$ unless otherwise noted)

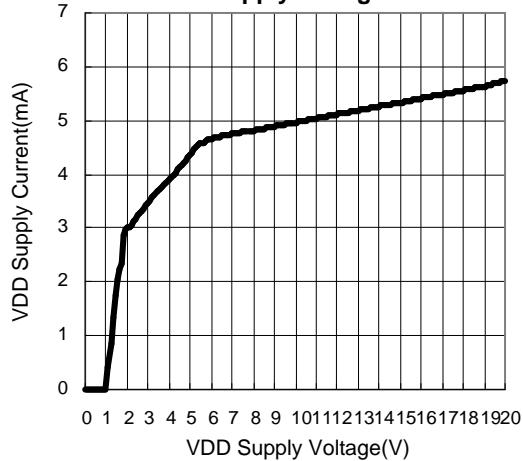
Symbol	Parameter	Test Conditions	APX9167A			Unit
			Min.	Typ.	Max.	
T_{ON}	Lock Detection On Time		-	0.4	-	sec
T_{OFF}	Lock Detection Off Time		-	4	-	sec
I_{LIM}	Internal Current Limit (50% Output)		-	1100	-	mA
OTS	Over Temperature Shutdown Threshold		-	165	-	°C
	Over Temperature Shutdown Hysteresis		-	30	-	°C

Magnetic Characteristics ($T_A=25^\circ C$, $V_{DD}=12V$ unless otherwise noted)

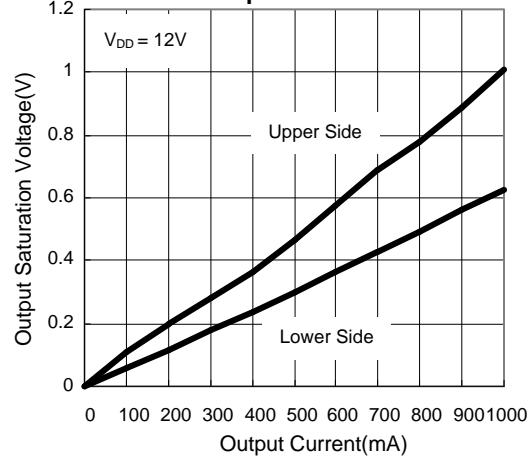
Symbol	Parameter	Test Conditions	APX9167A			Unit
			Min.	Typ.	Max.	
Bop	Magnetic Operation Point		5	25	50	Gauss
Brp	Magnetic Release Point		-50	-25	-5	Gauss
Bphys	Magnetic Hysteresis		-	50	-	Gauss

Typical Operating Characteristics

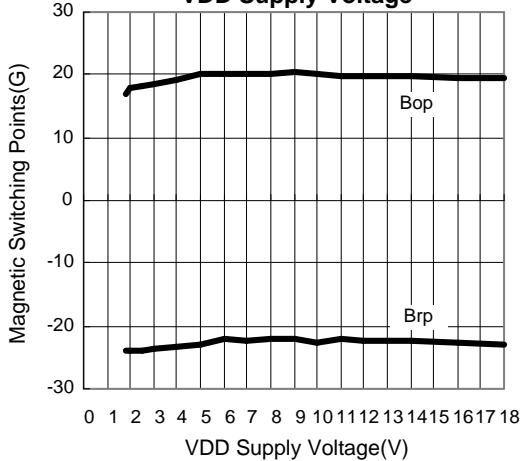
VDD Supply Current vs. VDD Supply Voltage



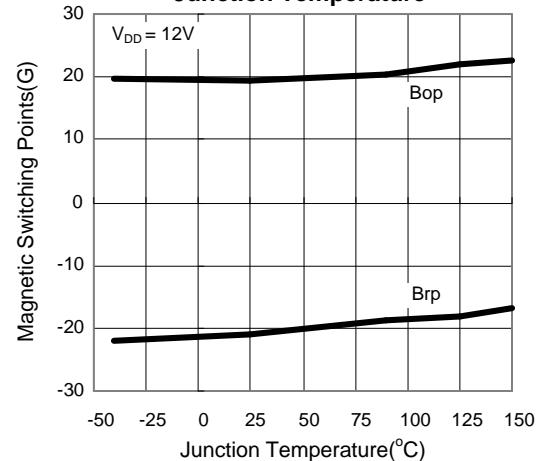
Output Saturation Voltage vs. Output Current



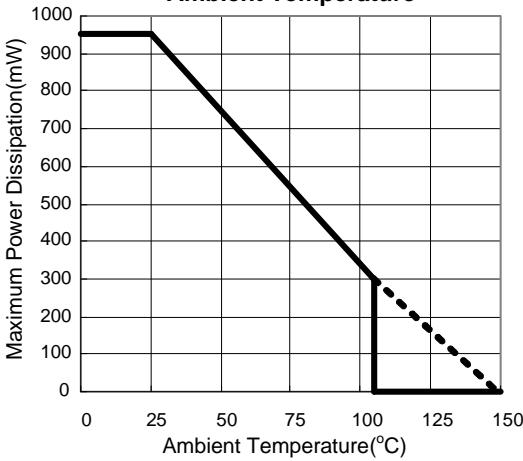
Magnetic Switching Points vs. VDD Supply Voltage



Magnetic Switching Points vs. Junction Temperature



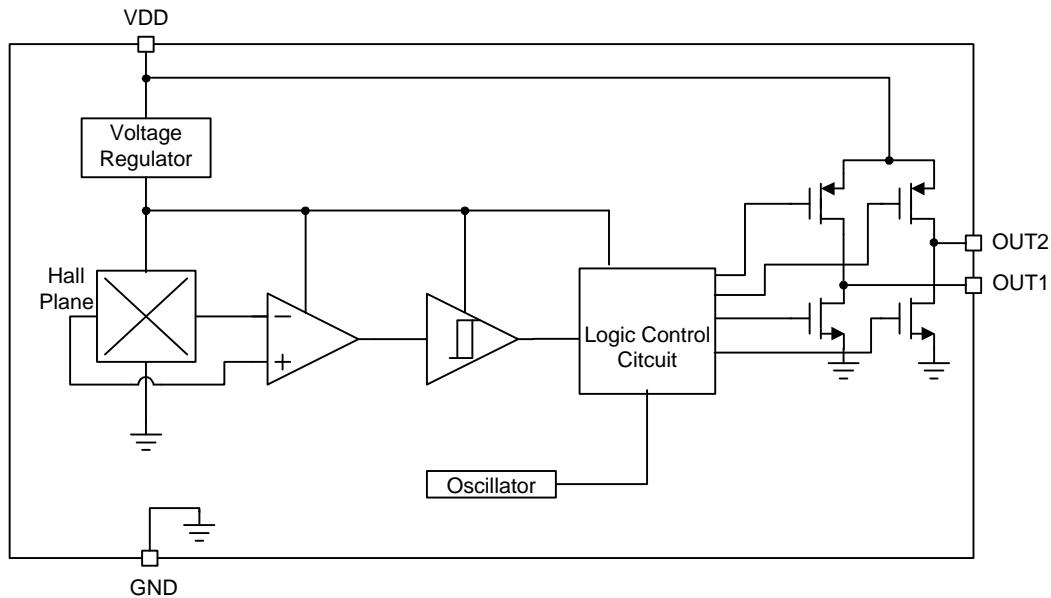
Maximum Power Dissipation vs. Ambient Temperature



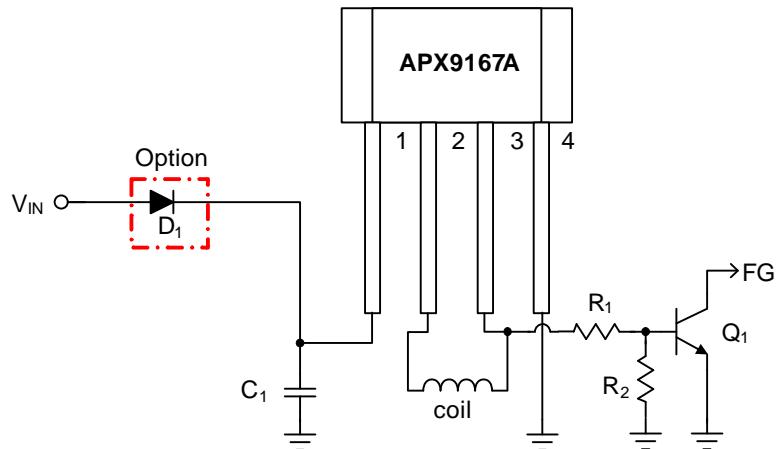
Pin Description

PIN		FUNCTION
NO.	NAME	
1	VDD	Supply Voltage Input.
2	OUT1	H-bridge output connection.
3	OUT2	H-bridge output connection.
4	GND	Ground of the IC.

Block Diagram



Typical Application Circuit



Notes: D₁ is to protect IC when reverse power input

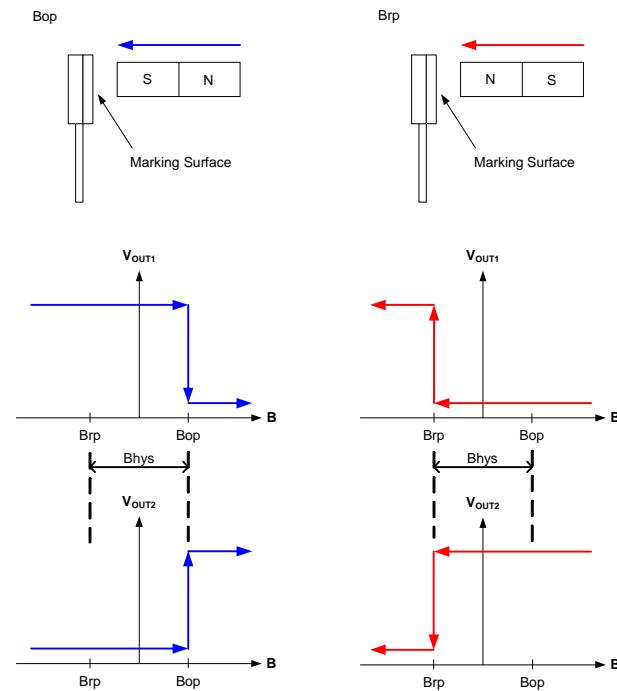
C₁ must use at least 1μF/25V capacitor and place as close to IC as possible.

R₁, R₂ and Q₁ are for FG function application circuit(by OUT2 only).

Function Description

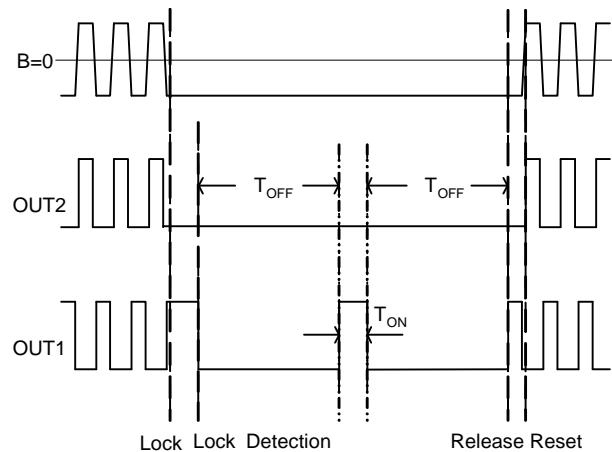
Output Switch Principle

The APX9167A built in a Hall-effect sensor plane to sense the vertical magnetic flux density (B). There are two output drivers in APX9167A to drive single-phase DC brushless motor. When the N pole magnetic field close to the IC marking surface and the magnetic flux density higher than operate point (B_{op}), the OUT1 pin output will turn to LOW and the OUT2 pin output will turn to HIGH. When the N pole magnetic field far away the IC marking surface and S pole magnetic field close to the IC marking surface until the magnetic flux density higher than release point (B_{rp}), the OUT1 pin output will turn HIGH and the OUT2 pin output will turn LOW.



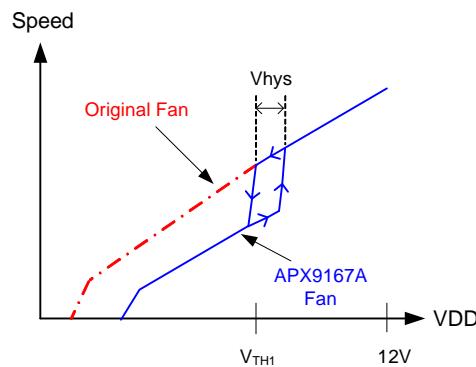
Lockup Protection and Automatic Restart

The APX9167A detects the rotation of the motor by internal hall sensor signal, and adjusts lock detection ON time (T_{on}) and lock detection OFF time (T_{off}) by internal counter.



Voltage Speed Control Function

In some DC fan applications, the system adjusts the input power voltage to control fan speed. The APX9167A internal circuit controls the lower fan speed in the low voltage below V_{TH1} . When adjusts the VDD pin voltage from 12V to V_{TH1} (7.5V typical), the APX9167A will apply lower current to coils and the fan speed will be lower than original fan. A slower fan speed will have a smaller fan noise.

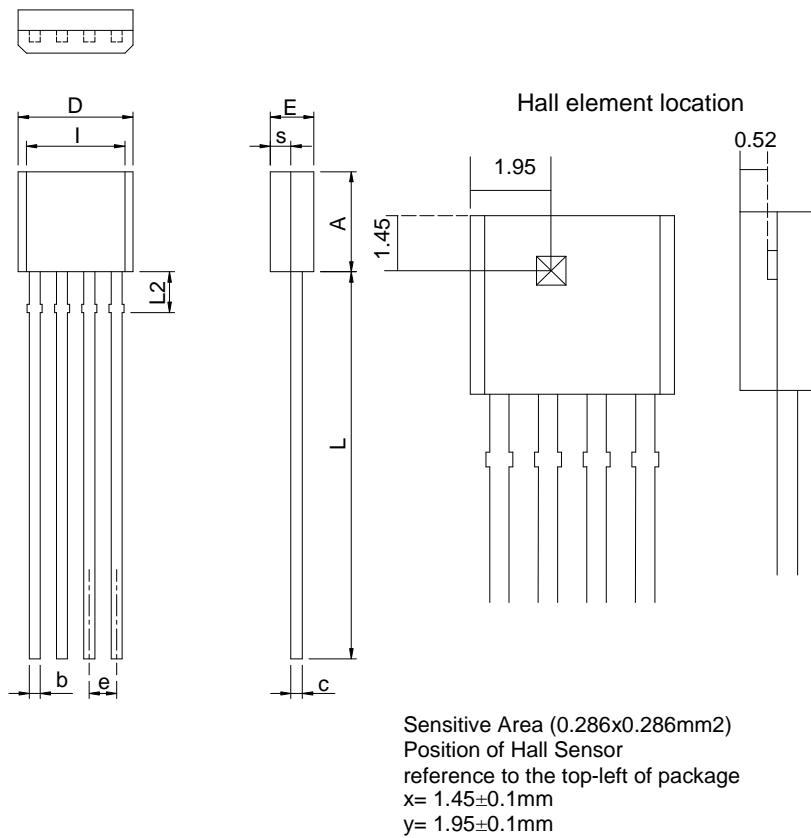


Thermal Protection

The APX9167A has a thermal protection. When the internal junction temperature reaches 165°C, the output devices will be switched off. When the IC's junction temperature cools by 30°C, the thermal sensor will turn the output devices on again, resulting in a pulsed output during continuous thermal protection.

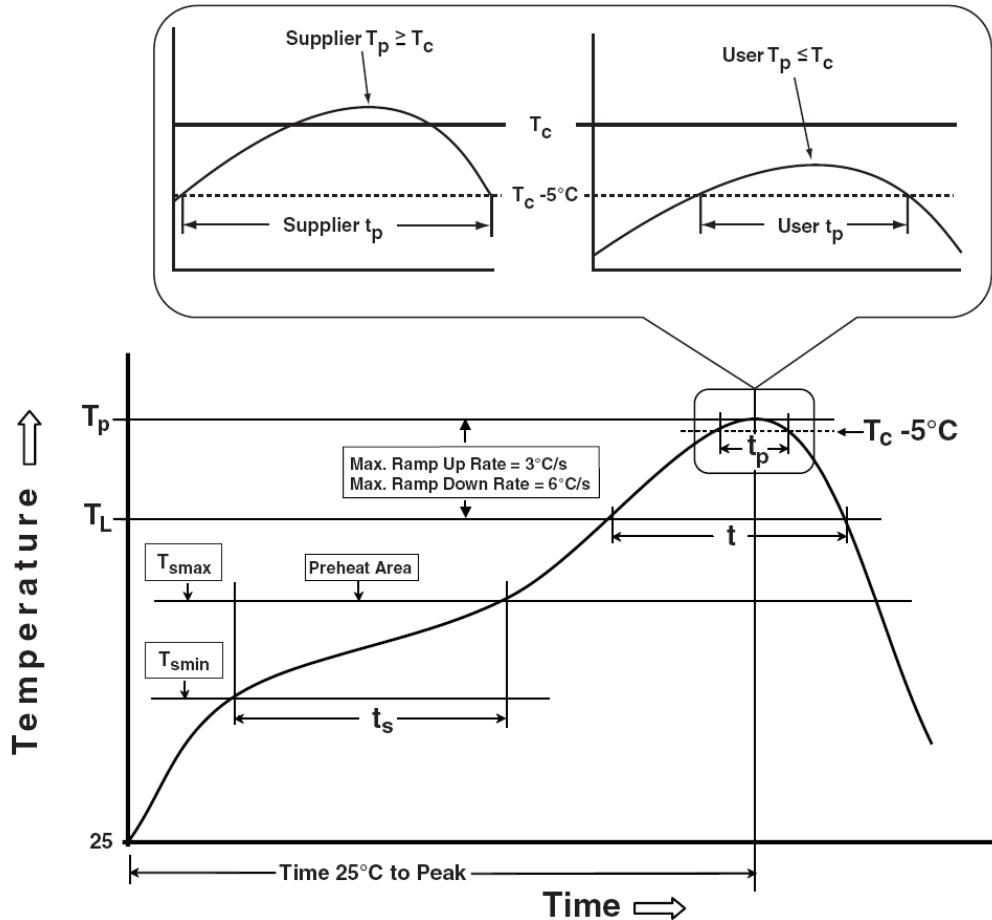
Package Information

TO-92M4



SYMBOL	TO-92M4			
	MILLIMETERS		INCHES	
	MIN.	MAX.	MIN.	MAX.
A	3.45	3.75	0.136	0.148
b	0.31	0.56	0.012	0.022
c	0.35	0.51	0.014	0.020
D	4.98	5.32	0.196	0.209
e	1.27 BSC		0.050 BSC	
E	1.40	1.80	0.055	0.071
I	3.78	4.67	0.149	0.184
L	13.50	15.80	0.531	0.622
L2	1.34	1.65	0.053	0.065
s	0.50	0.90	0.020	0.035

Classification Profile



Classification Reflow Profiles

Profile Feature	Sn-Pb Eutectic Assembly	Pb-Free Assembly
Preheat & Soak Temperature min (T_{smin}) Temperature max (T_{smax}) Time (T_{smin} to T_{smax}) (t_s)	100 °C 150 °C 60-120 seconds	150 °C 200 °C 60-120 seconds
Average ramp-up rate (T_{smax} to T_p)	3 °C/second max.	3°C/second max.
Liquidous temperature (T_L) Time at liquidous (t_L)	183 °C 60-150 seconds	217 °C 60-150 seconds
Peak package body Temperature (T_p)*	See Classification Temp in table 1	See Classification Temp in table 2
Time (t_p)** within 5°C of the specified classification temperature (T_c)	20** seconds	30** seconds
Average ramp-down rate (T_p to T_{smax})	6 °C/second max.	6 °C/second max.
Time 25°C to peak temperature	6 minutes max.	8 minutes max.

* Tolerance for peak profile Temperature (T_p) is defined as a supplier minimum and a user maximum.
** Tolerance for time at peak profile temperature (t_p) is defined as a supplier minimum and a user maximum.

Classification Reflow Profiles (Cont.)

Table 1. SnPb Eutectic Process – Classification Temperatures (Tc)

Package Thickness	Volume mm ³ <350	Volume mm ³ ≥350
<2.5 mm	235 °C	220 °C
≥2.5 mm	220 °C	220 °C

Table 2. Pb-free Process – Classification Temperatures (Tc)

Package Thickness	Volume mm ³ <350	Volume mm ³ 350-2000	Volume mm ³ >2000
<1.6 mm	260 °C	260 °C	260 °C
1.6 mm – 2.5 mm	260 °C	250 °C	245 °C
≥2.5 mm	250 °C	245 °C	245 °C

Reliability Test Program

Test item	Method	Description
SOLDERABILITY	JESD-22, B102	5 Sec, 245°C
HOLT	JESD-22, A108	1000 Hrs, Bias @ T _f =125°C
PCT	JESD-22, A102	168 Hrs, 100%RH, 2atm, 121°C
TCT	JESD-22, A104	500 Cycles, -65°C~150°C
HBM	MIL-STD-883-3015.7	VHBM 2KV
MM	JESD-22, A115	VMM 200V
Latch-Up	JESD 78	10ms, 1 _{tr} 100mA

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