



High Current Button Rectifiers

FEATURES

- Diffused junction
- Low leakage
- High surge capability
- Low cost construction utilizing void-free molded plastic technique
- Compliant to RoHS Directive 2011/65/EU and in accordance to WEEE 2002/96/EC
- Halogen-free according to IEC 61249-2-21 definition



ARS





MECHANICAL DATA

Case: ARS

Molding compound, UL flammability classification rating 94V-0

Base P/N with suffix "G" on packing code - green compound (halogen-free)

Terminal: Matte tin plated leads, solderable per JESD22-B102

Meet JESD 201 class 1A whisker test **Weight:** 1.68 g (approximately)

| MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS (T _A =25°C unless otherwise noted) | | | | | | | | | | |
|--|--------------------|--------------|-----|-----|-----|-----|------|------|------|--|
| PARAMETER | SYMBOL | ARS | ARS | ARS | ARS | ARS | ARS | ARS | S | |
| PARAIVIETER | SYMBOL | 35A | 35B | 35D | 35G | 35J | 35K | 35M | UNIT | |
| Maximum repetitive peak reverse voltage | V_{RRM} | 50 | 100 | 200 | 400 | 600 | 800 | 1000 | V | |
| Maximum RMS voltage | V_{RMS} | 35 | 70 | 140 | 280 | 420 | 560 | 700 | V | |
| Maximum DC blocking voltage | V_{DC} | 50 | 100 | 200 | 400 | 600 | 800 | 1000 | V | |
| Maximum average forward rectified current | I _{F(AV)} | 35 | | | Α | | | | | |
| Peak forward surge current, 8.3 ms single half sine-wave superimposed on rated load | I _{FSM} | 500 | | | | Α | | | | |
| Maximum instantaneous forward voltage (Note 1) @ 35 A | V _F | 1.0 | | | | V | | | | |
| Maximum reverse current @ Rated VR T _J =25 °C | I _R | 5 250 | | | | | | μA | | |
| T _J =125 °C | | | | | | | | | | |
| Typical reverse recovery time (Note 2) | trr | 3 | | | | | | μs | | |
| Typical junction capacitance (Note 3) | Cj | 300 | | | | | | pF | | |
| Typical Thermal Resistance | $R_{	heta JC}$ | 1 | | | | | °C/W | | | |
| Operating junction temperature range | T _J | - 50 to +175 | | | οС | | | | | |
| Storage temperature range | T _{STG} | - 50 to +175 | | | | | οС | | | |

Note 1: Pulse test with PW=300µs, 1% duty cycle

Note 2: Reverse Recovery Time Test Conditions: I_F =0.5A, I_R =1.0A, I_{RR} =0.25A

Note 3: Measured at 1 MHz and Applied Reverse Voltage of 4.0V D.C.



| ORDERING INFORMATION | | | | | |
|----------------------|--------------|------------------------|---------|----------------------|--|
| PART NO. | PACKING CODE | PACKING CODE SUFFIX | PACKAGE | PACKING | |
| ARS35x (Note 1) | В0 | G | ARS | 1,000 / Bulk packing | |

Note 1: "x" defines voltage from 50V (ARS35A) to 1000V (ARS35M)

| EXAMPLE | | | | | | |
|---------------|----------|--------------|------------------------|----------------|--|--|
| PREFERRED P/N | PART NO. | PACKING CODE | PACKING CODE SUFFIX | DESCRIPTION | | |
| ARS35M B0 | ARS35M | B0 | | | | |
| ARS35M B0G | ARS35M | B0 | G | Green compound | | |

RATINGS AND CHARACTERISTICS CURVES

(T_A=25°C unless otherwise noted)

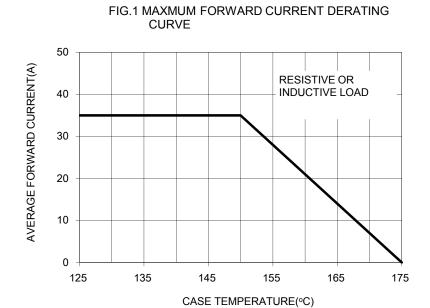
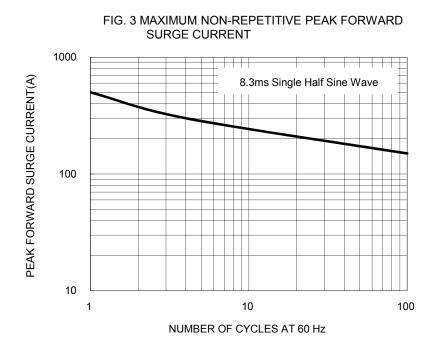


FIG. 2 TYPICAL REVERSE CHARACTERISTICS 100 INSTANTANEOUS REVERSE CURRENT (µA) T_J=125°C 10 1 T_J=25°C 0.1 0 140 40 60 80 100 120 PERCENT OF RATED PEAK REVERSE VOLTAGE (%)



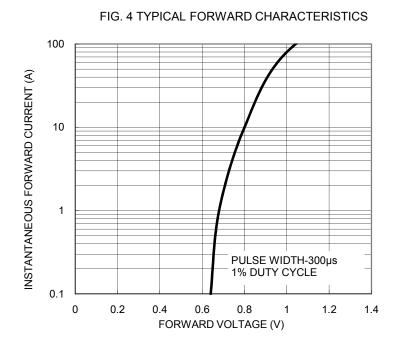




FIG. 5 TYPICAL JUNCTION CAPACITANCE

1000

(e)

1000

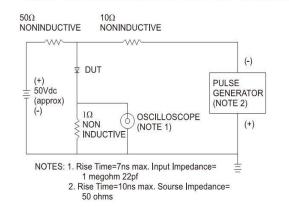
f=1.0MHz

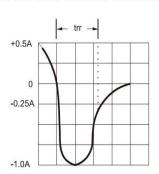
Vsig=50m Vp-p

10

REVERSE VOLTAGE (V)

FIG.6- REVERSE RECOVERY TIME CHARACTERISTIC AND TEST CIRCUIT DIAGRAM



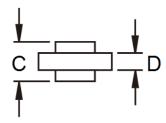


PACKAGE OUTLINE DIMENSIONS

ARS



| DIM. | Unit | (mm) | Unit (inch) | | | |
|--------|------|------|-------------|-------|--|--|
| DIIVI. | Min | Max | Min | Max | | |
| Α | 5.50 | 5.70 | 0.217 | 0.224 | | |
| В | 8.30 | 8.90 | 0.327 | 0.350 | | |
| С | 6.00 | 6.40 | 0.236 | 0.252 | | |
| D | 4.20 | 4.70 | 0.165 | 0.185 | | |



MARKING DIAGRAM



P/N = Specific Device Code G = Green Compound

YW = Date Code F = Factory Code





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