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Advanced Schottky Barrier Diodes



Features:

- Designed for mounting on small surface
- Low stored charge
- Majority carrier conduction

Mechanical data:

- Case: 0805(2012) Standard package, molded plastic
- Terminals : Solder plated, solderable per MIL-STD-750, method 2026.
- Polarity: Indicated by cathode band
- Mounting position: Any
- Weight: 4.8mg (approximately)

Absolute Maximum Ratings(Ta=25°C)

Characteristics	Symbol	Value	Unit
Continuous Reverse Voltage	VR	30	V
Average Rectified Current	Io	200	mA
Forward Surge Current @ 8.3ms single half sine-wave superimposed on rated load(JEDEC method)	IFSM	1.5	А
Capacitance between Terminals @ f=1MHz and applied 10VDC Reverse Voltage	Ст	20	pF
Junction Temperature	Tj	-40 to +125	°C
Storage Temperature Range	Tstg	-40 to +125	°C

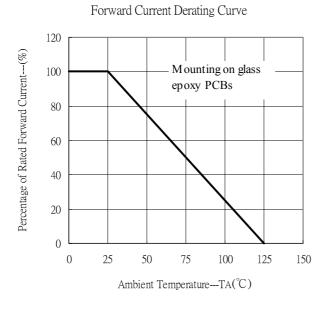
Electrical Characteristics (TA=25°C, unless otherwise noted)

Parameter	Condition	Symbol	Min	Тур	Max	Unit
Forward Voltage	$I_F = 200 \text{mAdc}$	V _F	-	-	0.55	V
Reverse Current	VR = 30VDC	I _R	-	-	15	μΑ

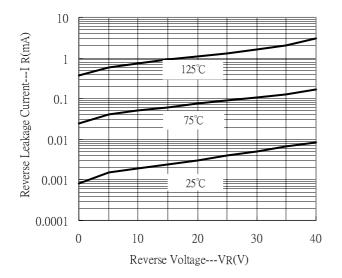


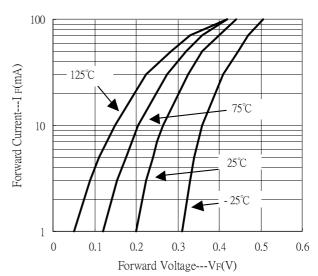
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Characteristic Curves



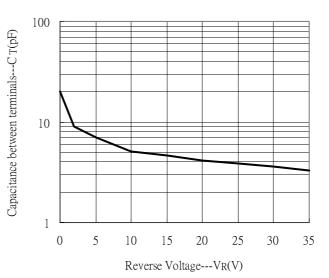
Reverse Leakage Current vs Reverse Voltage





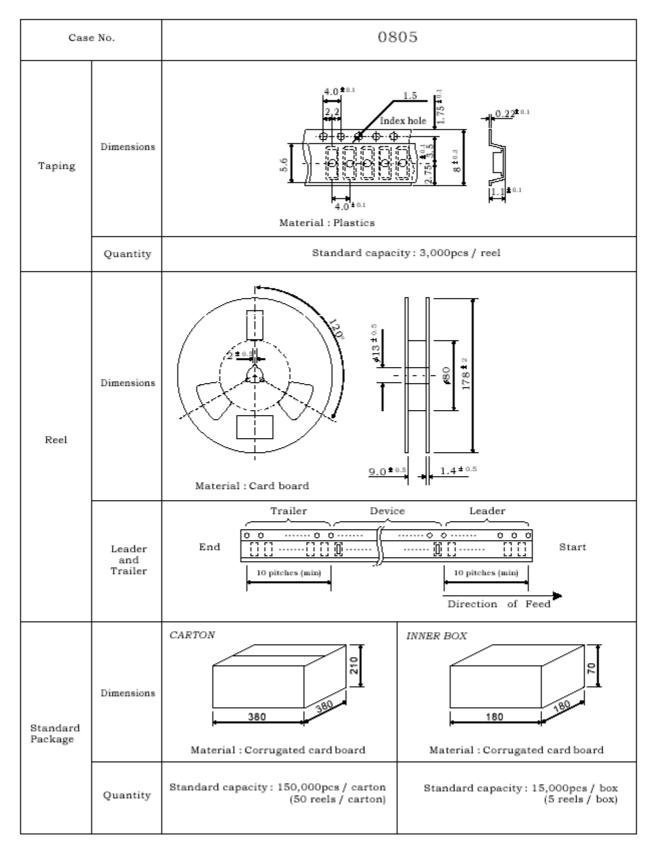
Forward Current vs Forward Voltage

Capacitance vs Reverse Voltage





Packing Information

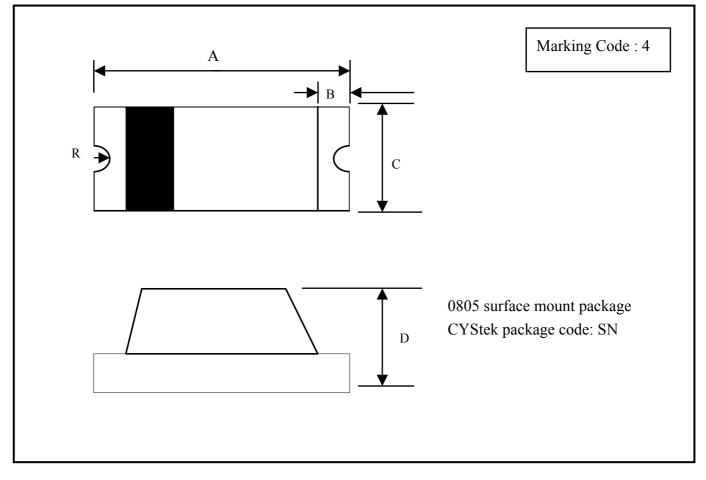




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0805(2012) Dimension



									*:Typical
DIM	Inches		Millimeters		DIM	Inches		Millimeters	
	Min.	Max.	Min.	Max.		Min.	Max.	Min.	Max.
Α	0.079	0.087	2.00	2.20	С	0.047	0.055	1.20	1.40
В	0.016	i(typ.)	0.40(typ.)		D	0.035	0.043	0.90	1.10
R	0.008	(tup.)	0.20(typ.)						
Notes : 1.Controlling dimension : millimeters. 2.Maximum lead thickness includes lead finish thickness, and minimum lead thickness is the minimum thickness of base material. 3.If there is any question with packing specification or packing method, please contact your local CYStek sales office.									

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