INNOLUX DISPLAY CORPORATION
LCD MODULE
SPECIFICATION

Customer: _____

Model Name: <u>AT056TN03 V.1</u>
SPEC NO.: <u>A056-03-TT-11</u>
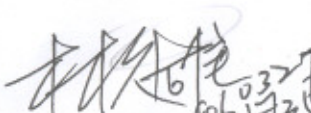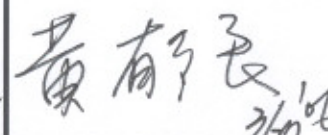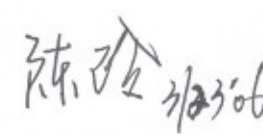Date: <u>2006/03/23</u>
Version: <u>01</u>

■ Preliminary Specification
☐ Final Specification

For Customer's Acceptance

Approved by	Comment

Approved by	Reviewed by	Prepared by
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Record of Revision

Version	Revise Date	Page	Content
1	2006/03/23		Initial Release

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Contents:

1. General Specifications

No.	Item	Specification	Remark
1	LCD size	5.6inch(Diagonal)	
2	Driver element	a-Si TFT active matrix	
3	Resolution	320X3(RGB)X234	
4	Display mode	Normally white, Transmissive	
5	Dot pitch	0.118(W)X0.362(H) mm	
6	Active area	113.28(W)X84.708(H) mm	
7	Module size	126.5(W)X100.0(H)X5.7(D) mm	Note 1
8	Surface treatment	Anti-Glare	
9	Color arrangement	RGB-stripe	
10	Interface	Analog	
11	Backlight power consumption	2.7W(Typ.)	
12	Panel power consumption	86mW(Typ.)	
13	Weight	TBD	

Note 1: Refer to Mechanical Drawing.

2. Pin Assignment

2.1.   TFT LCD Panel Driving Section

Pin No.	Symbol	I/O	Function	Remark
1	GND	P	Ground	
2	V_{CC}	P	Supply voltage for logic control circuit scan driver	
3	V_{GL}	P	Negative power for scan driver	
4	V_{GH}	P	Positive power for scan driver	
5	STVR	I/O	Vertical start pulse	Note 1
6	STVL	I/O	Vertical start pulse	Note 1
7	CKV	I	Shift clock input for scan driver	
8	U/D	I	UP/DOWN scan control input	Note 1, 2
9	OEV	I	Output enable control for scan driver	
10	V_{COM}	I	Common electrode driving signal	
11	V_{COM}	I	Common electrode driving signal	
12	L/R	I	LEFT/RIGHT scan control input	Note 1, 2
13	MOD	I	Sequential sampling and simultaneous sampling setting	
14	OEH	I	Output enable control for data driver	
15	STHL	I/O	Start pulse for horizontal scan line	Note 1
16	STHR	I/O	Start pulse for horizontal scan line	Note 1
17	CPH3	I	Sampling and shifting clock pulse for data driver	
18	CPH2	I	Sampling and shifting clock pulse for data driver	
19	CPH1	I	Sampling and shifting clock pulse for data driver	
20	V_{CC}	P	Supply voltage for logic control circuit scan driver	

| 21	GND	P	Ground	
22	V_R	I	Alternated video signal (Red)	
23	V_G	I	Alternated video signal (Green)	
24	V_B	I	Alternated video signal (Blue)	
25	AV_{DD}	P	Supply voltage for analog circuit	
26	AV_{SS}	P	Ground for analog circuit	

I: input, O: output, P: Power

Note 1: Selection of scanning mode

Setting of scan control input		IN/OUT state for start pulse				Scanning direction
U/D	L/R	STVR	STVL	STHR	STHL	
GND	V_{CC}	O	I	O	I	Up to down, left to right
V_{CC}	GND	I	O	I	O	Down to up, right to left
GND	GND	O	I	I	O	Up to down, right to left
V_{CC}	V_{CC}	I	O	O	I	Down to up, left to right

Note 2: Definition of scanning direction.
   Refer to the figure as below:
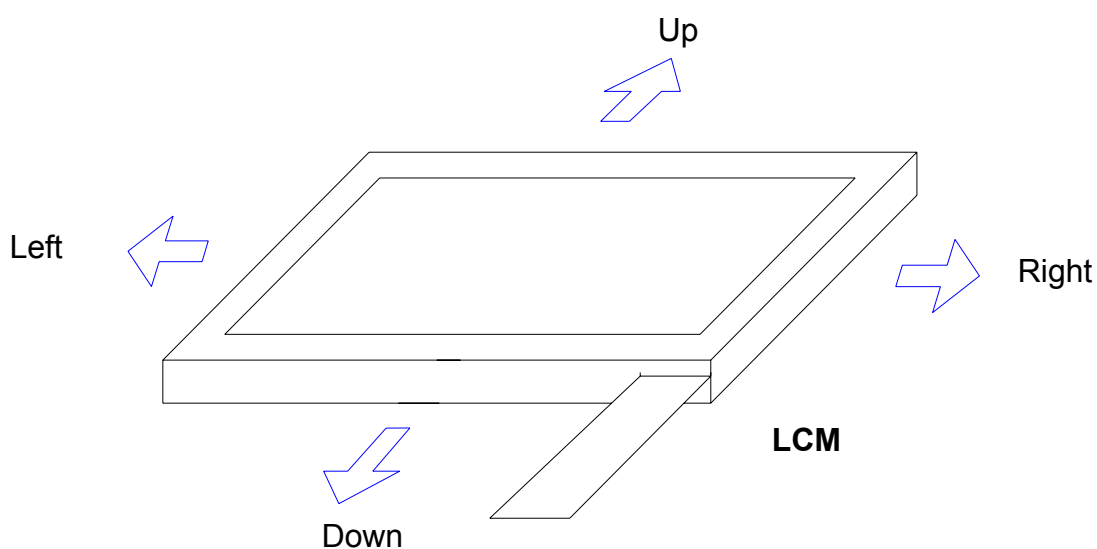
Note 3: MOD=H: Simultaneous sampling.
        MOD=L: Sequential sampling.
        Please set CPH2 and CPH3 to GND when MOD=H.

2.2.   Backlight Unit Section

Pin No.	Symbol	I/O	Function	Remark
1	HI	P	Power supply for backlight unit(High voltage)	Pink
2	NC	-		
3	GND	P	Ground for backlight unit	White

3. Operation Specifications

3.1. Absolute Maximum Rating

(GND=AV$_{SS}$=0V, Note 3)

Item	Symbol	Values		Unit	Remark
		Min.	**Max.**		
Power voltage	V$_{CC}$	-0.3	7	V	
	AV$_{DD}$	-0.3	7	V	
	V$_{GH}$	-0.3	18	V	
	V$_{GL}$	-15	0.3	V	
	V$_{GH}$-V$_{GL}$	-	33	V	
Input signal voltage	V$_i$	-0.3	AV$_{DD}$+0.3	V	Note 1
	V$_I$	-0.3	V$_{CC}$+0.3	V	Note 2
Operation temperature	T$_{OP}$	-30	85	℃	
Storage temperature	T$_{ST}$	-30	85	℃	

Note 1: V$_R$, V$_G$, V$_B$.
Note 2: STHL, STHR, OEH, L/R, CPH1~CPH3, STVR, STVL, OEV, CKV, U/D.
Note 3: The absolute maximum rating values of the module should not be exceeded.
Once exceeded absolute maximum rating values, the characteristics of the
module may not be recovered. Even in an extreme condition, may result in
module permanently destroyed.

3.1.1. Typical Operation Conditions

$(GND=AV_{SS}=0V, Note 4)$

Item	Symbol	Values			Unit	Remark
		Min.	Typ.	Max.		
Power voltage	V_{CC}	4.8	5	5.2	V	
	AV_{DD}	4.8	5	5.2	V	
	V_{GH}	14.3	15	15.7	V	
	V_{GL}	-10.5	-10	-9.5	V	
Video signal amplitude (V_R, V_G, V_B)	V_{iA}	$AV_{SS}+0.4$	-	$AV_{DD}-0.4$	V	Note 1
	V_{iAC}	-	3.5	-	V	AC component
	V_{iDC}	-	$AV_{DD}/2$	-	V	DC component
V_{COM}	V_{CAC}	-	5.4	-	V	Note 2
	V_{CDC}	1.71	1.91	2.11	V	DC component
Input logic high voltage	V_{IH}	$0.7V_{CC}$	-	V_{CC}	V	Note 3
Input logic low voltage	V_{IL}	0	-	$0.3V_{CC}$	V	

Note 1: Refer to Fig.3-3-(a).
Note 2: The brightness of LCD panel could be changed by adjusting the AC component of V_{COM}.
Note 3: STHL, STHR, OEH, L/R, CPH1~CPH3, STVR, STVL, OEV, CKV, U/D.
Note 4: GND, V_{CC}, and V_{GL} are applied to LCD first and then V_{GH} is applied.

3.1.2. Current Consumption

(GND=AV$_{SS}$=0V)

Item	Symbol	Values			Unit	Remark
		Min.	Typ.	Max.		
Current for Driver	I$_{GH}$	-	0.4	0.8	mA	V$_{GH}$ =15V
	I$_{GL}$	-	0.5	1.0	mA	V$_{GL}$ = -10V
	I$_{CC}$	-	5	8	mA	V$_{CC}$ =5V
	I$_{DD}$	-	10	20	mA	AV$_{DD}$ =5V

3.1.3. Backlight Driving Conditions

Item	Symbol	Values			Unit	Remark
		Min.	Typ.	Max.		
Lamp voltage	V$_L$	-	450	528	V$_{rms}$	Note 1,6
Lamp current	I$_L$	5.9	6	6.1	mA$_{rms}$	Note 6
Frequency	F$_L$	-	50	60	kHz	Note 3
Lamp starting voltage	V$_S$(25℃)	-	590	920	V$_{rms}$	Note 1,4,6
	V$_S$(0℃)	-	730	1160	V$_{rms}$	Note 2,4,6
Lamp life time	-	20,000	-	-	Hr	Note 5

Note 1: The ambient temperature is 25℃.
Note 2: The ambient temperature is 0℃.
Note 3: The lamp frequency should be selected as different as possible from display horizontal synchronous signal to avoid interference.
Note 4: For starting the backlight unit, the output voltage of DC/AC's transformer should be larger than the maximum lamp starting voltage.
Note 5: The "lamp life time" is defined as the module brightness decay to 50% of original brightness when the ambient temperature is 25℃ and I$_L$ =6mA.
Note 6: Measure inverter type: DA1205, C=27pF, Input Voltage=12.0V.

3.2. Timing Characteristics

3.2.1. Timing Conditions

Item	Symbol	Values			Unit	Remark
		Min.	**Typ.**	**Max.**		
Rising time	t_r	-	-	60	ns	Note 1
Falling time	t_f	-	-	60	ns	Note 1
High and low level pulse width	t_{CPH}	150	154	158	ns	CPH1~CPH3
CPH pulse duty	t_{CWH}	40	50	60	%	CPH1~CPH3
CPH pulse delay	t_{C12} t_{C23} t_{C31}	30	$t_{CPH}/3$	$t_{CPH}/2$	ns	CPH1~CPH3
STH setup time	t_{SUH}	20	-	-	ns	STHR, STHL
STH hold time	t_{HDH}	20	-	-	ns	STHR, STHL
STH pulse width	t_{STH}	-	1	-	t_{CPH}	STHR, STHL
STH period	t_H	61.5	63.5	65.5	µs	STHR, STHL
OEH pulse width	t_{OEH}	-	7	-	µs	
Sample and hold disable time	t_{DIS1}	-	55	--	µs	
OEV pulse width	t_{OEV}	-	27		µs	
CKV pulse width	t_{CKV}	16	-	40	µs	
Clean enable time	t_{DIS2}	-	16	--	µs	
Horizontal display start	t_{SH}	-	0	-	$t_{CPH}/3$	
Horizontal display timing range	t_{DH}	-	960	-	$t_{CPH}/3$	
STV setup time	t_{SUV}	400	-	-	ns	STVL, STVR
STV hold time	t_{HDV}	400	-	-	ns	STVL, STVR

STV pulse width	t_{STV}	-	-	1	t_H	STVL, STVR
Horizontal lines per field	t_V	256	262.5	268	t_H	Note 2
Vertical display start	t_{SV}	-	3	-	t_H	
Vertical display timing range	t_{DV}	-	234	-	t_H	
VCOM rising time	t_{rCOM}	-	-	5	µs	
VCOM falling time	t_{fCOM}	-	-	5	µs	
VCOM delay time	t_{DCOM}	-	-	3	µs	
RGB delay time	t_{DRGB}	-	-	1	µs	

Note 1: For all of the logic signals
Note 2: Please don't use odd horizontal lines to drive LCD panel for both odd and even
field simultaneously.

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3.2.2.   Timing Diagram
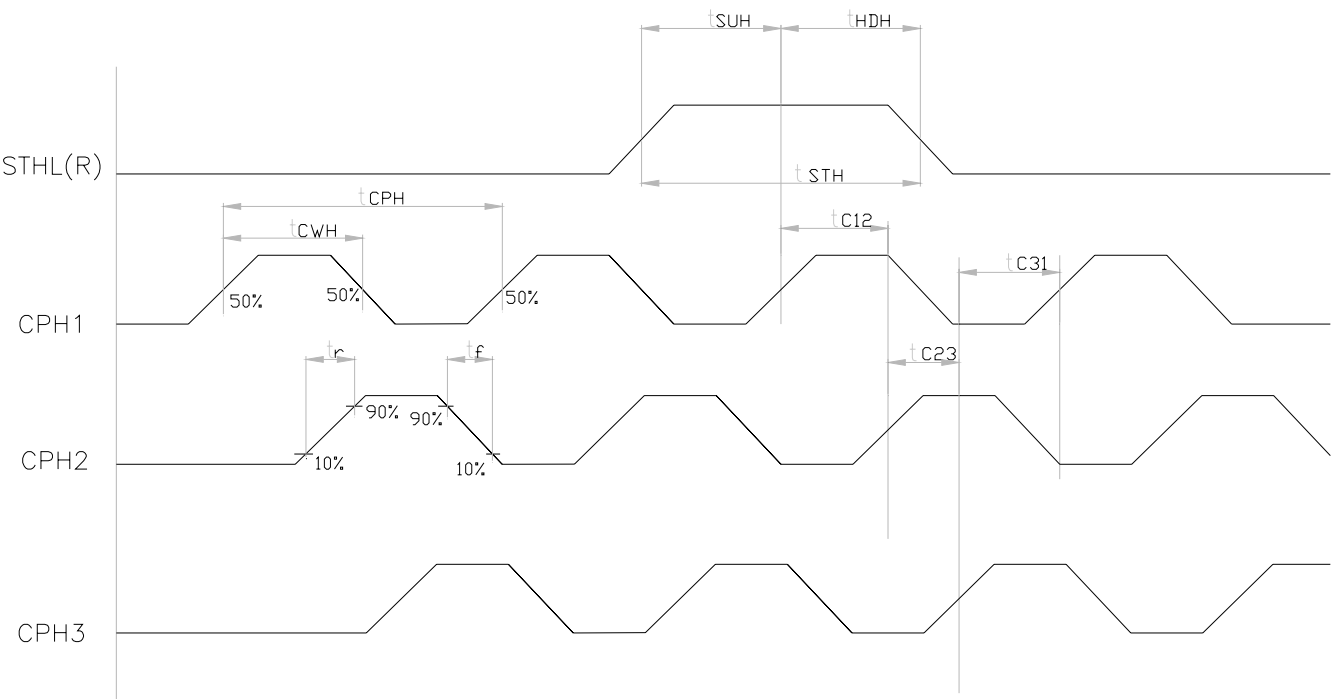
Fig.3-1 Sampling clock timing
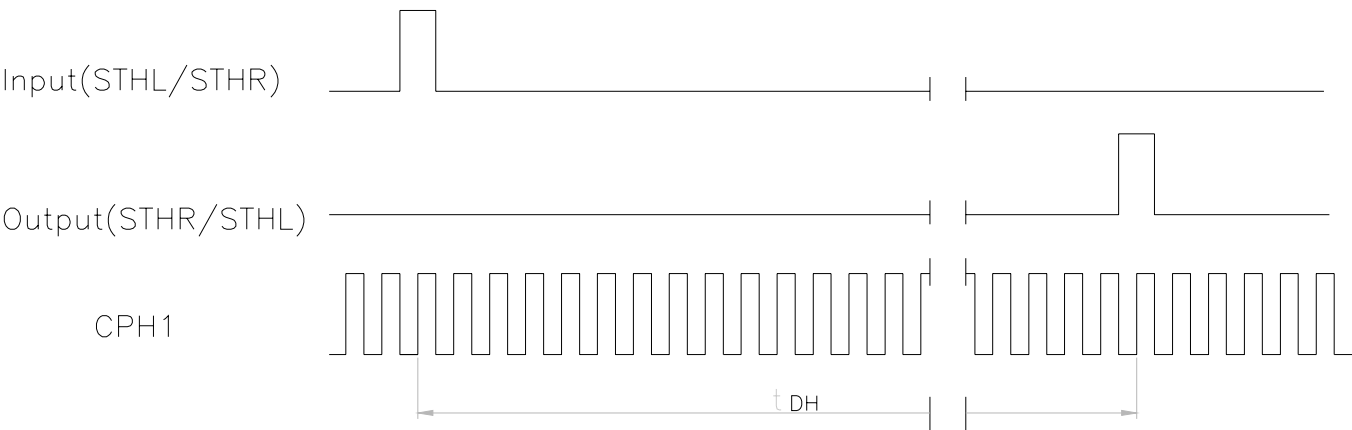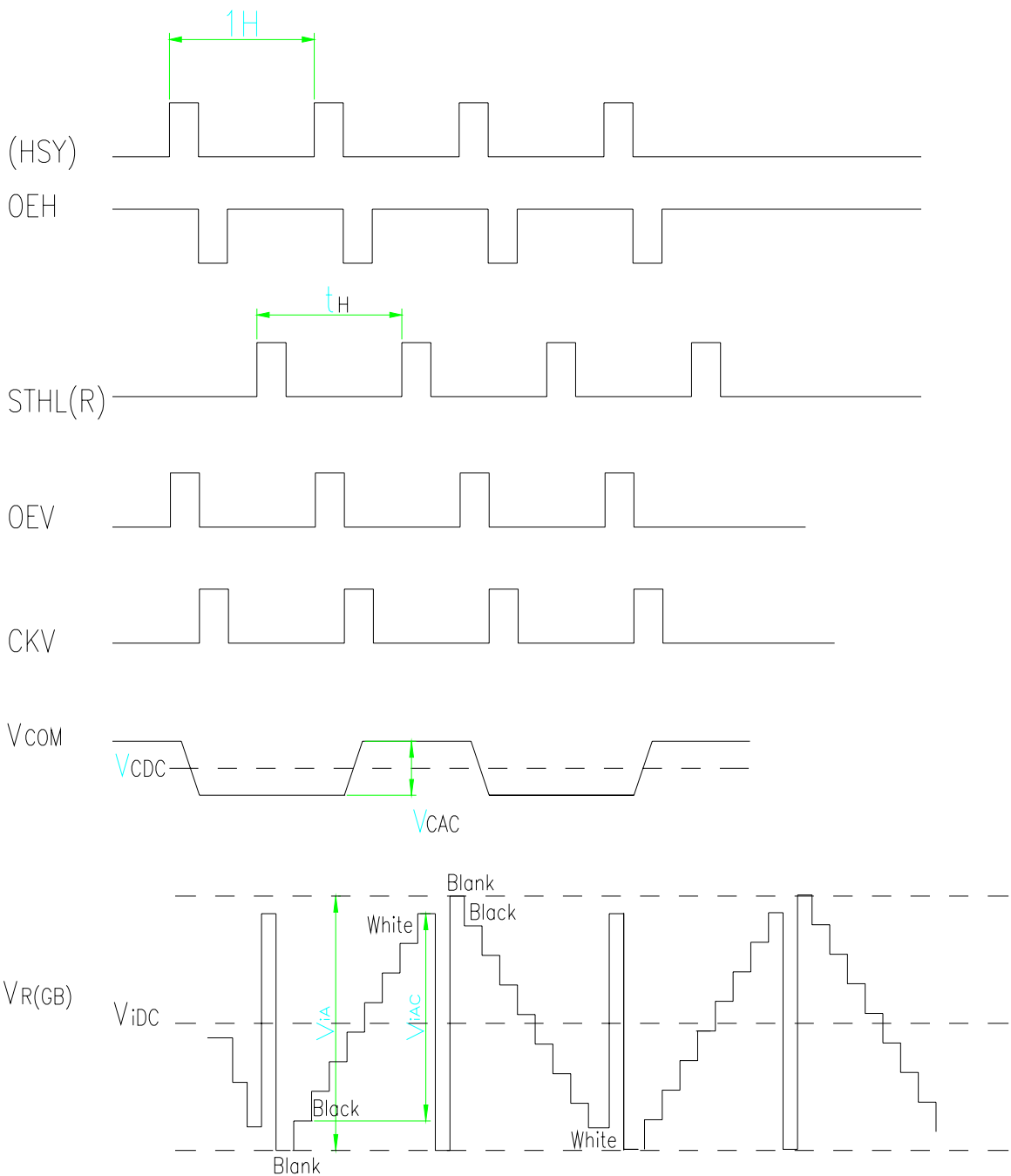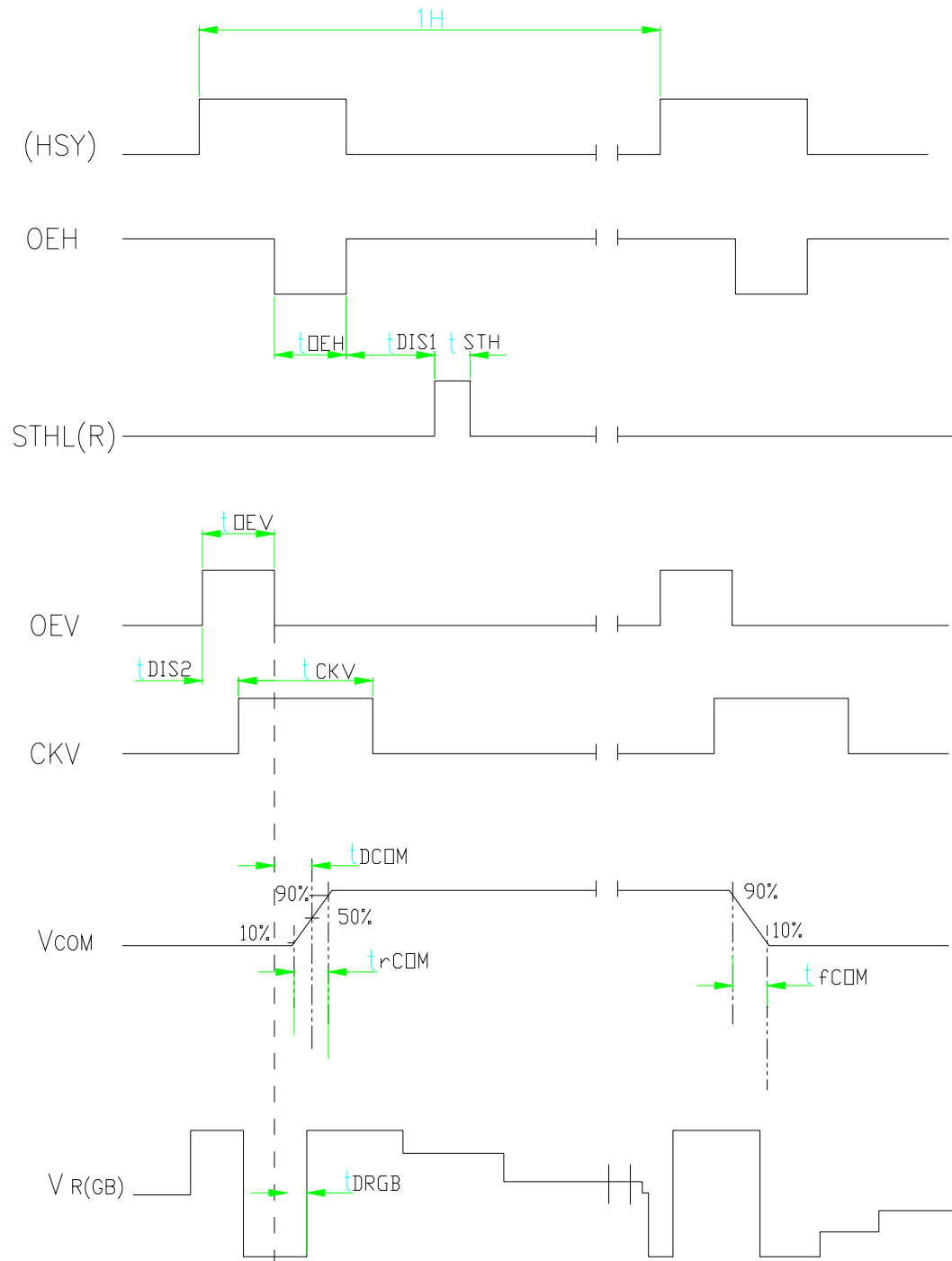

Fig.3-2 Horizontal display timing range

Fig.3-3-(a) Horizontal timing

Note: The falling edge of OEV should be synchronized with the falling edge of OEH
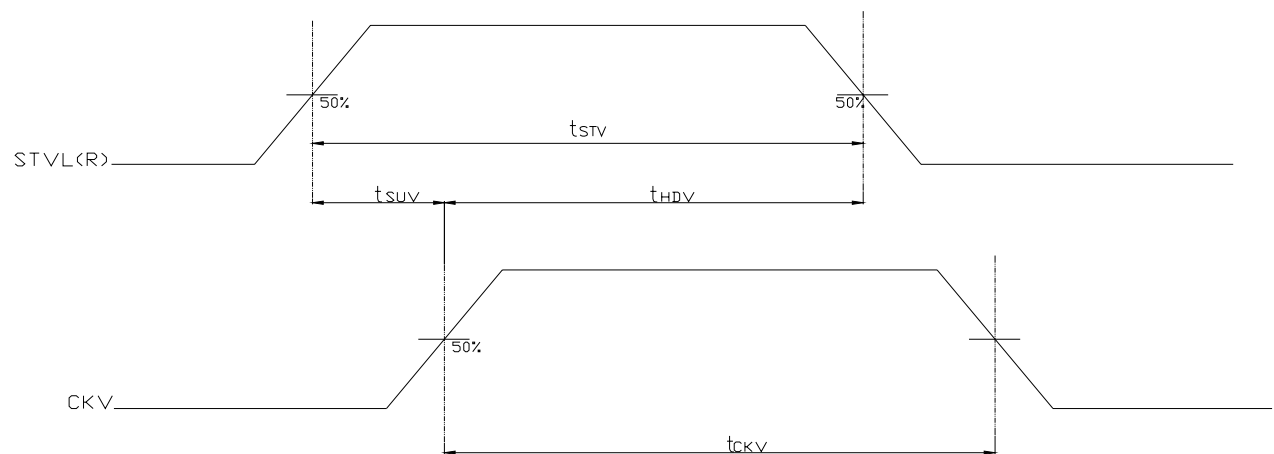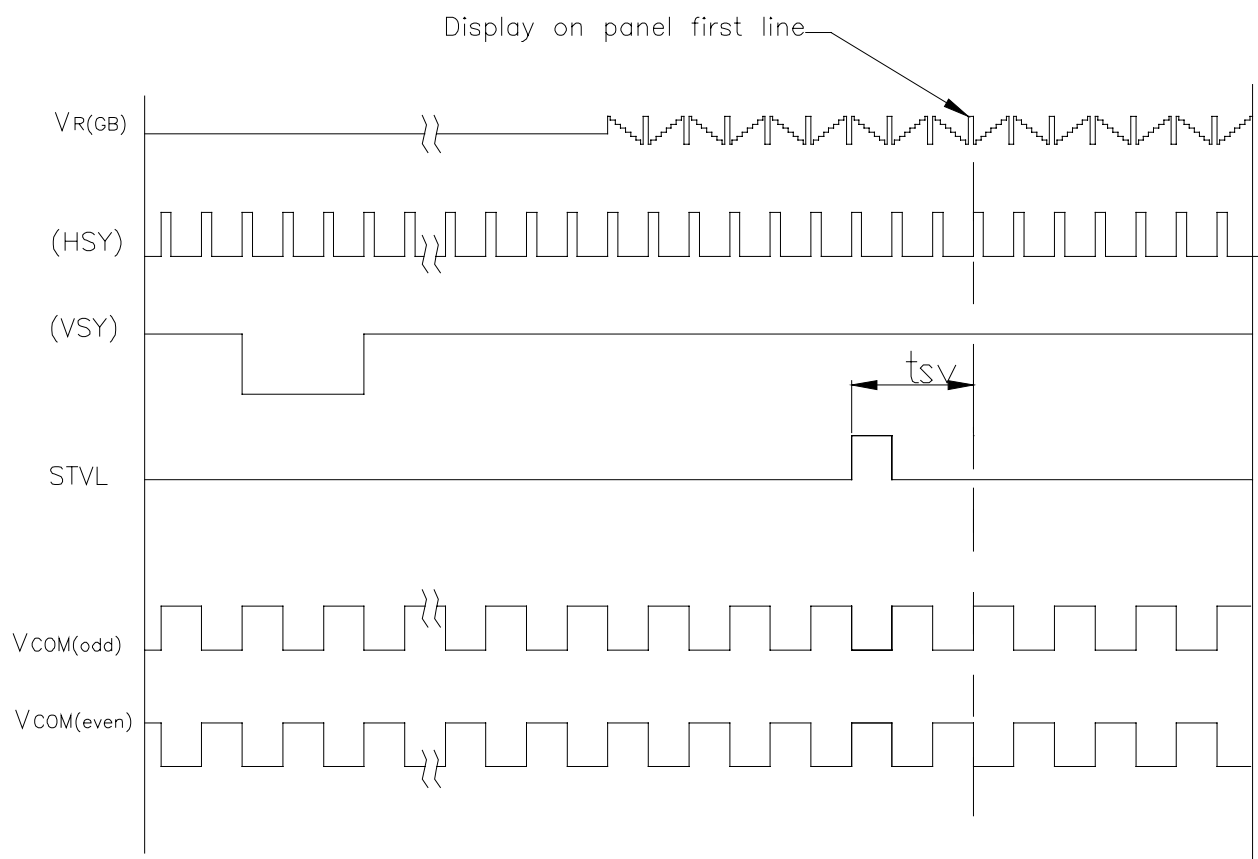Fig.3-3-(b) Detail horizontal timing

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Fig.3-4 Vertical shift clock timing

Fig.3-5(a) Vertical timing (from up to down)

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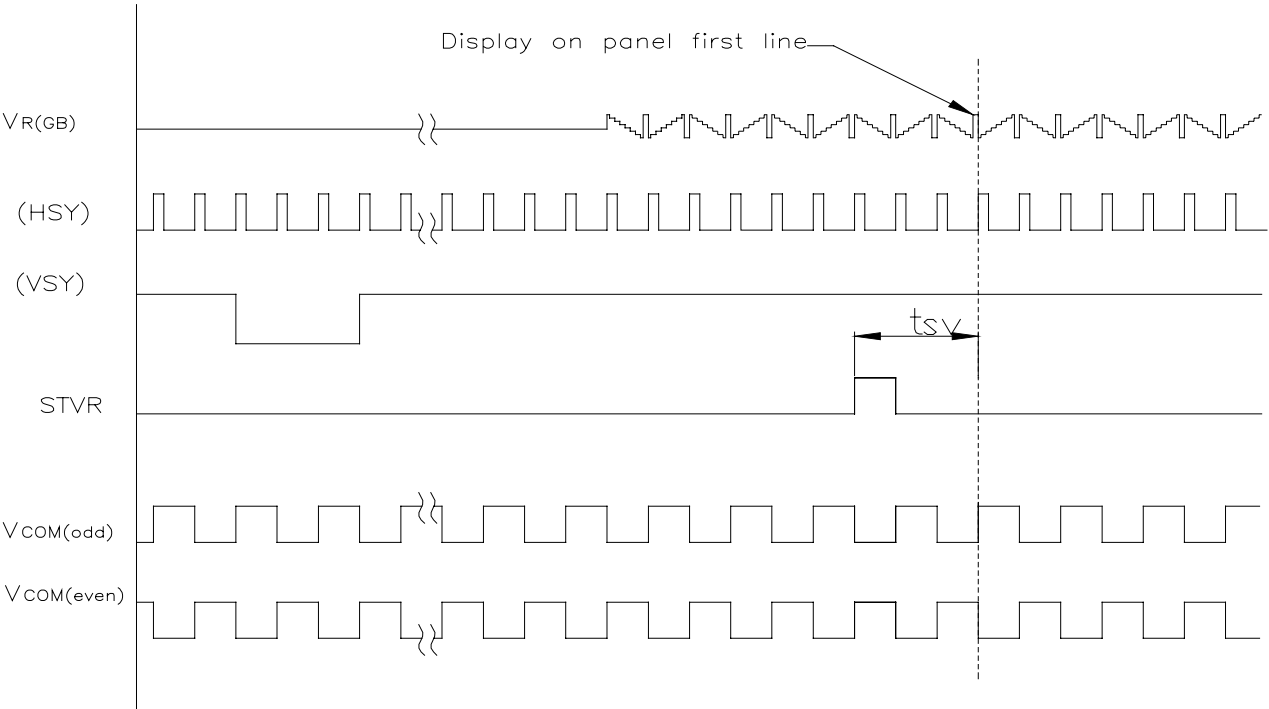
Fig.3-5(b) Vertical timing (from down to up)

4. Optical Specifications

Item	Symbol	Condition	Values			Unit	Remark
			Min.	Typ.	Max.		
Viewing angle (CR≥10)	θ_L	Φ=180°(9 o'clock)	40	45	-	degree	Note 1
	θ_R	Φ=0°(3 o'clock)	40	45	-		
	θ_T	Φ=90°(12 o'clock)	10	15	-		
	θ_B	Φ=270°(6 o'clock)	30	35	-		
Response time	T_{ON}	Normal θ=Φ=0°	-	15	30	msec	Note 3
	T_{OFF}		-	20	40	msec	Note 3
Contrast ratio	CR		250	300	-	-	Note 4
Color chromaticity	W_X		(0.27)	(0.32)	(0.37)	-	Note 2 Note 5 Note 6
	W_Y		(0.30)	(0.35)	(0.40)		
Luminance	L		200	250		cd/m²	Note 6
Luminance uniformity	Y_U		70	75	-	-	Note 7

Test Conditions:
1. V_{CC}=5V, AV_{DD}=5V, I_L=6mArms (Backlight current), the ambient temperature is 25℃.
2. The test systems refer to Note 2.

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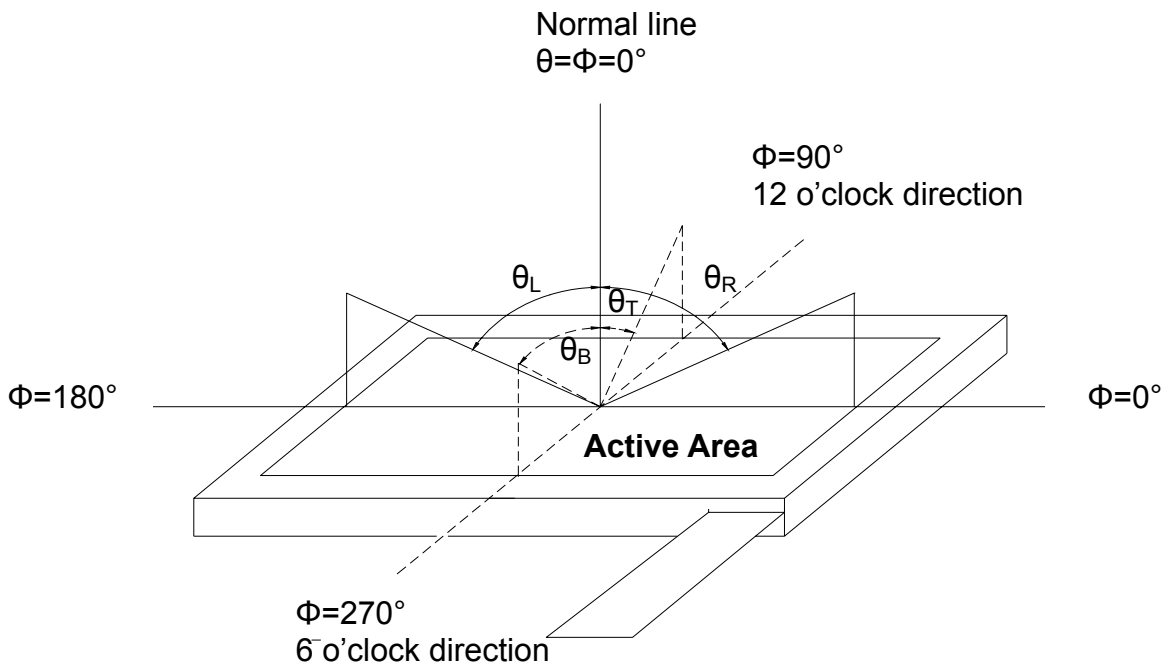
Note 1: Definition of viewing angle range

Fig. 4-1 Definition of viewing angle

Note 2: Definition of optical measurement system.

The optical characteristics should be measured in dark room. After 30 minutes operation, the optical properties are measured at the center point of the LCD screen. (Response time is measured by Photo detector TOPCON BM-7, other items are measured by BM-5A/Field of view: 1° /Height: 500mm.)
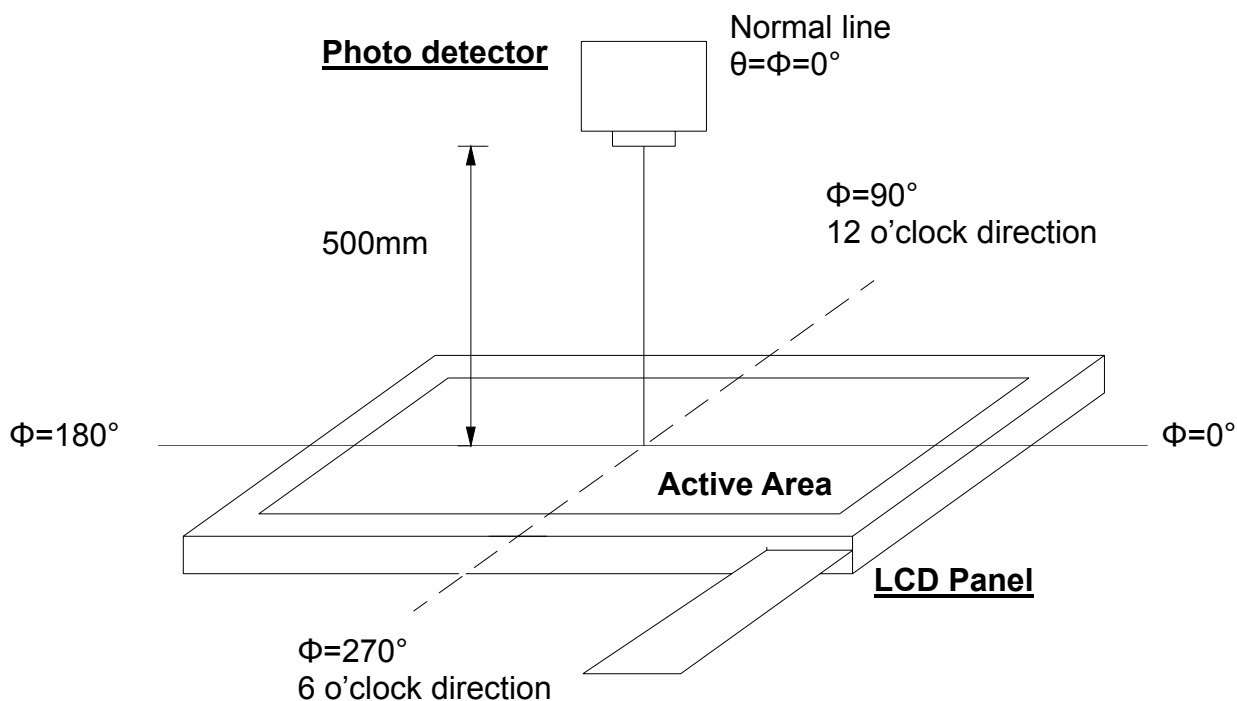

Fig. 4-2 Optical measurement system setup

Note 3: Definition of Response time

The response time is defined as the LCD optical switching time interval between "White" state and "Black" state. Rise time (T_{ON}) is the time between photo detector output intensity changed from 90% to 10%. And fall time (T_{OFF}) is the time between photo detector output intensity changed from 10% to 90%.
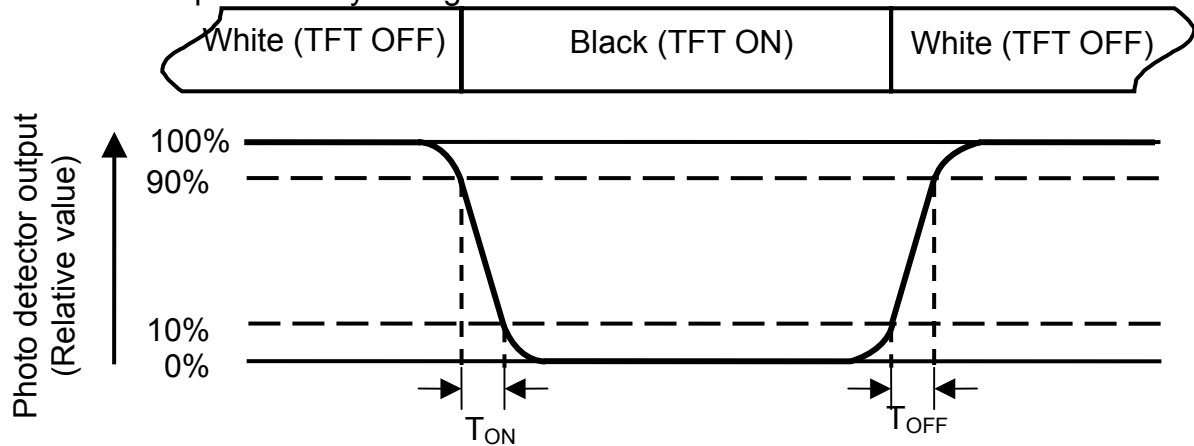

Fig. 4-3 Definition of response time

Note 4: Definition of contrast ratio

$$\text{Contrast ratio (CR)} = \frac{\text{Luminance measured when LCD is on the "White" state}}{\text{Luminance measured when LCD is on the "Black" state}}$$

White $V_i = V_{i50\%} \pm 1.5$ V

Black $V_i = V_{i50\%} \mp 2.0$ V

"$\pm$" means that the analog input signal swings in phase with V_{COM} signal.

"$\mp$" means that the analog input signal swings out of phase with V_{COM} signal.

$V_{i50\%}$: The analog input voltage when transmission is 50%

The 100% transmission is defined as the transmission of LCD panel when all the input terminals of module are electrically opened.

Note 5: Definition of color chromaticity (CIE1931)

Color coordinates measured at center point of LCD.

Note 6: All input terminals LCD panel must be ground while measuring the center area of the panel.

Note 7: Definition of Luminance Uniformity

      Active area is divided into 9 measuring areas (Refer to Fig. 4-4).Every measuring point is placed at the center of each measuring area.

$$Luminance \ Uniformity \ (Yu) = \frac{B_{min}}{B_{max}}$$

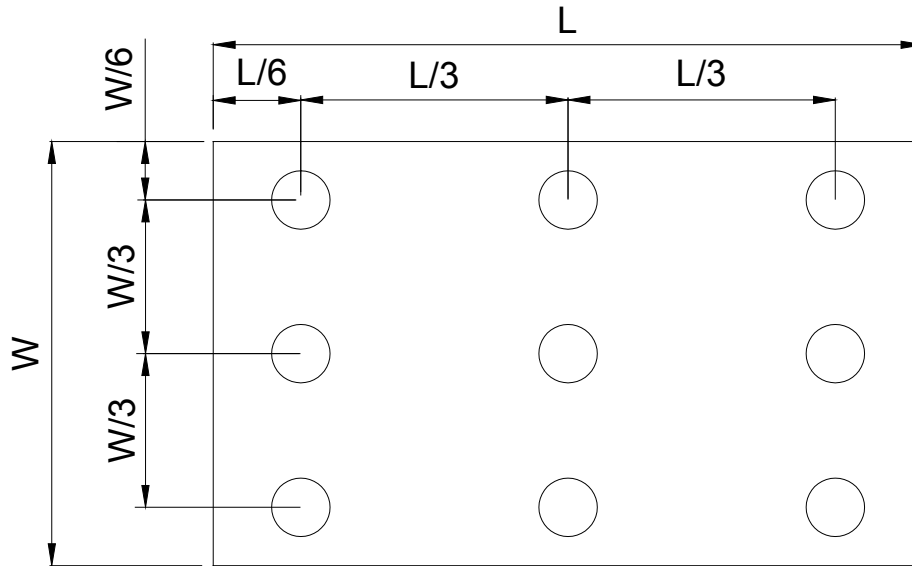L-------Active area length   W----- Active area width

Fig. 4-4 Definition of measuring points

B_{max}: The measured maximum luminance of all measurement position.
B_{min}: The measured minimum luminance of all measurement position.

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5. Reliability Test Items

(Note3)

Item	Test Conditions	Remark
High Temperature Storage	Ta = 85℃ 240 hrs	Note 1
Low Temperature Storage	Ta = -30℃ 240hrs	Note 1
High Temperature Operation	Ts = 85℃ 240hrs	Note 2
Low Temperature Operation	Ta = -30℃ 240hrs	Note 1
Operate at High Temperature and Humidity	+60℃, 90%RH 240 hrs	
Thermal Shock	-30℃/30 min ~ +85℃/30 min for a total 100 cycles, Start with cold temperature and end with high temperature	
Vibration Test	Frequency range:10~55Hz Stroke:1.5mm Sweep:10Hz~55Hz~10Hz 2 hours for each direction of X. Y. Z. (6 hours for total)	
Mechanical Shock	100G 6ms,±X, ±Y, ±Z 3 times for each direction	
Package Vibration Test	Random Vibration : 0.015G*G/Hz from 5-200HZ, -6dB/Octave from 200-500HZ 2 hours for each direction of X. Y. Z. (6 hours for total)	
Package Drop Test	Height:60 cm 1 corner, 3 edges, 6 surfaces	
Electro Static Discharge	± 2KV, Human Body Mode, 100pF/1500Ω	

Note 1: Ta is the ambient temperature of samples.
Note 2: Ts is the temperature of panel's surface.
Note 3: In the standard condition, there shall be no practical problem that may affect the
display function.

6. General Precautions

6.1. Safety

Liquid crystal is poisonous. Do not put it in your mouth. If liquid crystal touches your skin or clothes, wash it off immediately by using soap and water.

6.2. Handling

1. The LCD panel is plate glass. Do not subject the panel to mechanical shock or to excessive force on its surface.
2. The polarizer attached to the display is easily damaged. Please handle it carefully to avoid scratch or other damages.
3. To avoid contamination on the display surface, do not touch the module surface with bare hands.
4. Keep a space so that the LCD panels do not touch other components.
5. Put cover board such as acrylic board on the surface of LCD panel to protect panel from damages.
6. Transparent electrodes may be disconnected if you use the LCD panel under environmental conditions where the condensation of dew occurs.
7. Do not leave module in direct sunlight to avoid malfunction of the ICs.

6.3. Static Electricity

1. Be sure to ground module before turning on power or operating module.
2. Do not apply voltage which exceeds the absolute maximum rating value.

6.4. Storage

1. Store the module in a dark room where must keep at +25±10℃ and 65%RH or less.
2. Do not store the module in surroundings containing organic solvent or corrosive gas.
3. Store the module in an anti-electrostatic container or bag.

6.5. Cleaning

1. Do not wipe the polarizer with dry cloth. It might cause scratch.
2. Only use a soft sloth with IPA to wipe the polarizer, other chemicals might permanent damage to the polarizer.

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7. Mechanical Drawing

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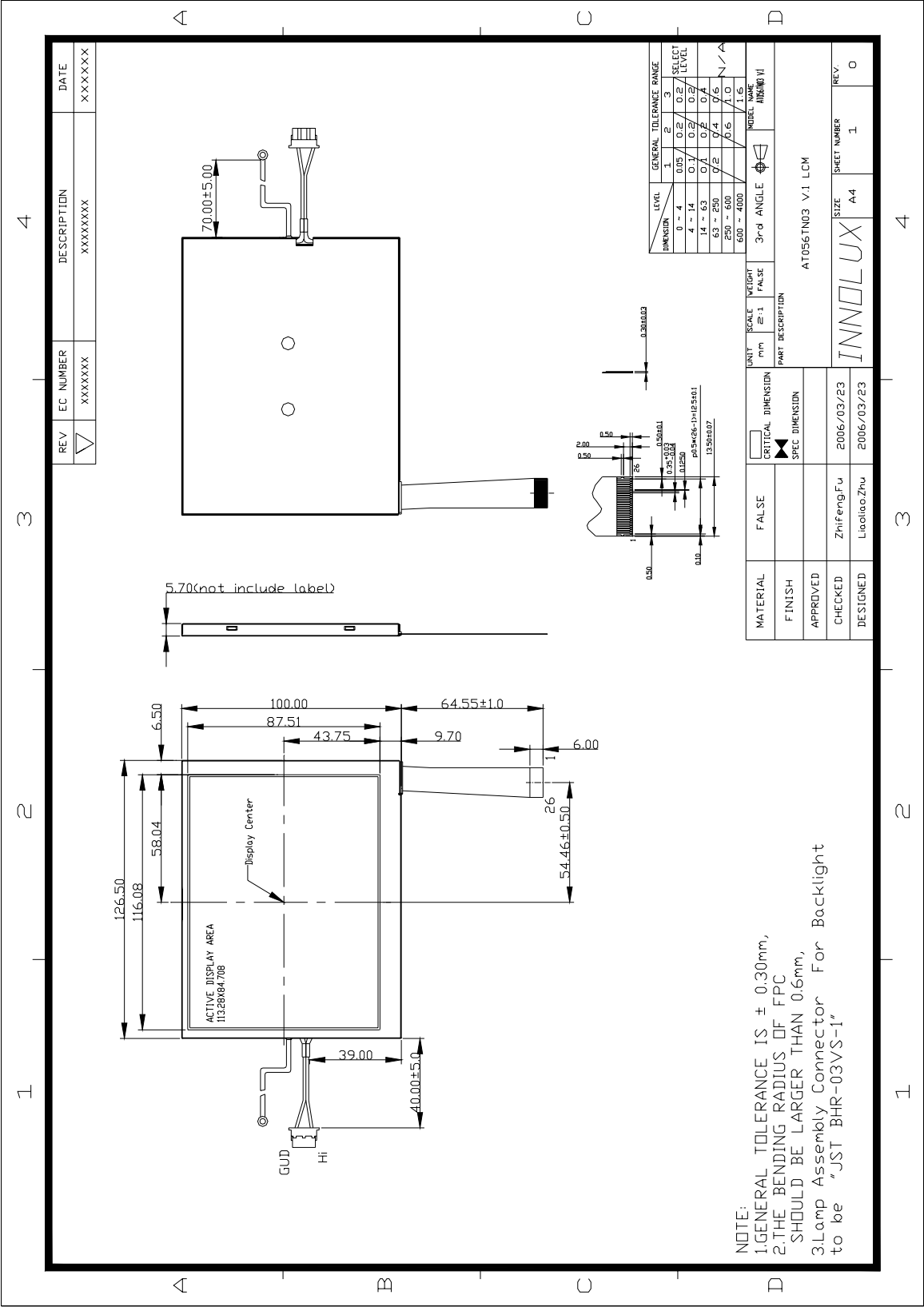
8. Package Drawing

8.1.   Packaging Material Table

No.	Item	Model (Material)	Dimensions(mm)	Unit Weight (kg)	Quantity	Remark
1	LCM Module	AT056TN03 V.1	126.5×100×5.7	TBD	68pcs	
2	Partition	B Corrugated paper	105×349	0.020	2pcs	
3	Corrugated Board	B Corrugated paper	510×350	0.144	2pcs	
4	Partition Paper	BC Corrugated paper	512×226×349	1.170	1set	
5	Dust-Proof Bag	PE	700×530	0.0604	1pcs	
6	A/S Bag	PE	170×110×0.2	0.0018	68pcs	
7	Carton	Corrugated paper	530*355*255	1.100	1pcs	
8	Total Weight	TBD	kg± 5%			

8.2.   Packaging Quantity

Total LCM quantity in Carton: no. of   Partition   4 Rows x quantity per Row 17= 68

8.3.   Packaging Drawing

1. Bend FPC and lamp wire to LCM backside.
2. Fix with crepe paper tape .
3. Put LCM into A/S BAG .