



N-Channel Silicon MOSFET

ATP203 — General-Purpose Switching Device Applications

Features

- Low ON-resistance.
- Large current.
- Slim package.
- 4.5V drive.
- Halogen free compliance.

Specifications

Absolute Maximum Ratings at $T_a=25^\circ\text{C}$

Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	V_{DS}		30	V
Gate-to-Source Voltage	V_{GS}		± 20	V
Drain Current (DC)	I_D		75	A
Drain Current ($PW \leq 10\mu\text{s}$)	I_{DP}	$PW \leq 10\mu\text{s}$, duty cycle $\leq 1\%$	225	A
Allowable Power Dissipation	P_D	$T_c=25^\circ\text{C}$	50	W
Channel Temperature	T_{ch}		150	$^\circ\text{C}$
Storage Temperature	T_{stg}		-55 to +150	$^\circ\text{C}$
Avalanche Energy (Single Pulse) *1	E_{AS}		52	mJ
Avalanche Current *2	I_{AV}		38	A

Note : *1 $V_{DD}=10\text{V}$, $L=50\mu\text{H}$, $I_{AV}=38\text{A}$ *2 $L \leq 50\mu\text{H}$, Single pulseElectrical Characteristics at $T_a=25^\circ\text{C}$

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Drain-to-Source Breakdown Voltage	$V_{(BR)DSS}$	$I_D=1\text{mA}$, $V_{GS}=0\text{V}$	30			V
Zero-Gate Voltage Drain Current	I_{DSS}	$V_{DS}=30\text{V}$, $V_{GS}=0\text{V}$			1	μA
Gate-to-Source Leakage Current	I_{GSS}	$V_{GS}=\pm 16\text{V}$, $V_{DS}=0\text{V}$			± 10	μA

Marking : ATP203

Continued on next page.

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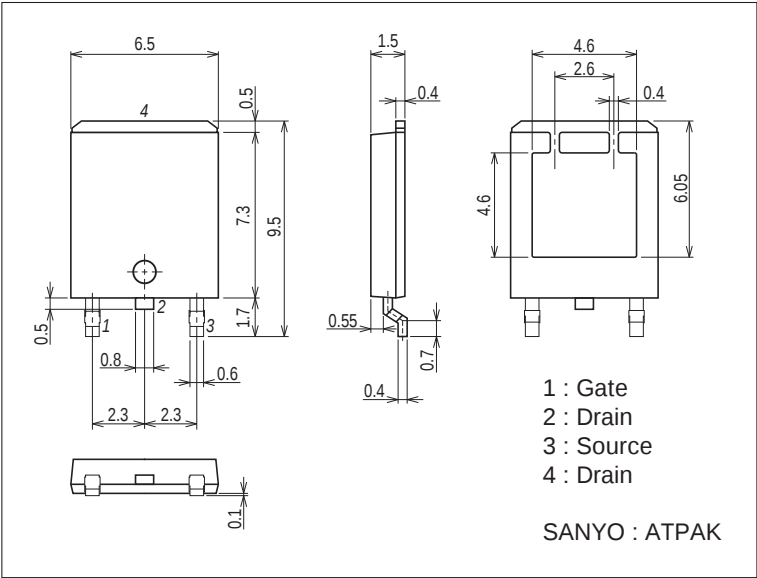
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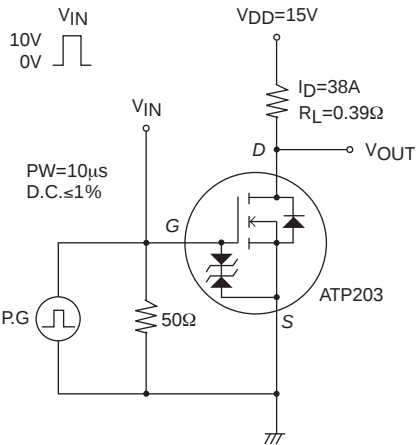
Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Cutoff Voltage	$V_{GS(off)}$	$V_{DS}=10V, I_D=1mA$	1.2		2.6	V
Forward Transfer Admittance	$ y_{fs} $	$V_{DS}=10V, I_D=38A$	13	22		S
Static Drain-to-Source On-State Resistance	$R_{DS(on)1}$	$I_D=38A, V_{GS}=10V$		6.3	8.2	m Ω
	$R_{DS(on)2}$	$I_D=19A, V_{GS}=4.5V$		9.5	13.5	m Ω
Input Capacitance	C_{iss}	$V_{DS}=10V, f=1MHz$		2750		pF
Output Capacitance	C_{oss}	$V_{DS}=10V, f=1MHz$		450		pF
Reverse Transfer Capacitance	C_{rss}	$V_{DS}=10V, f=1MHz$		265		pF
Turn-ON Delay Time	$t_d(on)$	See specified Test Circuit.		24		ns
Rise Time	t_r	See specified Test Circuit.		420		ns
Turn-OFF Delay Time	$t_d(off)$	See specified Test Circuit.		130		ns
Fall Time	t_f	See specified Test Circuit.		75		ns
Total Gate Charge	Q_g	$V_{DS}=15V, V_{GS}=10V, I_D=75A$		44		nC
Gate-to-Source Charge	Q_{gs}	$V_{DS}=15V, V_{GS}=10V, I_D=75A$		14		nC
Gate-to-Drain "Miller" Charge	Q_{gd}	$V_{DS}=15V, V_{GS}=10V, I_D=75A$		5.6		nC
Diode Forward Voltage	V_{SD}	$I_S=75A, V_{GS}=0V$		1.02	1.2	V

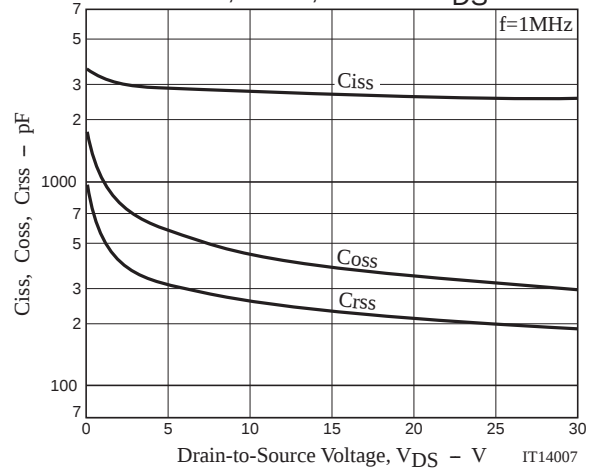
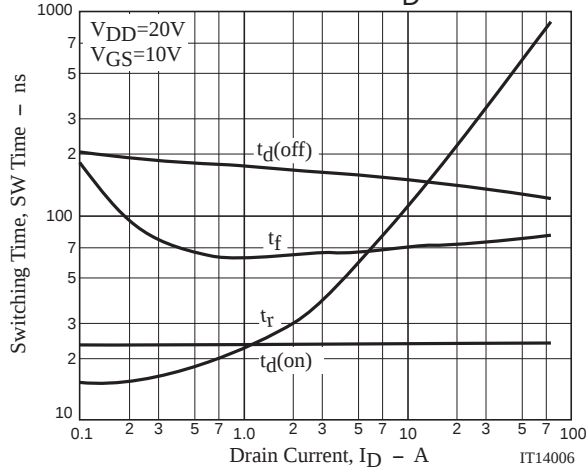
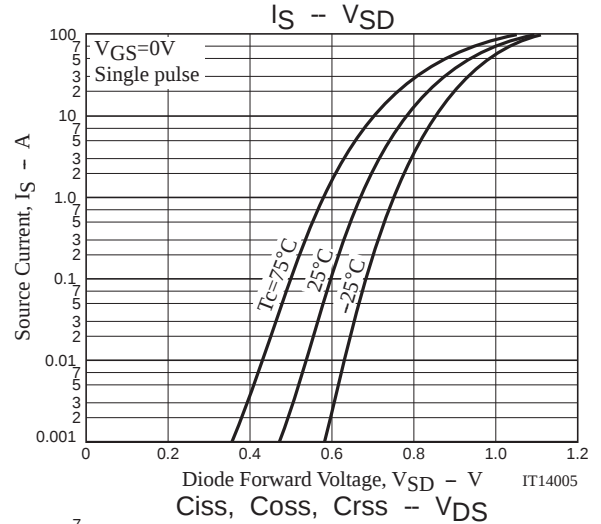
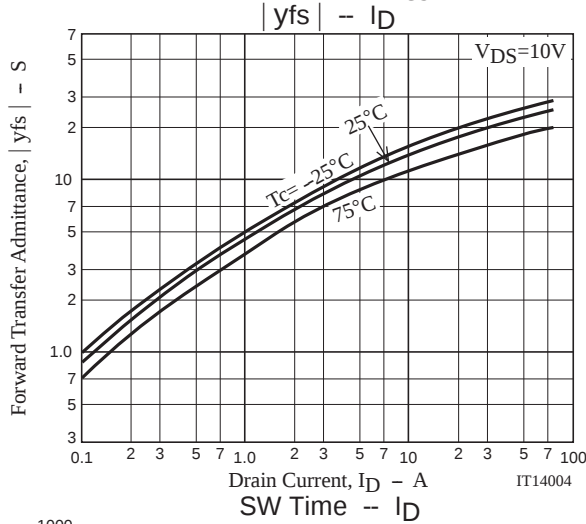
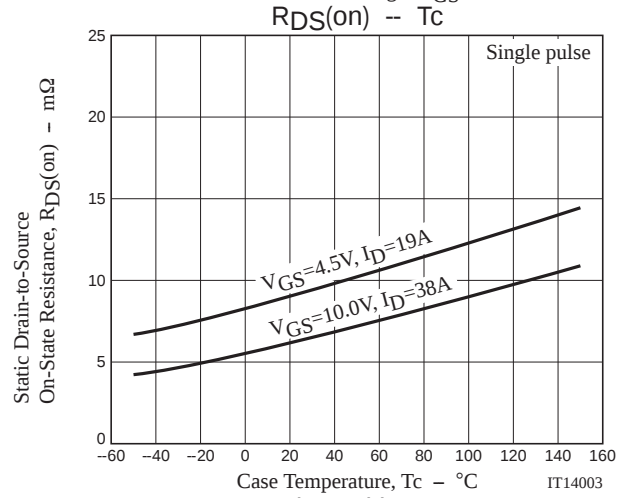
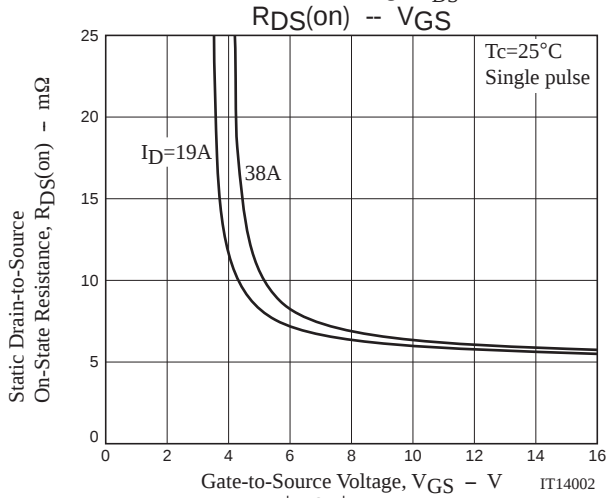
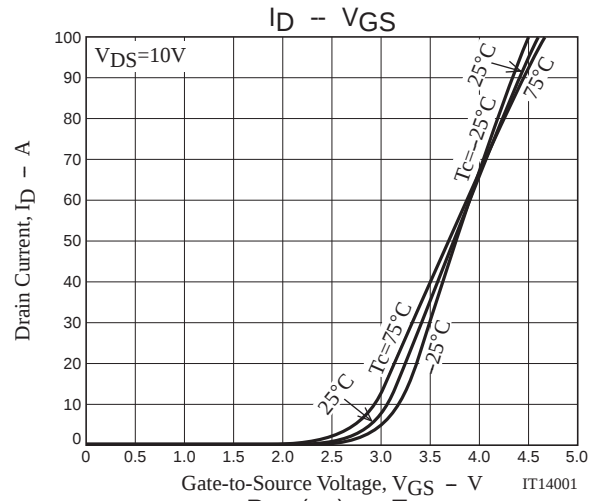
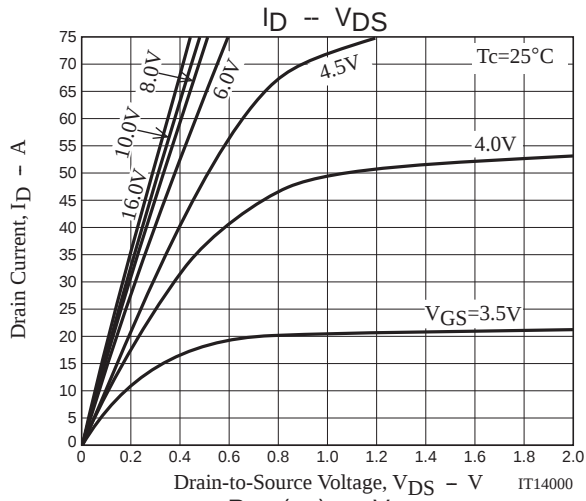
Package Dimensions

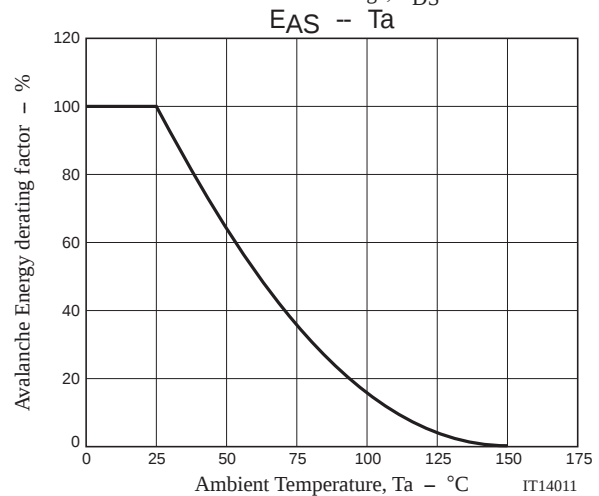
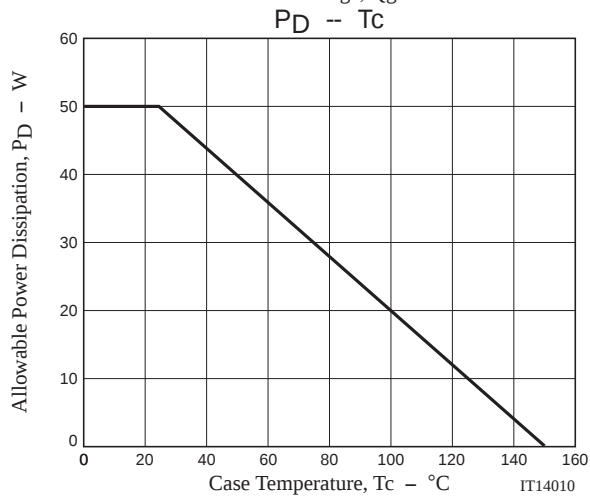
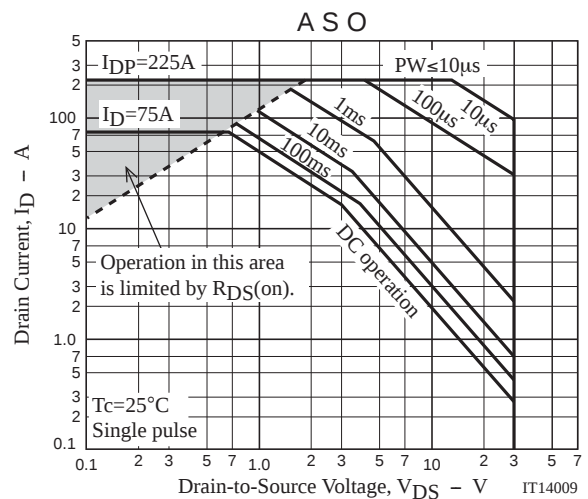
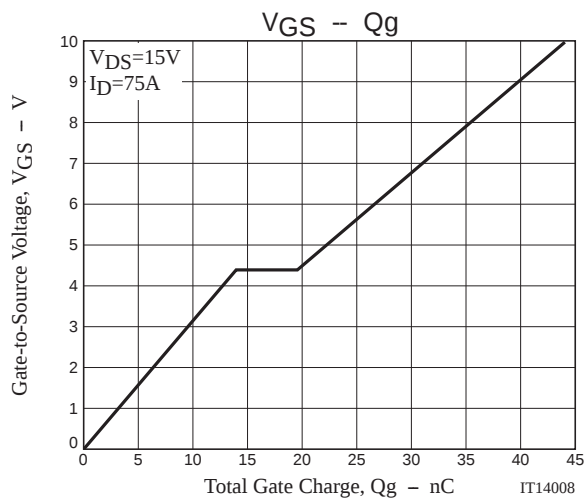
unit : mm (typ)
7057-001



Switching Time Test Circuit







Note on usage : Since the ATP203 is a MOSFET product, please avoid using this device in the vicinity of highly charged objects.

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