

## SANYO Semiconductors DATA SHEET

An ON Semiconductor Company

N-Channel Silicon MOSFET

# ATP218 — General-Purpose Switching Device Applications

#### **Features**

- ON-resistance RDS(on)1=2.9m $\Omega$ (typ.)
- · 2.5V drive

- Input Capacitance Ciss=6600pF(typ.)
- · Halogen free compliance

#### **Specifications**

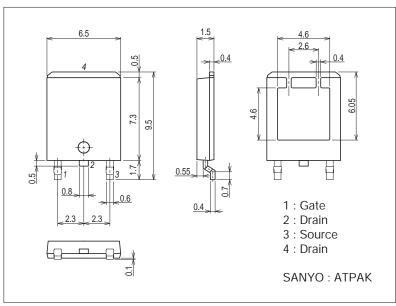
Absolute Maximum Ratings at Ta=25°C

| Parameter                          | Symbol           | Conditions             | Ratings     | Unit |
|------------------------------------|------------------|------------------------|-------------|------|
| Drain-to-Source Voltage            | V <sub>DSS</sub> |                        | 30          | V    |
| Gate-to-Source Voltage             | VGSS             |                        | ±10         | V    |
| Drain Current (DC)                 | ID               |                        | 100         | А    |
| Drain Current (PW≤10μs)            | IDP              | PW≤10μs, duty cycle≤1% | 300         | Α    |
| Allowable Power Dissipation        | PD               | Tc=25°C                | 60          | W    |
| Channel Temperature                | Tch              |                        | 150         | °C   |
| Storage Temperature                | Tstg             |                        | -55 to +150 | °C   |
| Avalanche Energy (Single Pulse) *1 | EAS              |                        | 235         | mJ   |
| Avalanche Current *2               | I <sub>AV</sub>  |                        | 50          | А    |

Note :\*1 VDD=15V, L=100 $\mu$ H, IAV=50A

#### **Package Dimensions**

unit : mm (typ) 7057-001



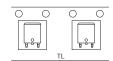
#### **Product & Package Information**

• Package : ATPAK

• JEITA, JEDEC :-

• Minimum Packing Quantity : 3,000 pcs./reel

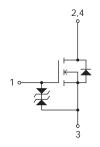
#### Packing Type: TL



#### Marking



#### **Electrical Connection**

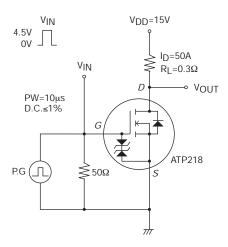


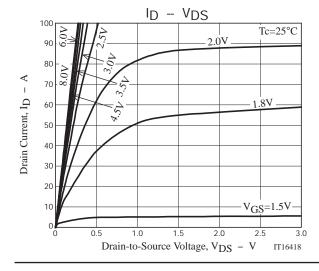
<sup>\*2</sup> L≤100µH, Single pulse

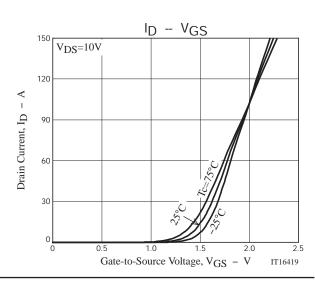
#### Electrical Characteristics at Ta=25°C

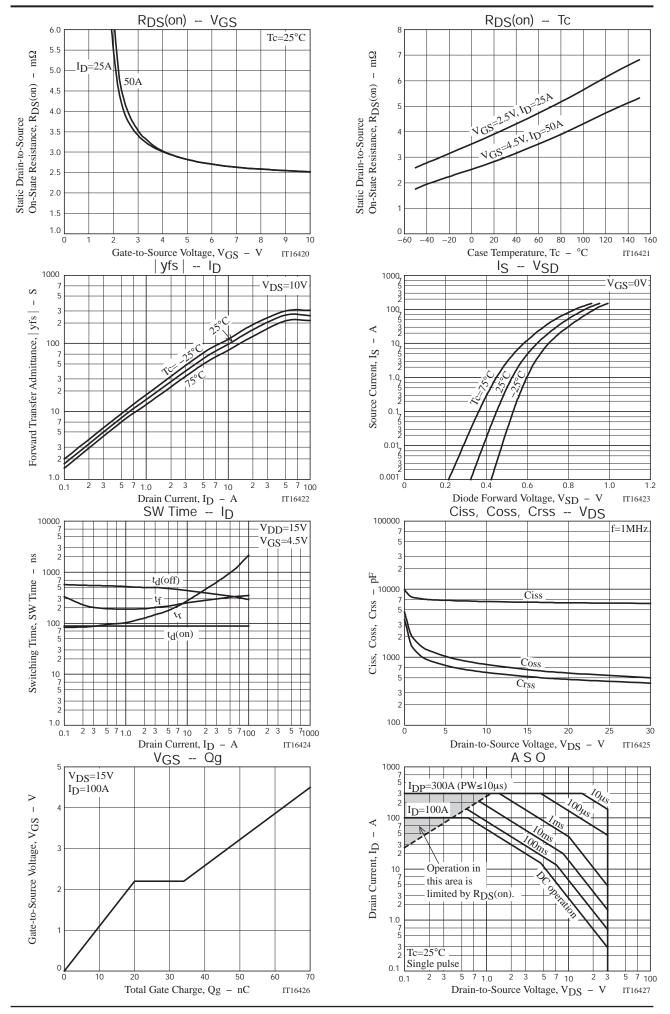
| Parameter                                  | Symbol                | Conditions  | Ratings |      |     | Linit |
|--|-----------------------|---|---------|------|-----|-------|
|  |                       |   | min     | typ  | max | Unit  |
| Drain-to-Source Breakdown Voltage          | V(BR)DSS              | ID=1mA, VGS=0V  | 30      |      |     | V     |
| Zero-Gate Voltage Drain Current            | IDSS                  | V <sub>DS</sub> =30V, V <sub>GS</sub> =0V                         |         |      | 1   | μΑ    |
| Gate-to-Source Leakage Current             | IGSS                  | V <sub>GS</sub> =±8V, V <sub>DS</sub> =0V                         |         |      | ±10 | μΑ    |
| Cutoff Voltage                             | VGS(off)              | V <sub>DS</sub> =10V, I <sub>D</sub> =1mA                         | 0.5     |      | 1.3 | V     |
| Forward Transfer Admittance                | yfs                   | VDS=10V, ID=50A   |         | 260  |     | S     |
| Static Drain-to-Source On-State Resistance | R <sub>DS</sub> (on)1 | I <sub>D</sub> =50A, V <sub>G</sub> S=4.5V                        |         | 2.9  | 3.8 | mΩ    |
|  | R <sub>DS</sub> (on)2 | I <sub>D</sub> =25A, V <sub>GS</sub> =2.5V                        |         | 4.0  | 5.6 | mΩ    |
| Input Capacitance                          | Ciss                  | V <sub>DS</sub> =10V, f=1MHz                                      |         | 6600 |     | pF    |
| Output Capacitance                         | Coss                  | V <sub>DS</sub> =10V, f=1MHz                                      |         | 780  |     | pF    |
| Reverse Transfer Capacitance               | Crss                  | V <sub>DS</sub> =10V, f=1MHz                                      |         | 600  |     | pF    |
| Turn-ON Delay Time                         | t <sub>d</sub> (on)   | See specified Test Circuit.                                       |         | 88   |     | ns    |
| Rise Time                                  | t <sub>r</sub>        | See specified Test Circuit.                                       |         | 960  |     | ns    |
| Turn-OFF Delay Time                        | t <sub>d</sub> (off)  | See specified Test Circuit.                                       |         | 340  |     | ns    |
| Fall Time                                  | tf                    | See specified Test Circuit.                                       |         | 320  |     | ns    |
| Total Gate Charge                          | Qg                    | V <sub>DS</sub> =15V, V <sub>GS</sub> =4.5V, I <sub>D</sub> =100A |         | 70   |     | nC    |
| Gate-to-Source Charge                      | Qgs                   | V <sub>DS</sub> =15V, V <sub>GS</sub> =4.5V, I <sub>D</sub> =100A |         | 20   |     | nC    |
| Gate-to-Drain "Miller" Charge              | Qgd                   | V <sub>DS</sub> =15V, V <sub>GS</sub> =4.5V, I <sub>D</sub> =100A |         | 14   |     | nC    |
| Diode Forward Voltage                      | VSD                   | IS=100A, VGS=0V   |         | 0.91 | 1.2 | V     |

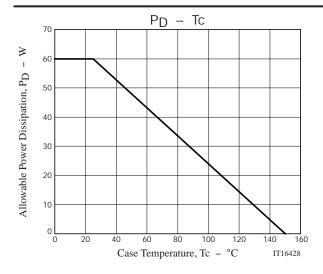
### **Switching Time Test Circuit**

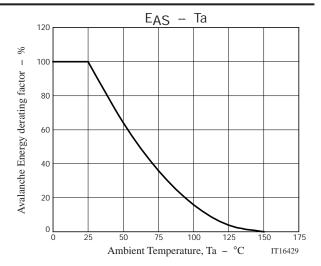












Note on usage: Since the ATP218 is a MOSFET product, please avoid using this device in the vicinity of highly charged objects.

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