AUTOMOTIVE GRADE

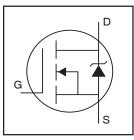
PD - 97458

AUIRF1010Z AUIRF1010ZS AUIRF1010ZL

HEXFET® Power MOSFET

Features

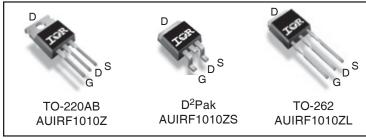
- Advanced Process Technology
- Ultra Low On-Resistance
- 175°C Operating Temperature
- Fast Switching
- Repetitive Avalanche Allowed up to Tjmax
- Lead-Free, RoHS Compliant
- Automotive Qualified *



$V_{(BR)DSS}$	55V
R _{DS(on)} max.	7.5m Ω
I _{D (Silicon Limited)}	94A
I _{D (Package Limited)}	75A

Description

www.Data Specifically designed for Automotive applications, this HEXFET® Power MOSFET utilizes the latest processing techniques to achieve extremely low onresistance per silicon area. Additional features of this design are a 175°C junction operating temperature, fast switching speed and improved repetitive avalanche rating. These features combine to make this design an extremely efficient and reliable device for use in Automotive applications and a wide variety of other applications.



G	D	S
Gate	Drain	Source

Absolute Maximum Ratings

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only; and functional operation of the device at these or any other condition beyond those indicated in the specifications is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability. The thermal resistance and power dissipation ratings are measured under board mounted and still air conditions. Ambient temperature (T_A) is 25°C, unless otherwise specified.

	Parameter	Max.	Units
I _D @ T _C = 25°C	Continuous Drain Current, V _{GS} @ 10V (Silicon Limited)	94	
I _D @ T _C = 100°C	Continuous Drain Current, VGS @ 10V (Silicon Limited)	66	Α
I _D @ T _C = 25°C	Continuous Drain Current, V _{GS} @ 10V (Package Limited)	75	
I _{DM}	Pulsed Drain Current ①	360	
P _D @T _C = 25°C	Power Dissipation	140	W
	Linear Derating Factor	0.90	W/°C
V_{GS}	Gate-to-Source Voltage	± 20	V
E _{AS}	Single Pulse Avalanche Energy (Thermally Limited) ②	130	mJ
E _{AS} (tested)	Single Pulse Avalanche Energy Tested Value ®	180	
I _{AR}	Avalanche Current ①	See Fig.12a, 12b, 15, 16	А
E _{AR}	Repetitive Avalanche Energy ©		mJ
TJ	Operating Junction and	-55 to + 175	
T _{STG}	Storage Temperature Range		°C
	Soldering Temperature, for 10 seconds (1.6mm from case)	300	
	Mounting Torque, 6-32 or M3 screw ⑦	10 lbf•in (1.1N•m)	

Thermal Resistance

	Parameter	Тур.	Max.	Units
R _{eJC}	Junction-to-Case 9		1.11	
R _{ecs}	Case-to-Sink, Flat Greased Surface ♡	0.50		°C/W
$R_{\theta JA}$	Junction-to-Ambient ⑦		62	
$R_{\theta JA}$	Junction-to-Ambient (PCB Mount) ®		40	

HEXFET® is a registered trademark of International Rectifier.

^{*}Qualification standards can be found at http://www.irf.com/

Static Electrical Characteristics @ T_J = 25°C (unless otherwise specified)

	Parameter	Min.	Тур.	Max.	Units	Conditions
$V_{(BR)DSS}$	Drain-to-Source Breakdown Voltage	55			V	$V_{GS} = 0V, I_D = 250\mu A$
$\Delta V_{(BR)DSS}/\Delta T_{J}$	Breakdown Voltage Temp. Coefficient		0.049		V/°C	Reference to 25°C, I _D = 1mA
R _{DS(on)}	Static Drain-to-Source On-Resistance		5.8	7.5	mΩ	V _{GS} = 10V, I _D = 75A ③
$V_{GS(th)}$	Gate Threshold Voltage	2.0		4.0	V	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$
gfs	Forward Transconductance	33			S	$V_{DS} = 25V, I_D = 75A$
I _{DSS}	Drain-to-Source Leakage Current			20	μA	$V_{DS} = 55V, V_{GS} = 0V$
				250		$V_{DS} = 55V, V_{GS} = 0V, T_{J} = 125^{\circ}C$
I _{GSS}	Gate-to-Source Forward Leakage			200	nA	$V_{GS} = 20V$
	Gate-to-Source Reverse Leakage			-200		V _{GS} = -20V

Dynamic Electrical Characteristics @ T_J = 25°C (unless otherwise specified)

	Parameter	Min.	Тур.	Max.	Units	Conditions
Q_g	Total Gate Charge		63	95		I _D = 75A
Q_{gs}	Gate-to-Source Charge		19		nC	$V_{DS} = 44V$
Q_{gd}	Gate-to-Drain ("Miller") Charge		24			V _{GS} = 10V ③
t _{d(on)}	Turn-On Delay Time		18			$V_{DD} = 28V$
t _r	Rise Time		150			$I_D = 75A$
t _{d(off)}	Turn-Off Delay Time		36		ns	$R_G = 6.8 \Omega$
t _f	Fall Time		92			V _{GS} = 10V ③
L _D	Internal Drain Inductance		4.5			Between lead,
					nH	6mm (0.25in.)
L _S	Internal Source Inductance		7.5			from package
						and center of die contact
C _{iss}	Input Capacitance		2840			$V_{GS} = 0V$
C _{oss}	Output Capacitance		420			$V_{DS} = 25V$
C _{rss}	Reverse Transfer Capacitance		250		рF	f = 1.0MHz
C _{oss}	Output Capacitance		1630			$V_{GS} = 0V, V_{DS} = 1.0V, f = 1.0MHz$
C _{oss}	Output Capacitance		360			$V_{GS} = 0V, V_{DS} = 44V, f = 1.0MHz$
C _{oss} eff.	Effective Output Capacitance		560			V _{GS} = 0V, V _{DS} = 0V to 44V ④

Diode Characteristics

	Parameter	Min.	Тур.	Max.	Units	Conditions
I _S	Continuous Source Current			75		MOSFET symbol
	(Body Diode)				Α	showing the
I _{SM}	Pulsed Source Current			360		integral reverse
	(Body Diode) ①					p-n junction diode.
V_{SD}	Diode Forward Voltage			1.3	V	$T_J = 25^{\circ}C$, $I_S = 75A$, $V_{GS} = 0V$ ③
t _{rr}	Reverse Recovery Time		22	33	ns	$T_J = 25^{\circ}C$, $I_F = 75A$, $V_{DD} = 25V$
Q _{rr}	Reverse Recovery Charge		15	23	nC	di/dt = 100A/µs ③
t _{on}	Forward Turn-On Time	Intrinsic	Intrinsic turn-on time is negligible (turn-on is dominated by LS+LD)			

Notes:

- ① Repetitive rating; pulse width limited by max. junction temperature. (See fig. 11).
- ② Limited by T_{Jmax} , starting $T_J = 25^{\circ}C$, L = 0.05mH, $R_G = 25\Omega$, $I_{AS} = 75A$, $V_{GS} = 10V$. Part not recommended for use above this value.
- $\ \ \,$ $\ \ \,$ $\ \ \,$ $\ \ \,$ $\ \ \,$ $\ \$ $\ \ \,$ Coss eff. is a fixed capacitance that gives the same charging time as $\ \ \,$ Coss while $\ \ \,$ VDS is rising from 0 to 80% VDSS .
- S Limited by T_{Jmax}, see Fig.12a, 12b, 15, 16 for typical repetitive avalanche performance.
- © This value determined from sample failure population, starting T_J = 25°C, L = 0.05mH, R_G = 25 Ω , I_{AS} = 75A, V_{GS} =10V.
- This is only applied to TO-220AB pakcage.
- This is applied to D²Pak, when mounted on 1" square PCB (FR-4 or G-10 Material). For recommended footprint and soldering techniques refer to application note #AN-994.

Qualification Information[†]

				Automotive		
				(per AEC-Q101) ^{††}		
	C		Comments: This part number(s) passed Autom qualification. IR's Industrial and Consumer qualific level is granted by extension of the higher Autom level.			
www.Data	Sheet4U.com		TO-220AB	N/A		
	Moisture Sensitivi	ty Level	TO-262	N/A		
			D ² Pak	MSL1		
		Machine Model		Class M4		
				AEC-Q101-002		
	FOD	Human Body Model		Class H1C		
	ESD			AEC-Q101-001		
		Charged Device Model		Class C3		
			AEC-Q101-005			
	RoHS Compliant	•	Yes			

- † Qualification standards can be found at International Rectifier's web site: http://www.irf.com/
- †† Exceptions to AEC-Q101 requirements are noted in the qualification report.

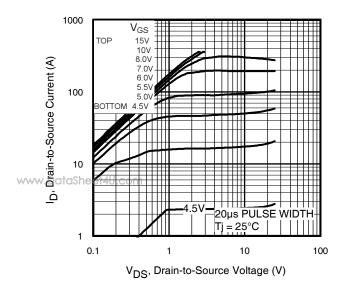


Fig 1. Typical Output Characteristics

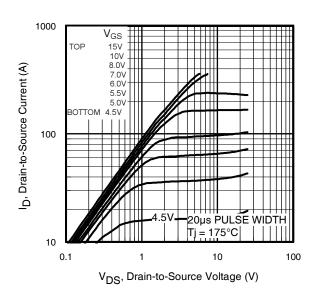


Fig 2. Typical Output Characteristics

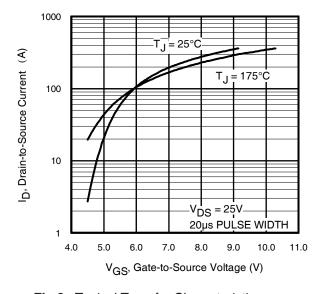


Fig 3. Typical Transfer Characteristics

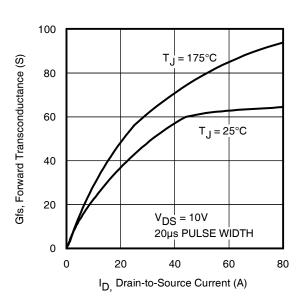


Fig 4. Typical Forward Transconductance
Vs. Drain Current

AUIRF1010Z/S/L

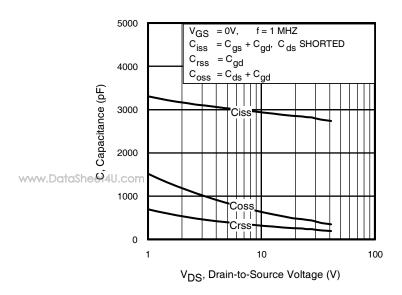


Fig 5. Typical Capacitance Vs. Drain-to-Source Voltage

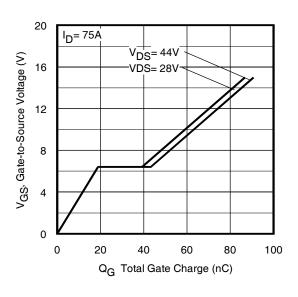


Fig 6. Typical Gate Charge Vs. Gate-to-Source Voltage

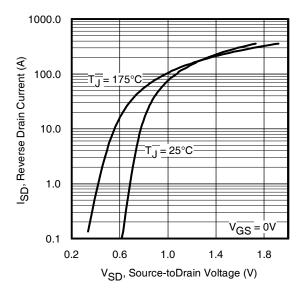


Fig 7. Typical Source-Drain Diode Forward Voltage

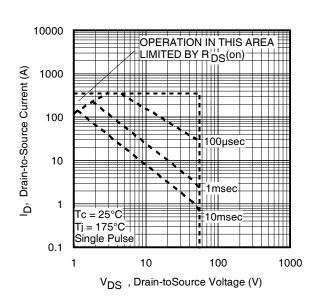


Fig 8. Maximum Safe Operating Area

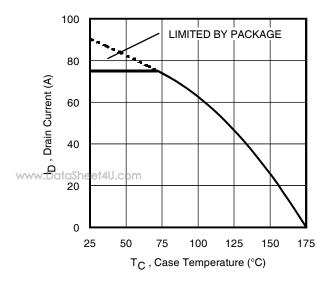


Fig 9. Maximum Drain Current Vs. Case Temperature

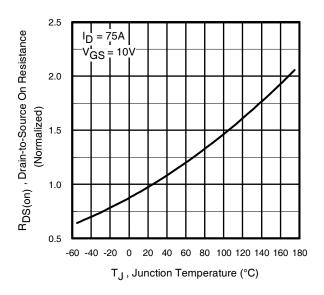


Fig 10. Normalized On-Resistance Vs. Temperature

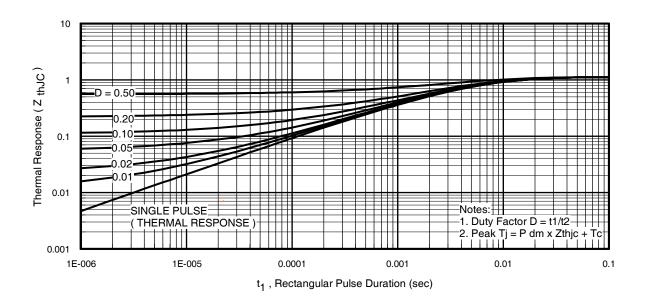
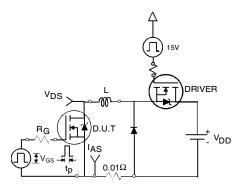


Fig 11. Maximum Effective Transient Thermal Impedance, Junction-to-Case

AUIRF1010Z/S/L



www.DataSheet4U.Com Fig 12a. Unclamped Inductive Test Circuit

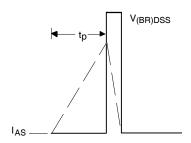


Fig 12b. Unclamped Inductive Waveforms

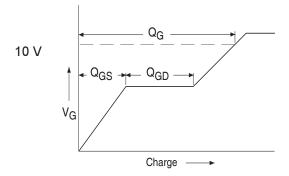


Fig 13a. Basic Gate Charge Waveform

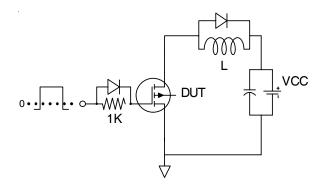


Fig 13b. Gate Charge Test Circuit www.irf.com

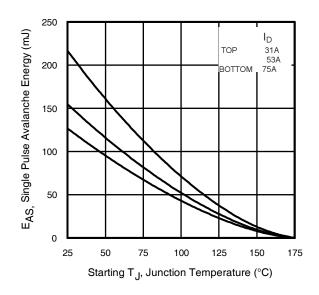


Fig 12c. Maximum Avalanche Energy Vs. Drain Current

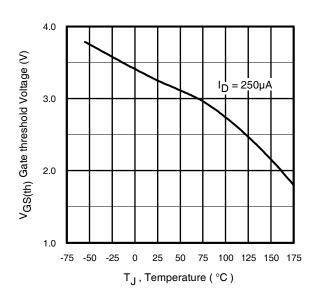


Fig 14. Threshold Voltage Vs. Temperature

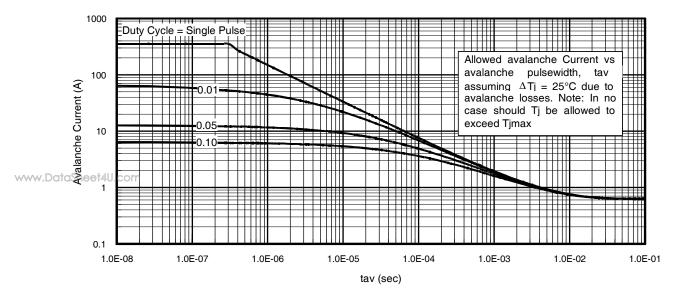


Fig 15. Typical Avalanche Current Vs. Pulsewidth

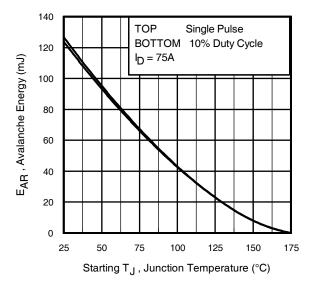


Fig 16. Maximum Avalanche Energy Vs. Temperature

Notes on Repetitive Avalanche Curves, Figures 15, 16: (For further info, see AN-1005 at www.irf.com)

- Avalanche failures assumption:
 Purely a thermal phenomenon and failure occurs at a temperature far in excess of T_{jmax}. This is validated for every part type.
- 2. Safe operation in Avalanche is allowed as long $asT_{j\max}$ is not exceeded.
- 3. Equation below based on circuit and waveforms shown in Figures 12a, 12b.
- 4. P_{D (ave)} = Average power dissipation per single avalanche pulse.
- 5. BV = Rated breakdown voltage (1.3 factor accounts for voltage increase during avalanche).
- 6. I_{av} = Allowable avalanche current.
- 7. ΔT = Allowable rise in junction temperature, not to exceed T_{jmax} (assumed as 25°C in Figure 15, 16). t_{av} = Average time in avalanche.

D = Duty cycle in avalanche = $t_{av} \cdot f$

 $Z_{thJC}(D, t_{av}) = Transient thermal resistance, see figure 11)$

$$\begin{split} P_{D~(ave)} &= 1/2~(~1.3 \cdot BV \cdot I_{aV}) = \triangle T/~Z_{thJC} \\ I_{av} &= 2\triangle T/~[1.3 \cdot BV \cdot Z_{th}] \\ E_{AS~(AR)} &= P_{D~(ave)} \cdot t_{av} \end{split}$$

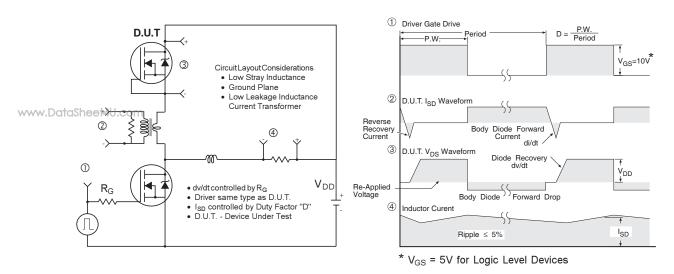


Fig 17. Peak Diode Recovery dv/dt Test Circuit for N-Channel HEXFET® Power MOSFETs

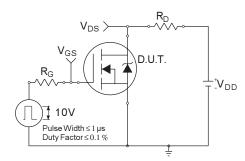


Fig 18a. Switching Time Test Circuit

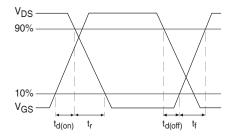
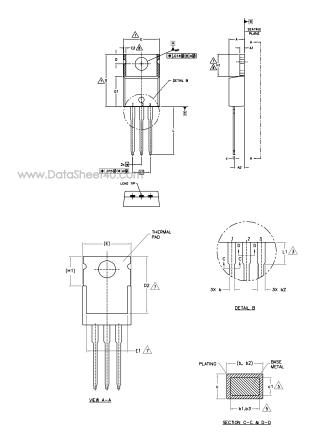


Fig 18b. Switching Time Waveforms

www.irf.com

TO-220AB Package Outline

Dimensions are shown in millimeters (inches)



NOTES:

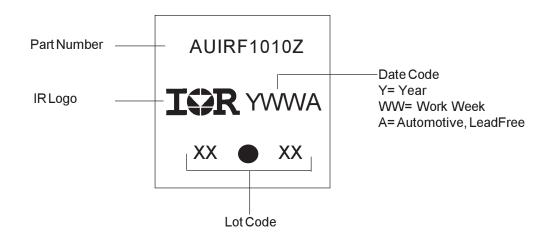
- 1 DIVENSIONING AND TOLEDANIONS AS DED ASME VIA E M. 1004
- 2.- DIMENSIONS ARE SHOWN IN INCHES [MILLIMETERS].
- LEAD DIMENSION AND FINISH UNCONTROLLED IN L1.
 DIMENSION D, D1 & E DO NOT INCLUDE MOLD FLASH, WOLD FLA
- SHALL NOT EXCEED .005" (0.127) PER SIDE. THESE DIMENSIONS ARE
 MEASURED AT THE OUTERWOST EXTREMES OF THE PLASTIC BODY.
- 5.4 DIMENSION 61, 63 & c1 APPLY TO BASE METAL ONL
- 7 THERMAL PAD CONTOUR OPTIONAL WITHIN DIMENSI
- B DIMENSION E2 X H1 DEFINE A ZONE WHERE STAMPING
- AND SINGULATION IRREGULARITIES ARE ALLOWED.
- 9.- OUTLINE CONFORMS TO JEDEC TO-220, EXCEPT A2 (mox.) AND D2 (min.

SYMBOL	MILLIM	ETERS	INC	HES	1
	MIN.	MAX.	MIN.	MAX.	NOTES
A	3.56	4.83	.140	.190	
A1	0.51	1,40	.020	.055	
A2	2.03	2,92	.080	.115	
b	0.38	1,01	.015	.040	
ь1	0.38	0.97	.015	.038	5
b2	1,14	1.78	.045	.070	
b3	1.14	1.73	.045	.068	5
c	0.36	0.61	.014	.024	
c1	0.36	0.56	.014	.022	5
D	14.22	16.51	.560	.650	4
D1	8.38	9.02	.330	.355	
D2	11.68	12.88	.460	.507	7
E	9.65	10.67	.380	.420	4,7
E1	6.86	8,89	.270	.350	7
E2	-	0.76	-	.030	8
e	2.54 BSC		.100	BSC	1
e1	5.0B	BSC	.200	BSC	
H1	5.84	6,86	.230	.270	7,8
L	12.70	14.73	.500	.580	
L1	3,56	4,06	,140	,160	3
øP	3.54	4.08	.139	.161	
0	2,54	3,42	.100	,135	

EAD ASSIGNMENTS

I.- GATE
2.- DRAIN
3.- SOURCE
IGBTs. CoPACK
1.- GATE
2.- COLLEC
3.- DIVITES

TO-220AB Part Marking Information



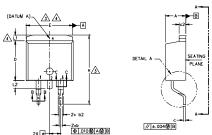
TO-220AB packages are not recommended for Surface Mount Application.

Note: For the most current drawing please refer to IR website at http://www.irf.com/package/

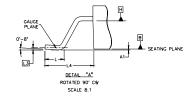
AUIRF1010Z/S/L

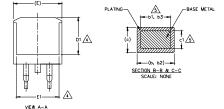
D²Pak (TO-263AB) Package Outline

Dimensions are shown in millimeters (inches)









LEAD ASSIGNMENTS

DIODES

1.- ANODE (TWO DIE) / OPEN (ONE DIE)
2, 4.- CATHODE
3.- ANODE

<u>HEXFET</u>

1.- GATE
2. 4.- DRAIN
3.- SOURCE

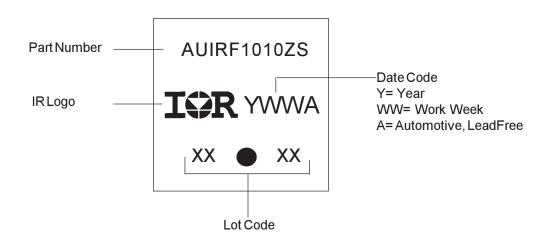
1,- GATE 2, 4,- COLLECT

DIMENSIONS МВО MILLIMETERS MIN. MAX. MIN 4.06 4.83 Α1 0.00 0.254 000 010 0.51 0.99 .020 .039 0.51 0.89 .020 .035 Ь1 1.14 1.78 .045 .070 ь3 1,14 1,73 .045 .068 5 0.38 0.74 .015 029 0.38 0.58 .015 .023 .045 c2 1.14 1.65 .065 D 8.38 9.65 .330 .380 D1 6.86 .270 Ε 9.65 10.67 .380 .420 3,4 E1 6.22 2.54 BSC .100 BSC 14,61 15.88 .575 .625 1.78 2.79 .070 .110 L L1 1.65 .066 4 L2 1,78 .070 0.25 BSC .010 BSC L3 4.78 .188

NOTES:

- 1. DIMENSIONING AND TOLERANCING PER ASME Y14,5M-1994
- 2. DIMENSIONS ARE SHOWN IN MILLIMETERS [INCHES].
- 3 DIMENSION D & E DO NOT INCLUDE MOLD FLASH, MOLD FLASH SHALL NOT EXCEED 0.127 [.005] PER SIDE. THESE DIMENSIONS ARE MEASURED AT THE OUTMOST EXTREMES OF THE PLASTIC BODY AT DATUM H.
- THERMAL PAD CONTOUR OPTIONAL WITHIN DIMENSION E, LI, DI & EI.
- 5 DIMENSION 61 AND c1 APPLY TO BASE METAL ONLY.
- 6. DATUM A & B TO BE DETERMINED AT DATUM PLANE H
- 7. CONTROLLING DIMENSION: INCH.
- 8. OUTLINE CONFORMS TO JEDEC OUTLINE TO-263AB.

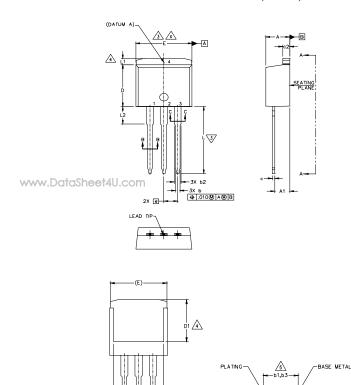
D²Pak (TO-263AB) Part Marking Information



Note: For the most current drawing please refer to IR website at http://www.irf.com/package/

TO-262 Package Outline

Dimensions are shown in millimeters (inches)



S Y M		DIMENSIONS				
M B O L	MILLIM	ETERS	INC	HES	O T E S	
L	MIN.	MAX.	MIN.	MAX.	S	
Α	4.06	4.83	.160	.190		
A1	2.03	3.02	.080	.119		
ь	0.51	0.99	.020	.039		
ь1	0.51	0.89	.020	.035	5	
b2	1.14	1.78	.045	.070		
ь3	1.14	1.73	.045	.068	5	
С	0.38	0.74	.015	.029		
c1	0.38	0.58	.015	.023	5	
c2	1.14	1.65	.045	.065		
D	8.38	9.65	.330	.380	3	
D1	6.86	_	.270	_	4	
E	9.65	10.67	.380	.420	3,4	
E1	6.22	_	.245		4	
e	2.54	BSC	.100 BSC			
L	13.46	14.10	.530	.555		
L1	_	1.65	_	.065	4	
L2	3.56	3.71	.140	.146		

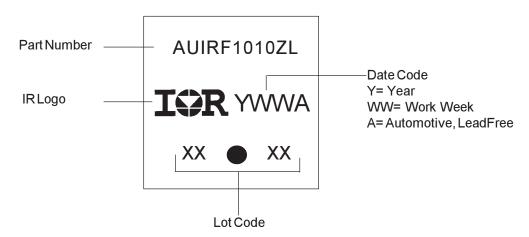
DIMENSIONING AND TOLERANCING PER ASME Y14.5W-1994

7.- OUTLINE CONFORM TO JEDEC TO-282 EXCEPT A1(max.), b(min.) AND D1(min.) WHERE DIMENSIONS DERIVED THE ACTUAL PACKAGE OUTLINE.

TO-262 Part Marking Information

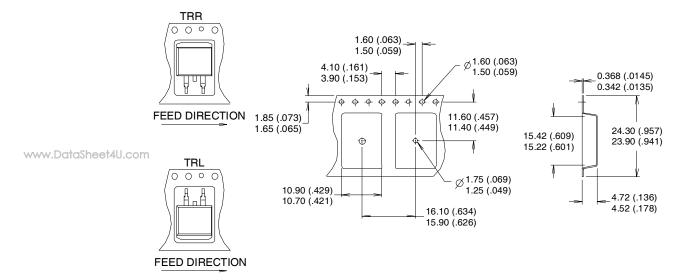
SECTION A-A

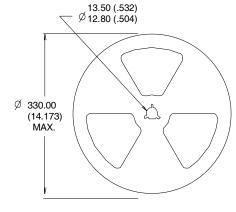
_(b,b2)-SECTION B-B & C-C SCALE: NONE

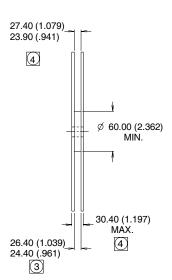


Note: For the most current drawing please refer to IR website at http://www.irf.com/package/

D²Pak Tape & Reel Information







NOTES:

- 1. COMFORMS TO EIA-418.
- 2. CONTROLLING DIMENSION: MILLIMETER.
- DIMENSION MEASURED @ HUB.
- INCLUDES FLANGE DISTORTION @ OUTER EDGE.

Ordering Information

Base part	Package Type	Standard Pack		Complete Part Number
		Form	Quantity	
AUIRF1010Z	TO-220	Tube	50	AUIRF1010Z
AUIRF1010ZL	TO-262	Tube	50	AUIRF1010ZL
AUIRF1010ZS	D2Pak	Tube	50	AUIRF1010ZS
		Tape and Reel Left	800	AUIRF1010ZSTRL
		Tape and Reel Right	800	AUIRF1010ZSTRR
DataSheet4U.com				

International TOR Rectifier

AUIRF1010Z/S/L

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15