APATL0000322

Date:Apr.16, 2008

For Messrs. Hitachi, Ltd., Consumer Business Group

CUSTOMER'S ACCEPTANCE SPECIFICATIONS

AX094A001A

CONTENTS

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Accepted by:_____   Proposed by: _____

Date:_____

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	IPS4PS2611-AX094A001A-1	11-1/1	8.DESIGNATION OF LOT MARK Added : JQA ″S″ Mark
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	IPS4PS2614-AX094A001A-2	14-1/2	11.Packing Added : Cushion Changed : Size:(L)1200×(W)1000×(H)685 　　　→ Size:(L)1200×(W)1000×(H)800
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	IPS4PS2610-AX094A001A-8	10-1/2,2/2	7.Dimensional out line Added : Holes of the under flame Changed : Shape of inverter covers
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	IPS4PS2611-AX094A001A-8	11-1/2,2/2	8.4 The Record of the revison described on the label Added : RevG : Inverter cover and Liquid crystal
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	—	—	11.Packing Added : Packing reliability
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Jun. 04, 2007	IPS4PS2611-AX094A001A-9	11-2/2	8.4 The Record of the revison described on the label Added : RevH : Changed Diffuser Board
	IPS4PS2611-AX094A001A-10	11-2/2	
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Jan.24, 2008	IPS4PS2611-AX094A001A-10	11-2/2	8.4 The Record of the revison described on the label Added : Rev.J : Changed Lamp
	IPS4PS2611-AX094A001A-11	11-2/2	
Apr.16, 2008	IPS4PS2611-AX094A001A-11	11-2/2	The Inverter changing OVP spec is added for spare parts. Current : 1070-1140V → Added : 1170-1300V 8.4 The Record of the revison described on the label Added : RevK: Changed Inverter OVP
	IPS4PS2611-AX094A001A-12	11-2/2	

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DESCRIPTION

The following specifications are applied to the following IPS-TFT module.

Note : Inverter for back light unit is built in this module.

Product Name : AX094A001A

General Specifications

Effective Display Area   : (H)819.60×(V)460.80   (mm)

Number of Pixels   : (H)1,366×(V)768   (pixels)

Pixel Pitch   : (H)0.600×(V)0.600   (mm)

Color Pixel Arrangement   : R+G+B Vertical Stripe

Display Mode   : Transmissive Mode
Normally Black Mode

Top Polarizer Type   : Anti-Glare

Number of Colors   : 16,777,216   (colors)

Viewing Angle Range   : Super Wide Version
(Horizontal & Vertical : 178°, CR≧10)

Input Signal   : 1-channel LVDS (LVDS:Low Voltage Differential Signaling)

Back Light   :20pcs. of EEFL(EEFL:External Electrode Fluoresent Lamp)

Moving Image Technology   :Flexible BI

External Dimensions   : (H)877.0×(V)516.8×(t)55.5   (mm)

Weight   : **9,500g Typ.**

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1. ABSOLUTE MAXIMUM RATINGS

1.1 Environmental Absolute Maximum Ratings

ITEM	Operating		Storage		Unit	Note
	Min.	Max.	Min.	Max.		
Temperature	0	**50**	-20	60	℃	1),5)
Humidity	2)		2)		%RH	1)
Vibration	-	4.9(0.5G)	-	14.7 (1.5G)	m/s ^{2}	3)
Shock	-	29.4(3G)	-	294 (30G)	m/s ^{2}	4)
Corrosive Gas	Not Acceptable		Not Acceptable		-	
Illumination at LCD Surface	-	50,000	-	50,000	lx	

Note　1) Temperature and Humidity should be applied to the glass surface of a IPS-TFT module,

　　　　　not to the system installed with a module.

　　　　　The temperature at the center of rear surface should be less than 70℃ on the condition of operating.

　　　　　The brightness of a EEFL tends to drop at low temperature. Besides, the life-time becomes shorter

　　　　　at low temperature.

　　　2) Ta≦40 ℃ ‥‥‥Relative humidity should be less than

　　　　　　　　　　　85%RH max. Dew is prohibited.

　　　　　Ta＞40 ℃ ‥‥‥Relative humidity should be lower than

　　　　　　　　　　　the moisture of the 85%RH at 40℃.

　　　3) Frequency of the vibration is between 15Hz and 100Hz. (Remove the resonance point)

　　　4) Pulse width of the shock is 10 ms.

　　　5) Long operation under low temperature may cause some portion of display area to be reddish for

　　　　　several minutes after turning on the product.

　　　　　However, it does not affect the characteristics and reliability of the product.

1.2 Electrical Absolute Maximum Ratings

(1)TFT Module

Vss = 0 V

ITEM	SYMBOL	Min.	Max.	Unit	Note
Power Supply Voltage	V_{DD}	0	13.2	V	
Input Voltage for logic	V_I	-0.3	3.6	V	1)
Electrostatic Durability	V_{ESD0}	±100		V	2),3)
	V_{ESD1}	±8		kV	2),4)

Note　1)It is applied to pixel data signal and clock signal.

　　　2)Discharge Coefficient：200pF-250Ω, Environmental：25℃-70％RH

　　　3)It is applied to I/F connector pins.

　　　4)It is applied to the surface of a metallic bezel and a LCD panel.

(2) Back-light Inverter

Vss = 0 V

ITEM	SYMBOL	Min.	Max.	Unit	Note
Input Voltage	Vin	0	28.0	V	
ON/OFF Control Input Voltage	ON/OFF	-0.3	5.5	V	
Brightness Control Voltage	BRT	-0.3	5.5	V	

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2. INITIAL OPTICAL CHARACTERISTICS

The following optical characteristics are measured under stable conditions. It takes about 30 minutes to reach stable conditions. The measuring point is the center of display area unless otherwise noted. The optical characteristics should be measured in a dark room or equivalent state.
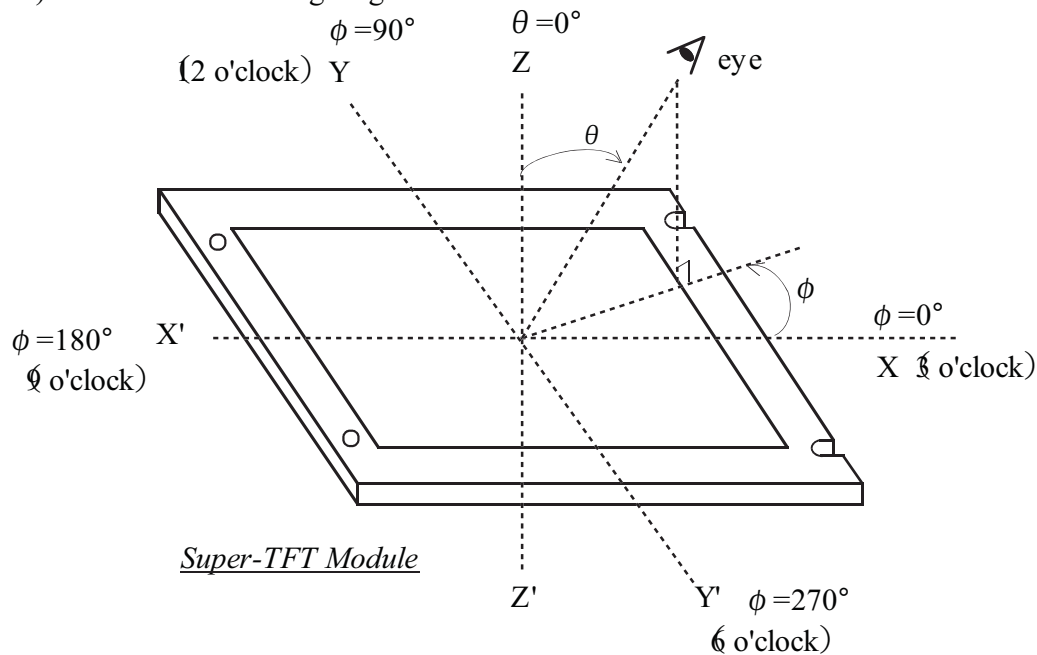
Measuring equipment :CS-1000A, or equivalent

Ambient Temperature =25℃、VDD=12.0V、f V=60Hz、

Vin=24V、BRT=3.3V

ITEM		SYMBOL	CONDITION	Min.		Typ.		Max.		UNIT	NOTE
				RevA~F	RevG	RevA~F	RevG	RevA~F	RevG		
Contrast Ratio		CR		600	650	850	1000	-		-	2)
Response Time	Rise	ton		-	-	9	8	20		ms	3)
	Fall	toff		-	-	7	6	20		ms	3)
Brightness of white		Bwh		420		(500)		-		cd/m^2	
Brightness uniformity		Buni		-		-		30		%	4)
Color Chromaticity (CIE)	Red	χ	θ=0° 1)	0.62		0.65		0.68		-	[Gray scale =255]
		y		0.30		0.33		0.36			
	Green	χ		0.26		0.29		0.32			
		y		0.58		0.61		0.64			
	Blue	χ		0.12		0.15		0.18			
		y		0.04		0.07		0.10			
	White	χ		0.242		0.272		0.302			
		y		0.248		0.278		0.308			
Variation of Color Position (CIE)	Red	Δχ	θ=+50° φ=0°,90° 180°,270° 1)	-		-		0.04		-	5) [Gray scale =255]
		Δy		-		-		0.04			
	Green	Δχ		-		-		0.04			
		Δy		-		-		0.04			
	Blue	Δχ		-		-		0.04			
		Δy		-		-		0.04			
	White	Δχ		-		-		0.04			
		Δy		-		-		0.04			
Contrast Ratio at 89°		CR89°		10		-		-		-	Estimated value

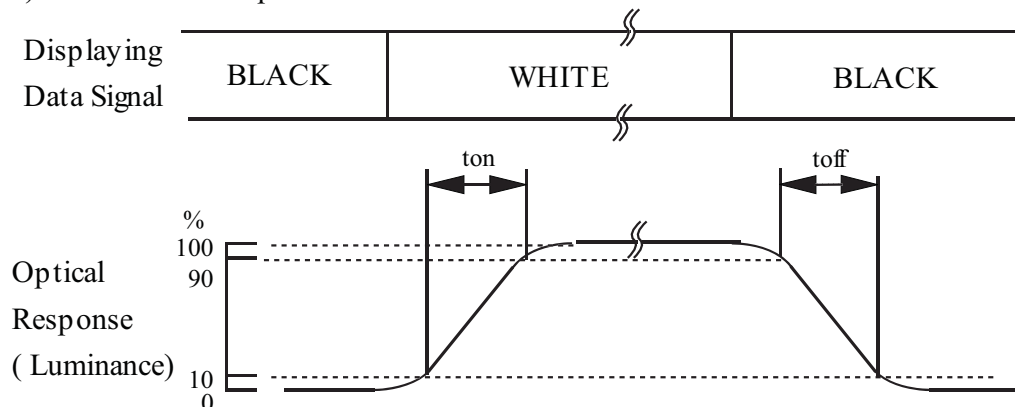| IPS Alpha Technology,Ltd.	Date	Apr.16, 2008	Sheet No.	IPS4PS 2605 -AX094A001A-12	Page	5-1/2

Note 1) Definition of Viewing Angle
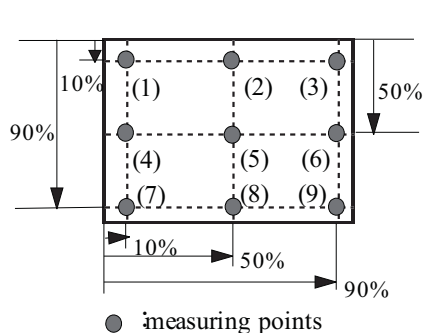

Super-TFT Module

2) Definition of Contrast Ratio (CR)

$$CR = \frac{(\text{Luminance at displaying WHITE})}{(\text{Luminance at displaying BLACK})}$$

3) Definition of Response Time

4) Definition of Brightness Uniformity

● measuring points

Display pattern is white (255 level). The brightness uniformity is defined as the following equation. Brightness at each point is measured, and average, maximum and minimum brightness is calculated.

$$B_{uni} = \frac{|B_{max} \text{ or } B_{min} - B_{ave}|}{B_{ave}} \times 100$$

where, B_{max} = Maximum brightness

B_{min} = Minimum brightness

B_{ave} = Average brightness = $\dfrac{\sum\limits_{k=1}^{9} (B(k))}{9}$

5) Variation of color position on CIE is defined as difference between colors at $\theta = 0°$ and at $\theta = 50°$ & $\phi = 0°, 90°, 180°, 270°$.

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3. ELECTRICAL CHARACTERISTICS

3.1 TFT-LCD Module

Ta=25℃, Vss=0V

ITEM		SYMBOL	Min.	Typ.	Max.	Unit	Note
Power Supply Voltage		V_{DD}	11.4	12.0	12.6	V	
Power Supply Current		I_{DD}	-	0.76	0.95	A	1),2)
Ripple Voltage of Power Supply		V_{DDR}	-	-	0.15	V	
FBI Mode select	High	FBI1，FBI2	2.2	2.5	3.3		
	Low		0	-	0.6		

Note 1) DC current at fv=60.0Hz, fCLK=**66**MHz, VDD=12.0V and Display pattern is Horizontal stripe.
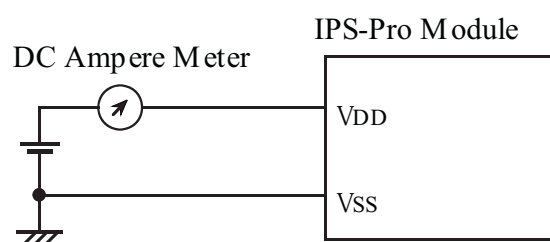
2) Current fuse is built in a module. Current capacity of power supply for VDD
    should be larger than 4A, so that the fuse can be opened at the trouble of power supply.

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3.2 Back Light

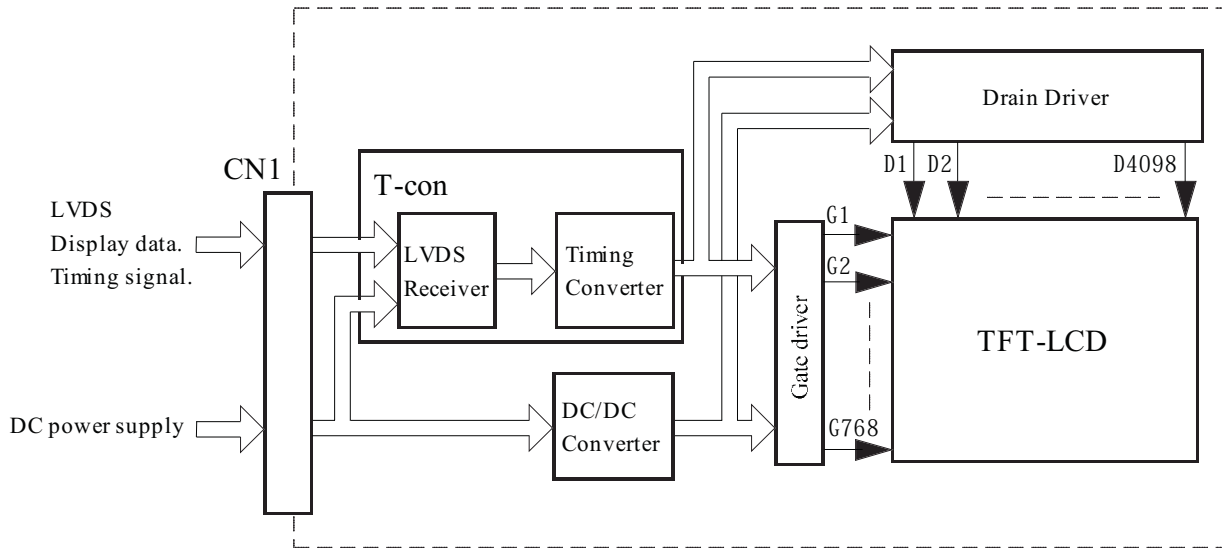| ITEM		Symbol	VALUE			Unit	Notes
			Min	Typ	Max		
Input Voltage		Vin	21.6	24.0	26.4	V	
Input Current		Iin	3.5	5.0	6.0	A	Vin=24V, BRT=3.3V, 1)
ON/OFF Control Input Voltage	ON	ON/OFF	2.0	-	5.5	V	
	OFF		-0.3	-	0.8	V	
Brightness Control Input Voltage	Min.Brightness	BRT	-0.1	0	0.1	V	
	Max.Brightness		3.0	-	3.3	V	
PWM Duty	Min.Brightness	on-Duty	-	20	-	%	Vin=24.0V,BRT=0V
	Max.Brightness		89	-	95	%	Vin=24.0V,BRT=3.3V
Output Current		IL	-	5.0	6.0	mArms	Vin=24.0V,BRT=3.3V
Output Frequency		f	45	50	55	kHz	
EEFL Life Time		-	50000	-	-	hours	2)

Note   1)This characteristics should be applied putting on the lamp about 20 minutes later
with ambient temperature. (Ta=25℃±2℃)

2)Life time of a lamp is defined. The life is determined as the time at which brighness of the
lamp is 50% compared to that of initial value at that typical lamp current on condition of
continuous operating at 25℃±2℃.

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4. BLOCK DIAGRAM

(1) Super-TFT Module

(2) Back light unit

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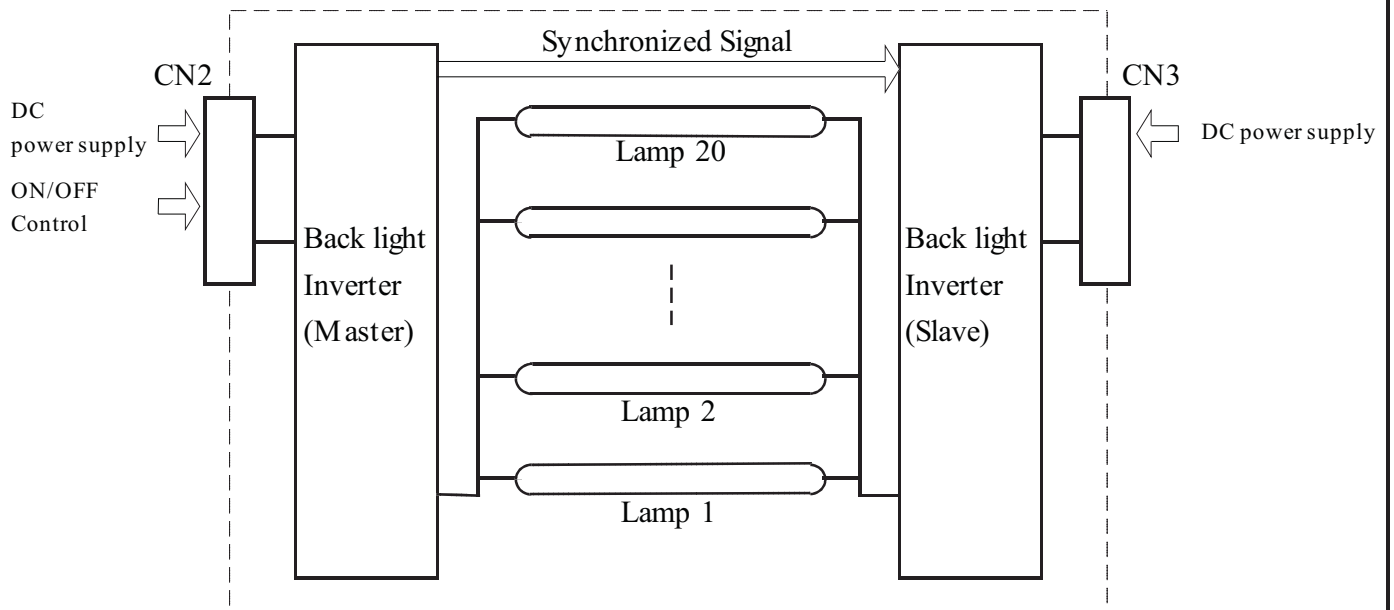
5. INTERFACE PIN ASSIGNMENT

5. 1 TFT-LCD MODULE

<u>CN1 : FX15S-41S-0.5SH</u>

(Matching connector :FX15S-41P-C)

Pin No.	Symbol	Description	Note	Pin No.	Symbol	Description	Note
1	VDD	Power Supply (typ.+12V)	1)	22	CLK-	Pixel Clock	3)
2	VDD			23	CLK+		
3	VDD			24	VSS	GND(0V)	2)
4	VDD			25	Rx3-	Pixel Data	3)
5	VDD			26	Rx3+		
6	VDD			27	VSS	GND(0V)	2)
7	VSS	GND(0V)	2)	28	IC	Internally Conected , Keep Open	
8	VSS			29	IC	Internally Conected , Keep Open	
9	VSS			30	IC	Internally Conected , Keep Open	
10	VSS			31	IC	Internally Conected , Keep Open	
11	VSS			32	FBI1	FBI Mode Select	
12	VSS			33	FBI2	FBI Mode Select	
13	Rx0-	Pixel Data	3)	34	BRT	Brightness Control	
14	Rx0+			35	IC	Internally Conected , Keep Open	
15	VSS	GND(0V)	2)	36	IC	Internally Conected , Keep Open	
16	Rx1-	Pixel Data	3)	37	IC	Internally Conected , Keep Open	
17	Rx1+			38	IC	Internally Conected , Keep Open	
18	VSS	GND(0V)	2)	39	IC	Internally Conected , Keep Open	
19	Rx2-	Pixel Data	3)	40	IC	Internally Conected , Keep Open	
20	Rx2+			41	IC	Internally Conected , Keep Open	
21	VSS	GND(0V)	2)				

Notes   1) All VDD pins shall be connected to +12.0V(Typ.).

2) All VSS pins shall be grounded. Metal bezel is internally connected to VSS.

3) Rx n+ and Rx n- (n=0,1,2,3) should be wired by twist-pairs or side-by-side FPC patterns, respectively.

5. 2 FBI Mode Select

FBI Mode	FBI1	FBI2
Normal	Low	Low
FBI A-mode	High	Low
FBI C-mode	Low	High
Demo mode	High	High

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5. 3 BACK-LIGHT UNIT

<u>CN2 : JST S12B-PH-SM4-TB</u>

(Matching connector : JST PHR-12)

Pin No.	SYMBOL	Description	Note
1	VIN	Power supply(Typ. 24.0V)	1)
2	VIN		
3	VIN		
4	VIN		
5	VIN		
6	VSS	GND(0V)	2)
7	VSS		
8	VSS		
9	VSS		
10	VSS		
11	IC	Internally Conected , Keep Open	
12	ON/OFF	High:Lamp ON,　　Low:Lamp OFF	

Notes　1) All VIN pins shall be connected to +24.0V(Typ.).

2) All VSS pins shall be grounded. Metal bezel is internally connected to VSS.

<u>CN3 : JST S12B-PH-SM4-TB</u>

(Matching connector : JST PHR-12)

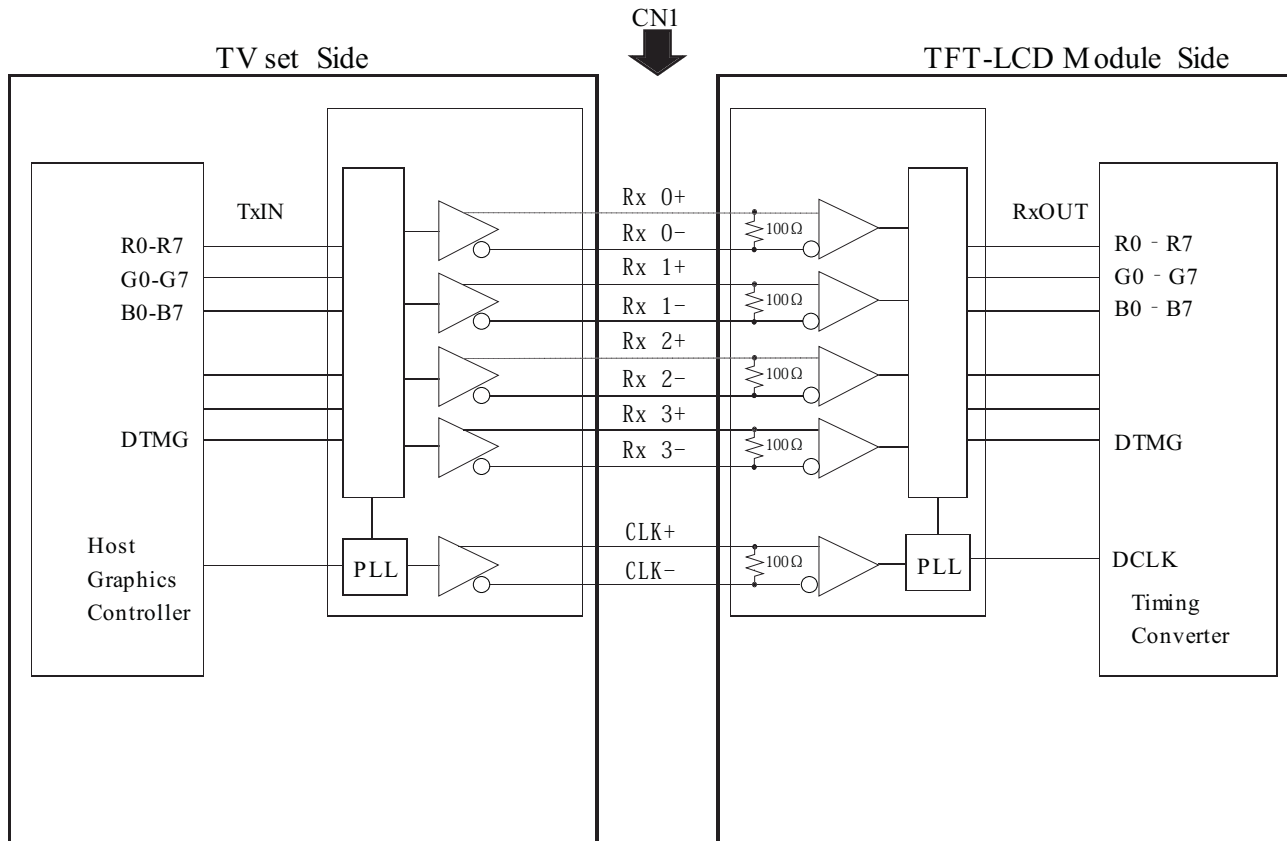| Pin No.	SYMBOL	Description	Note
1	VIN	Power supply(Typ. 24.0V)	1)
2	VIN		
3	VIN		
4	VIN		
5	VIN		
6	VSS	GND(0V)	2)
7	VSS		
8	VSS		
9	VSS		
10	VSS		
11	IC	Internally Conected , Keep Open	
12	IC	Internally Conected , Keep Open	

Notes　1) All VIN pins shall be connected to +24.0V(Typ.).

2) All VSS pins shall be grounded. Metal bezel is internally connected to VSS.

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BLOCK DIAGRAM OF INTERFACE

CN1

TV set Side   TFT-LCD Module Side

TxIN	RxOUT
R0-R7	R0‐R7
G0-G7	G0‐G7
B0-B7	B0‐B7
DTMG	DTMG
Host Graphics Controller	DCLK
PLL	PLL Timing Converter

Rx 0+
Rx 0−
Rx 1+
Rx 1−
Rx 2+
Rx 2−
Rx 3+
Rx 3−
CLK+
CLK−
100Ω

R0～R7	：Pixel R Data
G0～G7	：Pixel G Data
B0～B7	：Pixel B Data
DTMG	：Display timing signal

Notes   1) The system must have the transmitter to drive the module.
          2) LVDS cable impedance shall be 50 ohms per signal line or about 100 ohms per twist-pair line when it is used differentially.

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LVDS INTERFACE

	SIGNAL	TRANSMITTER THC63LVDM83A		INTERFACE CONNECTOR		RECEIVER THC63LVDF84A		TFT CONTROL INPUT
		PIN	INPUT	PC	TFT-LCD	PIN	OUTPUT	
24bit	R2	51	Tx IN0	TA OUT0+	Rx 0+	27	Rx OUT0	R2
	R3	52	Tx IN1			29	Rx OUT1	R3
	R4	54	Tx IN2			30	Rx OUT2	R4
	R5	55	Tx IN3			32	Rx OUT3	R5
	R6	56	Tx IN4			33	Rx OUT4	R6
	R7	3	Tx IN6	TA OUT0-	Rx 0-	35	Rx OUT6	R7
	G2	4	Tx IN7			37	Rx OUT7	G2
	G3	6	Tx IN8			38	Rx OUT8	G3
	G4	7	Tx IN9			39	Rx OUT9	G4
	G5	11	Tx IN12	TA OUT1+	Rx 1+	43	Rx OUT12	G5
	G6	12	Tx IN13			45	Rx OUT13	G6
	G7	14	Tx IN14			46	Rx OUT14	G7
	B2	15	Tx IN15	TA OUT1-	Rx 1-	47	Rx OUT15	B2
	B3	19	Tx IN18			51	Rx OUT18	B3
	B4	20	Tx IN19			53	Rx OUT19	B4
	B5	22	Tx IN20			54	Rx OUT20	B5
	B6	23	Tx IN21	TA OUT2+	Rx 2+	55	Rx OUT21	B6
	B7	24	Tx IN22			1	Rx OUT22	B7
	RSVD 1)	27	Tx IN24			3	Rx OUT24	not connect
	RSVD 1)	28	Tx IN25	TA OUT2-	Rx 2-	5	Rx OUT25	not connect
	DTMG	30	Tx IN26			6	Rx OUT26	DTMG
	R0	50	Tx IN27			7	Rx OUT27	R0
	R1	2	Tx IN5	TA OUT3+	Rx 3+	34	Rx OUT5	R1
	G0	8	Tx IN10			41	Rx OUT10	G0
	G1	10	Tx IN11			42	Rx OUT11	G1
	B0	16	Tx IN16			49	Rx OUT16	B0
	B1	18	Tx IN17	TA OUT3-	Rx 3-	50	Rx OUT17	B1
	RSVD 1)	25	Tx IN23			2	Rx OUT23	not connect
	DCLK	31	TxCLK IN	TxCLK OUT+ TxCLK OUT-	RxCLK IN+ RxCLK IN-	26	RxCLK OUT	DCLK

R0～R7 ：Pixel R Data （7；MSB, 0；LSB）
G0～G7 ：Pixel G Data （7；MSB, 0；LSB）
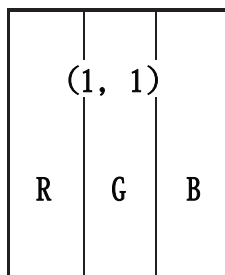B0～B7 ：Pixel B Data （7；MSB, 0；LSB）
DTMG ：Display timing signal

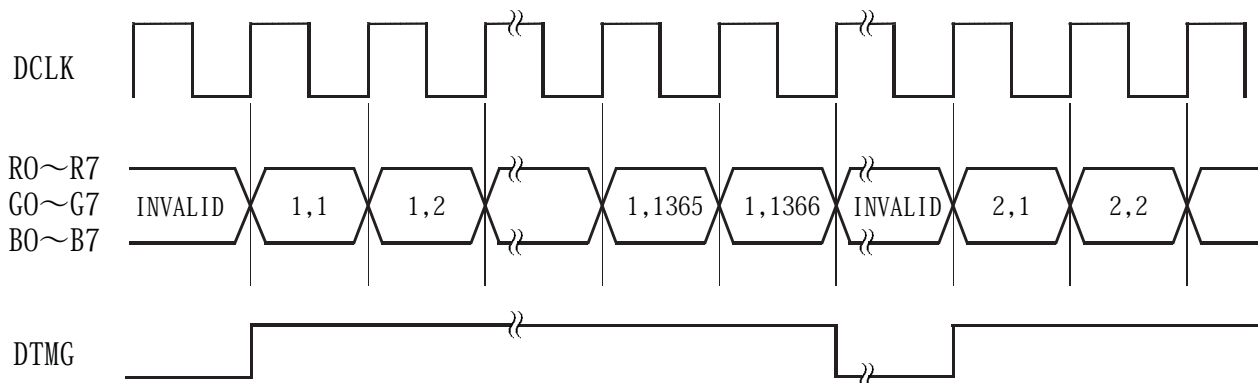Notes   1) RSVD(reserved) pins on the transmitter shall be "H" or "L".

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CORRESPONDENCE BETWEEN INPUT DATA AND DISPLAY IMAGE

Display data of adjacent one pixel is latched during one cycle of DCLK.

(1, 1)

| R | G | B |

pixel ： R0～R7 :R data
G0～G7 :G data
B0～B7 :B data

1,1	1,2	1,3	— — — — — — — — — — — —	1,1366
2,1	2,2	2,3	— — — — — — — — — — — —	2,1366
3,1	3,2	3,3	— — — — — — — — — — — —	3,1366
768,1	768,2	768,3	— — — — — — — — — — — —	768,1366

DCLK

R0～R7
G0～G7 | INVALID | 1,1 | 1,2 | | 1,1365 | 1,1366 | INVALID | 2,1 | 2,2 |
B0～B7

DTMG

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RELATIONSHIP BETWEEN DISPLAY COLORS AND INPUT SIGNALS

Input Color		Red Data								Green Data								Blue Data							
		R7	R6	R5	R4	R3	R2	R1	R0	G7	G6	G5	G4	G3	G2	G1	G0	B7	B6	B5	B4	B3	B2	B1	B0
		MSB							LSB	MSB							LSB	MSB							LSB
Basic Color	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red (255)	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Green(255)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	Blue (255)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Cyan	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Magenta	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
Red	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red (1)	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red (2)	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	Red (254)	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red (255)	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Green	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Green(1)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0
	Green(2)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	Green(254)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0
	Green(255)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
Blue	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Blue(1)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
	Blue(2)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	Blue(254)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0
	Blue(255)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1

Notes   1) Definition of gray scale:

         Color(n)・・・・Number in parenthesis indicates gray scale level. Larger n corresponds to brighter level.

     2) Data: 1:High,   0:Low
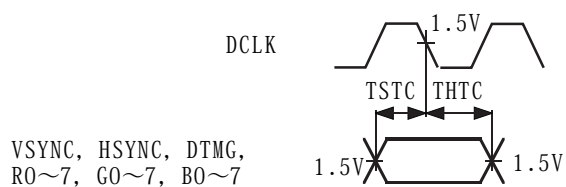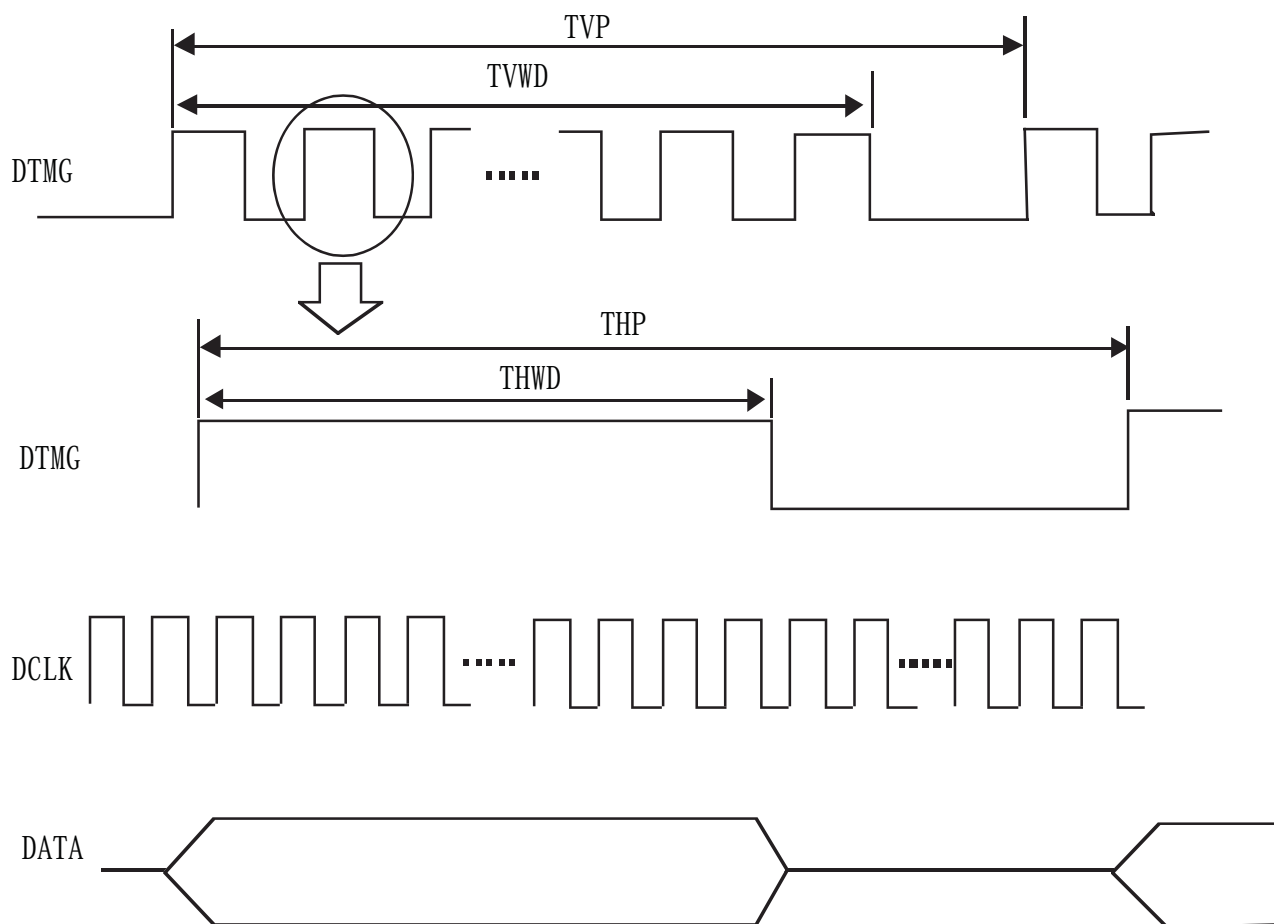
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6. INTERFACE TIMING

6.1 LVDS receiver timing
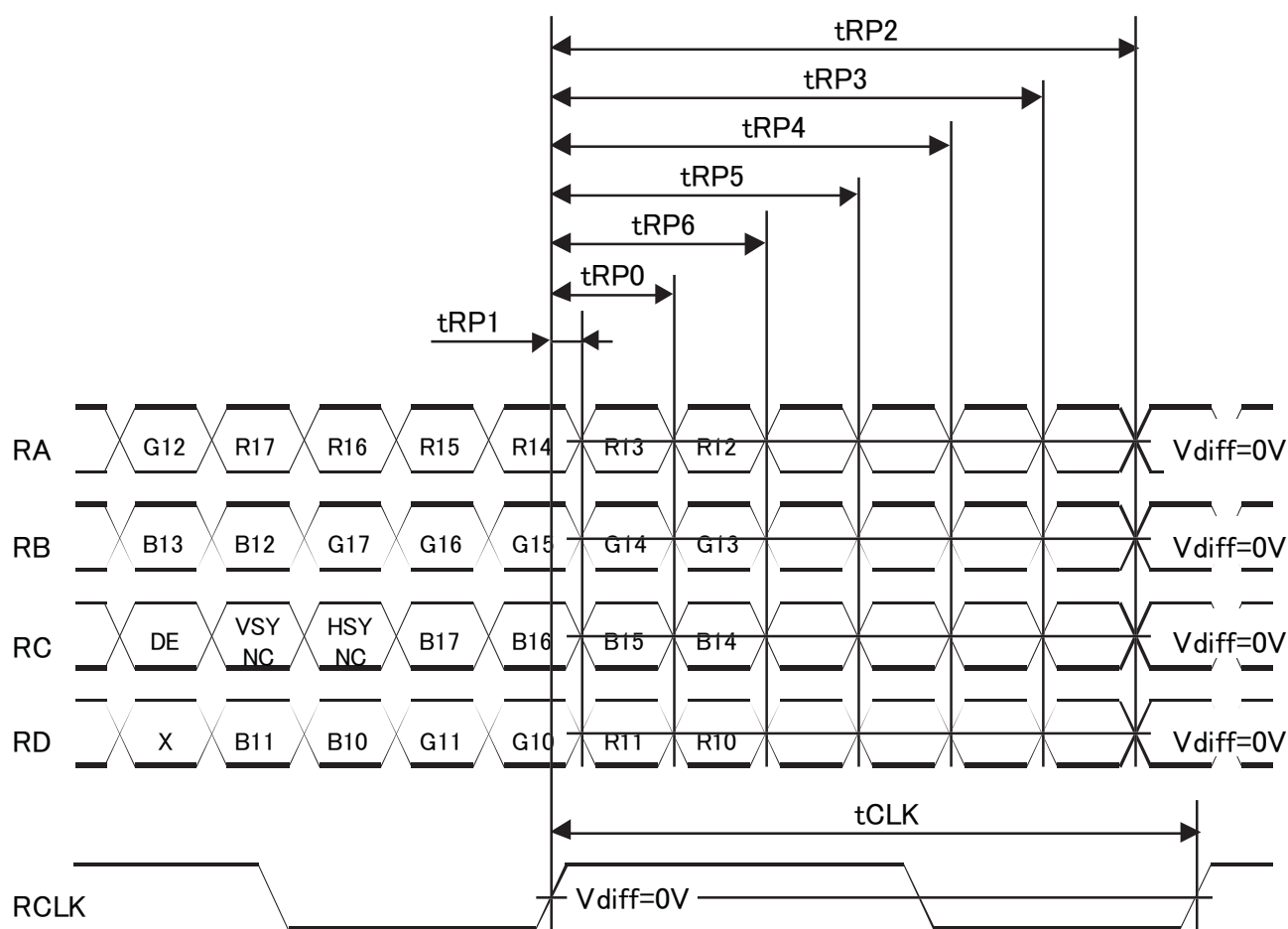
RA=(RA+)−(RA−), RB=(RB+)−(RB−), RC=(RC+)−(RC−), RD=(RD+)−(RD−)
RCLK=(RCLK+)−(RCLK−)

ITEM		SYMBOL	MIN.	TYP.	MAX.	UNIT
DCLK	Frequency	1/tCLK	64	66	68	MHz
RA RB RC RD	0 data position	tRP0	1/7tCLK − 0.15	1/7tCLK	1/7tCLK + 0.15	ns
	1st data position	tRP1	−0.15	0	+0.15	
	2nd data position	tRP2	2/7tCLK − 0.15	2/7tCLK	2/7tCLK + 0.15	
	3rd data position	tRP3	3/7tCLK − 0.15	3/7tCLK	3/7tCLK + 0.15	
	4th data position	tRP4	4/7tCLK − 0.15	4/7tCLK	4/7tCLK + 0.15	
	5th data position	tRP5	5/7tCLK − 0.15	5/7tCLK	5/7tCLK + 0.15	
	6th data position	tRP6	6/7tCLK − 0.15	6/7tCLK	6/7tCLK + 0.15	

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6.2 TIMING CHART

Notes   1) Reference level for each timing signal is 1.5V unless it is stated on the chart , high level voltage(VIH) and low level voltage(VIL) are defined as follows:

$$V_{IH} \geq 2.0\,V \qquad V_{IL} \leq 0.8\,V$$

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6.3 INTERFACE TIMING SPECIFICATIONS

Item		Symbol	Min.	Typ.	Max.	Unit	Note
DCLK	Frequency	fCLK	64	66	68	MHz	D=TCIL/TCIP
	Duty	D	0.35	0.5	0.65	–	
DTMG	Horizontal period	THP	1400	1406	1450	TCIP	
	Horizontal Width-Active	THWD	1366	1366	1366	TCIP	
	One line scanning freq.	fH	46.5	46.9	48.5	kHz	
	Vertical Period	TVP	772	783	825	THP	
	Vertical Width-Active	TVWD	768	768	768	THP	
	Frame frequency	fV	57	60	63	Hz	
COMMON	Set up Time	TSTC	5	–	–	ns	
	Hold Time	THTC	3	–	–	ns	

In addition to the above, these timing should conforms to the followings.

1) TSTC and THTC conforms to the specifications of LVDS transmitter.
   It is preferable to check the specifications of LVDS transmitter in your system.
2) Frequency of power supply for a EEFL may cause the interference with HSYNC frequency and cause beat or flicker on the display.
   Therefore, lamp frequency shall be as different as possible from HSYNC frequency in order to avoid the interference.

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6.4 TIMING BETWEEN INTERFACE SIGNALS AND POWER SUPPLY
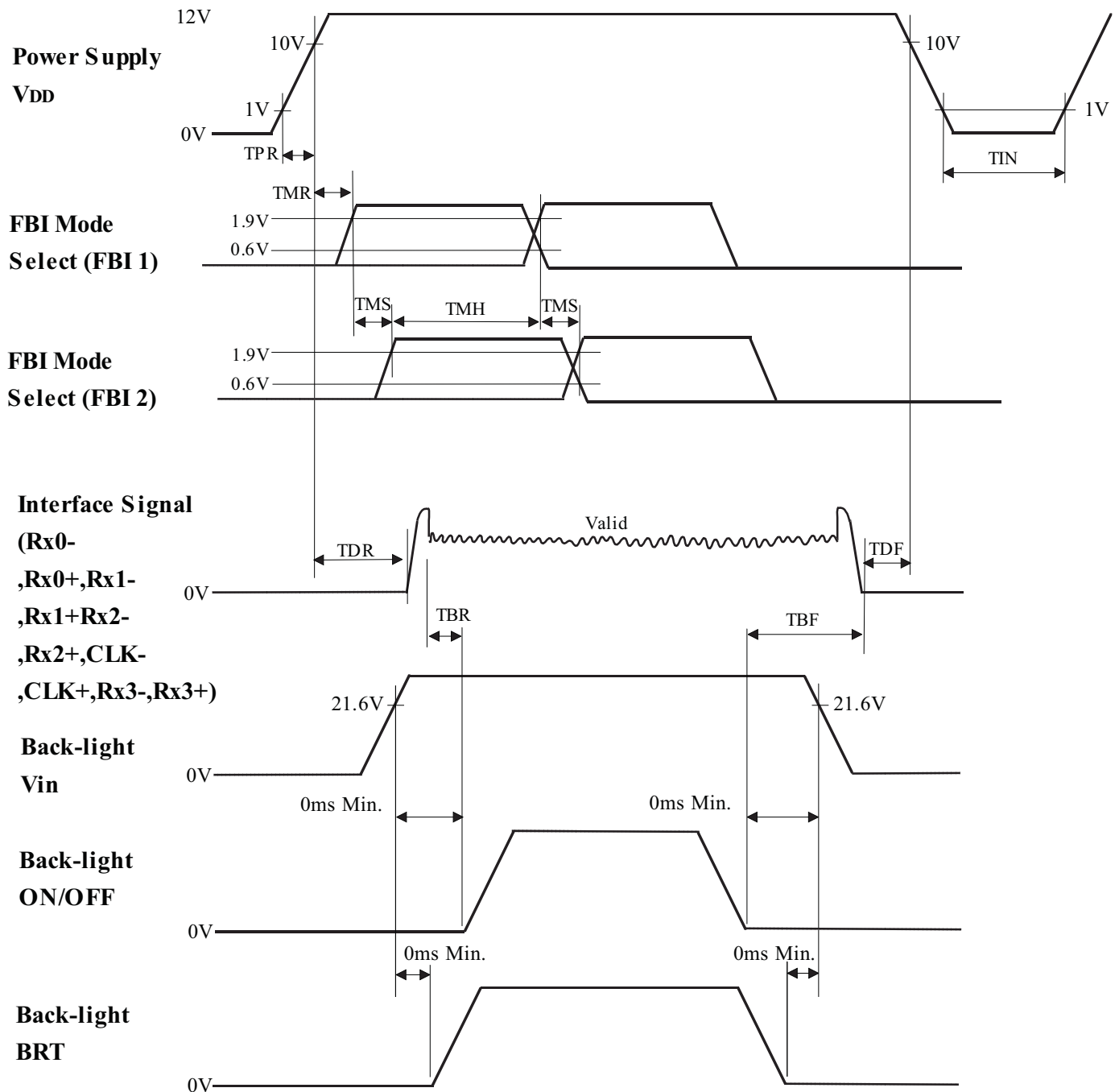
Note

1)Timing of power supply voltage and input signals should be used under the following specifications.

$0ms \leqq TPR \leqq 10ms$

$10ms \leqq TDR \leqq 55ms$

$0ms \leqq TDF \leqq 50ms$

$350ms \leqq TIN$

$200ms \leqq TBR$

$TBF \leqq 100ms$

$100ms \leqq TMR$

$-10ms \leqq TMS \leqq -550us, 550us \leqq TMS \leqq 10ms$

$200ms \leqq TMH$

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7.DIMENSIONAL OUT LINE
(1)FRONT VIEW
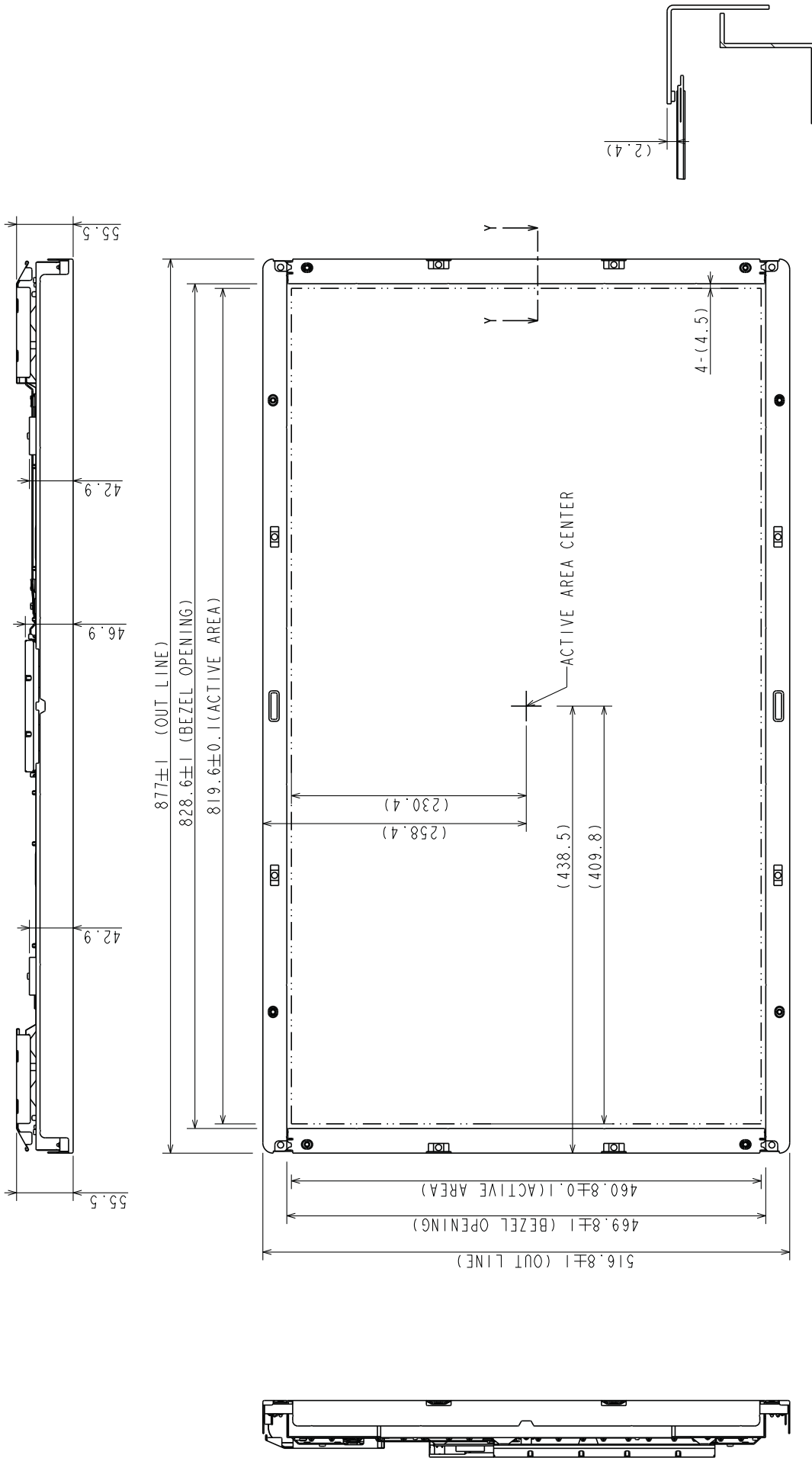
55.5

42.9

46.9

42.9

55.5

877±1 (OUT LINE)

828.6±1 (BEZEL OPENING)

819.6±0.1(ACTIVE AREA)

ACTIVE AREA CENTER

4-(4.5)

(230.4)

(258.4)

(438.5)

(409.8)

460.8±0.1(ACTIVE AREA)

469.8±1 (BEZEL OPENING)

516.8±1 (OUT LINE)

SECTION Y-Y

(2.4)

Note 1) The dimension in a parenthesis is a reference value.
2) Unspecified tolerance to be ±1.0

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(2)BACK VIEW

[UPPER SIDE]

[LOWER SIDE]

DETAIL A

M4 BR
USER HOLE
DP:MAX.6.0

∅6 (HT:2.0)

M4 BR
USER HOLE
DP:MAX.6.0

37

30±0.5
(47)

(217)
(32)
(120)

LAMP SUPPORT
HT:(3.5)

(55)

IF2

(280)
(235)
(323)

(438.5)

(119.2)

LAMP SUPPORT AREA

(135)
(85)

(143.4)
(139.2)

IF1

(877)
530±0.5
(129)

0

(96)
(118)

(316.5)

(438.5)

CABLE_CLIP
HT:(6)

(85)
(35)
(135)

(171)

LAMP SUPPORT AREA

FBI_CABLE

(15)
(35)
(300)

(55)
(323)
(235)

(316.5)

DETAIL A

CABLE_CLIP
HT:(6)

IF2

(13)

(120)

(258.4)
(258.4)

(95)

(280)

400±0.5

(516.8)

Note 1) The dimension in a parenthesis is a reference value.
     2) Unspecified tolerance to be ±1.0

8. DESIGNATION OF LOT MARK

8.1 LOT MARK

6 digits for production number
（00001～99999）

Special mark   4)

Production management sign

Week   3)

Month   2)

Year   1)
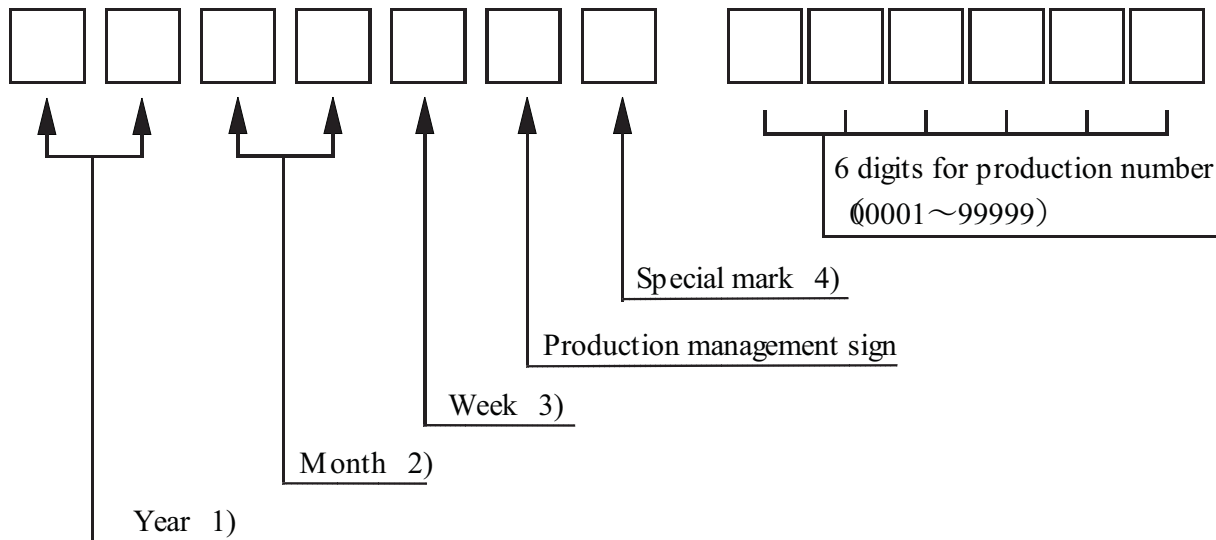
Notes

1)

Year	Mark
2006	06
2007	07
2008	08

2)

Month	Mark	Month	Mark
1	01	7	07
2	02	8	08
3	03	9	09
4	04	10	10
5	05	11	11
6	06	12	12

3)

Week（Day）	Mark
1～7	1
8～14	2
15～21	3
22～28	4
29～31	5

4)   It is the mark that was opened up by production person to take correspondence with production number.

8.2 Revision (REV.) control

REV. is the column for manufacturing convenience. A-Z except I and O may be written on this column.

8.3 Location of lot mark

Lot mark is printed on a label. The label is on the metallic bezel as shown in 7. External Dimensional. The style of character will be changed without notice.
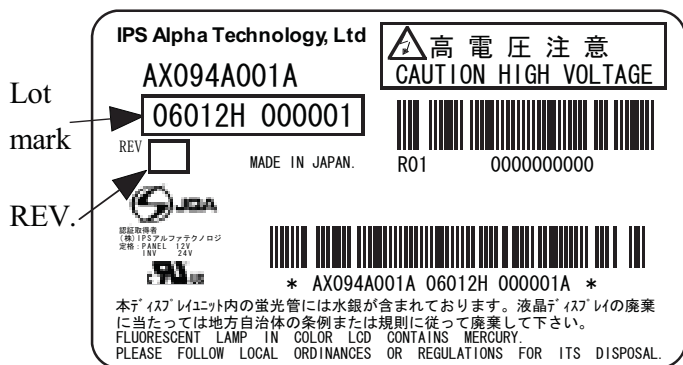
REV.A~F   REV.G~

Liquid crystal identification division

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8.4 Record of revision descrived on the label

Revision	Specification	A	B	C	D	E	F	G	H	J	K
Initial		○									
Inverter Rev.B	Inverter Added : R39=240kΩ Changed:C32=0.1uF→0.47uF		○								
Inverter Rev.C	Inverter Changed:C32=0.1uF→0.47uF C11=0.33uF→1uF R20/R106=4.7kΩ→3.9kΩ R49=100kΩ,R58=30kΩ→68kΩ R131=NM→0Ω,R132=0Ω→NM			○	○	○	○	○	○	○	○
UL Mark					○	○	○	○	○	○	○
TCON 7th cut	TCON IC:6th cut→7th cut				○	○	○	○	○	○	○
LCD	Improvement of vibration performance						○	○	○	○	○
Inverter cover	Change of shape for vibration noise							○	○	○	○
Liquid crystal								○	○	○	○
Diffuser Board	Changed supplyer								○	○	○
Lamp	Changed lamp									○	○
Inverter OVP	OVP 1070~1140V→1170～1300V										○

○ : Application product

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9. COSMETIC SPECIFICATIONS

9.1 Condition for cosmetic inspection

(1) Viewing zone

    a) The figure shows the correspondence between eyes (of inspector) and TFT-LCD module.

        $\theta \leqq 45°$: when non-operating inspection

        $\theta \leqq 5°$: when operating inspection

    b) Inspection should be executed only from front side and only A-zone.

    Cosmetic of B-zone and C-zone are ignore.

    (refer to 9.2 Definition of zone)

Inspection view

Light

about 500mm

θ θ

TFT-LCD Module
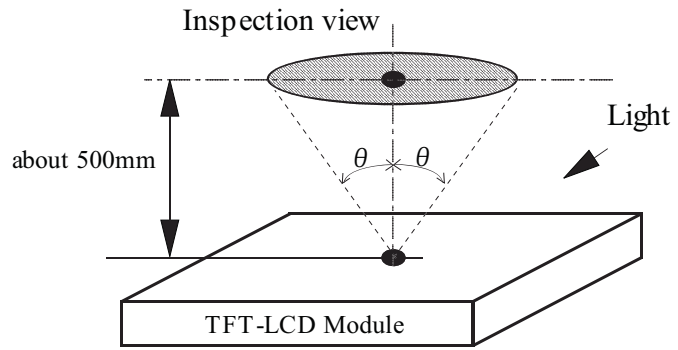
(2) Environmental

    a) Temperature : 25 degrees

    b) Ambient light : about 700 lx and non-directive when operating inspection.

                : about 1000 lx and non-directive when non-operating inspection.

    c) Back-light : when non-operating inspection, back-light should be off.

9.2 Definition of zone
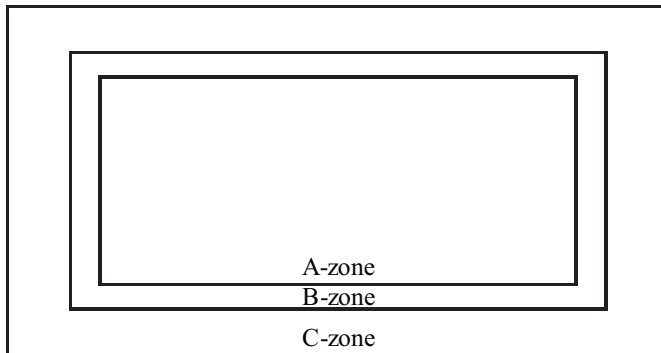
A-zone

B-zone

C-zone

・A-zone : Display area (pixel area)

・B-zone : Area between A-zone and C-zone

・C-zone : Metallic bezel area

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9.3 COSMETIC SPECIFICATIONS

When displaying conditions are not stable (ex. at turn on or off), the following specifications are not applied.

	No.	ITEM			Max. acceptable number A-zone	Unit	Note
Operating inspection	1	Dot defect	Sparkle mode	1-dot	0	pcs	1),2),4)
				2-dots	0	Units	1),2),5)
				3-dots	0		
				4-dots	0		
				Density	0	pcs/ φ20mm	1),2),6)
				Total	0	pcs	1),2)
			Black mode	1-dot	5	pcs	1),3),4)
				2-dots	1	Units	1),3),5)
				3-dots	0		
				4-dots	0		
				Density	3	pcs/ φ20mm	1),3),6)
				Total	5	pcs	1),3)
				Total	5	pcs	1)
	2	Line defect			Serious one is not allowed.	—	—
	3	Uneven brightness					
	4	Stain inclusion [Line shape W : width(mm) L : length(mm)]	W≦0.02	L : Ignore	Ignore	pcs	7)
			W≦0.04	L≦4.0	8		
				L>4.0	0		
			W≦0.08	L≦2.0	8		
				L>2.0	0		
			W>0.08	—	(See dot shape)		
	5	Stain inclusion [Dot shape D : ave. dia.(mm)]	D≦0.22		Ignore	pcs	7)
			D≦0.5		8		
			D>0.5		0		
	6	Scratch on polarizer [Line shape W : width(mm) L : length(mm)]	W≦0.02	L : Ignore	Ignore	pcs	8)
			W≦0.08	L≦20	10		
				L>20	0		
			W>0.08	—	0		
	7	Scratch on polarizer [Dot shape D : ave. dia.(mm)]	D≦0.2		Ignore	pcs	8)
			D≦0.6		10		
			D>0.6		0		

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	No.	ITEM		Max. acceptable number A-zone	Unit	Note
non-operating inspection	8	Bubbles, peeling in polarizer [D : ave. dia.(mm)]	D≦0.2	Ignore	pcs	8)
			D≦0.5	10		
			D> 0.5	0		
	9	Wrinkles on polarizer		Serious one is not allowed.	-	-

Note 1) Dot defect : defect area > 1/2 dot

2) Sparkle mode : brightness of dot is more than 30% at black. (visible to eye)

3) Black mode : brightness of dot is less than 70% at white. (visible to eye)

4) 1 dot : defect dot is isolated, not attached to other defect dot.

5) N dots : N defect dots are consecutive. (N means the number of defects dots)

6) Density : number of defect dots inside 20mm ϕ .

7) Those stains which can be wiped out easily are acceptable.

8) Polarizer area inside of B-zone is not applied.

9) No major (serious) defects when viewed in gray scale mode.

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10. PRECAUTION

Please pay attention to the followings when a IPS-TFT module with a back-light unit is used, handled and mounted.

10.1 Precaution to handling and mounting

(1) Applying strong force to a part of the module may cause partial deformation of frame or mold, and cause damage to the display.

(2) The module should gently and firmly be held by both hands. Never hold by just one hand in order to avoid any internal damage. Never drop or hit the module.

(3) The module should be installed with mounting holes of a module.

(4) Uneven force such as twisted stress should not be applied to a module when a module is mounted on the cover case. The cover case must have sufficient strength so that external force can not be transmitted directly to a module.

(5) It is recommended to leave a space between a module and a holding board of a module so that partial force is not applied to a module.
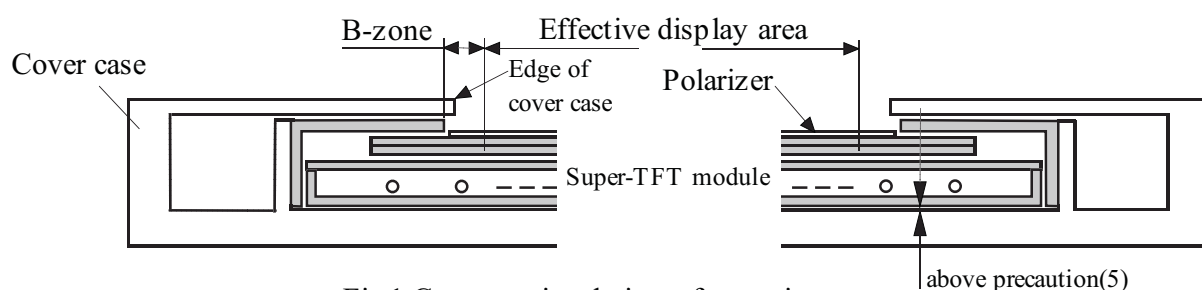
Fig.1 Cross sectional view of a monitor set

(6) The edge of a cover case should be located inside more than 1mm from the edge of a module front frame.

(7) A transparent protective plate should be added on the display area of a module in order to protect a polarizer and IPS-TFT cell. The transparent protective plate should have sufficient strength so that the plate can not touch a module by external force.

(8) Materials included acetic acid and choline should not be used for a cover case as well as other parts and boards near a module. Acetic acid attacks a polarizer. Choline attacks electric circuits due to electro-chemical reaction.

(9) The polarizer on a TFT cell should carefully be handled due to its softness, and should not be touched, pushed or rubbed with glass, tweezers or anything harder than HB pencil lead. The surface of a polarizer should not be touched and rubbed with bare hand, greasy clothes or dusty clothes.

(10) The surface of a polarizer should be gently wiped with absorbent cotton, chamois or other soft materials slightly contained petroleum benzene when the surface becomes dirty. Normal-hexane as cleaning chemicals is recommended in order to clean adhesives which fix front/rear polarizers on a IPS-TFT cell. Other cleaning chemicals such as acetone, toluen and alcohol should not be used to clean adhesives because they cause chemical damage to a polarizer.

(11) Saliva or water drops should be immediately wiped off. Otherwise, the portion of a polarizer may be deformed and its color may be faded.

(12) The module should not be opened or modified. It may cause not to operate properly.

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(13) Metallic bezel of a module should not be handled with bare hand or dirty gloves. Otherwise, color of a metallic frame may become dirty during its storage. It is recommended to use clean soft gloves and clean finger stalls when a module is handled at incoming inspection process and production (assembly) process.

(14) Lamp(EEFL) cables should not be pulled and held.

10.2 Precaution to operation

(1) The ambient temperature near the operated module should be satisfied with the absolute maximum ratings. Unless it meets the specifications, sufficient cooling system should be adopted to system.

(2) The spike noise causes the mis-operation of a module. The level of spike noise should be as follows:
$-200mV \leq$ over- and under- shoot of $VDD \leq +200mV$
VDD including over- and under- shoot should be satisfied with the absolute maximum ratings.

(3) Optical response time, luminance and chromaticity depend on the temperature of a IPS-TFT module. Response time and saturation time of EEFL luminance become longer at lower temperature operation.

(4) Sudden temperature change may cause dew on and/or in the a module. Dew males damage to a polarizer and/or electrical contacting portion. Dew causes fading of displayed quality.

(5) Fixed patterns displayed on a module for a long time may cause after-image. It will be recovered soon.

(6) A module has high frequency circuits. Sufficient suppression to electromagnetic interference should be done by system manufacturers. Grounding and shielding methods may be effective to minimize the interference.

(7) Noise may be heard when a back-light is operated. If necessary, sufficient suppression should be done by system manufacturers.

(8) The module should not be connected or removed while a main system works.

(9) Inserting or pulling I/F connectors causes any trouble when power supply and signal dates are on-state.I/F connectors should be inserted and pulled after power supply and signal dates are turned off.

10.3 Electrostatic discharge control

(1) Since a module consists of a IPS-TFT cell and electronic circuits with CMOS-ICs, which are very weak to electrostatic discharge, persons who are handling a module should be grounded through adequate methods such as a list band. I/F connector pins should not be touched directly with bare hands.

(2) Protection film for a polarizer on a module should be slowly peeled off so that the electrostatic charge can be minimized.

10.4 Precaution to strong light exposure

(1) A module should not be exposed under strong light. Otherwise, characteristics of a polarizer and color filter in a module may be degraded.

10.5 Precaution to storage

When modules for replacement are stored for a long time, following precautions should be taken care of:

(1) Modules should be stored in a dark place. It is prohibited to apply sunlight or fluorescent light during storage. Modules should be stored at 0 to 35°C at normal humidity (60%RH or less).

(2) The surface of polarizers should not come in contact with any other object. It is recommended that modules should be stored in the Hitachi's shipping box.

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10.6 Precaution to handling protection film

(1) The protection film for polarizers should be pealed off slowly and carefully by persons who are electrically grounded with adequate methods such as a list band. Besides, ionized air should be blown over during peeling action. Dusts on a polarizer should be blown off by an ionized nitrogen gun and so on.

(2) The protection film should be peeling off without rubbing it to the polarizer. Because, if the film is rubbed together with the polarizer, since the film is attached to the polarizer with a small amount of adhesive, the adhesive may remain on a polarizer.

(3) The module with protection film should be stored on the conditions explained in 10.5 (1). However, in case that the storage time is too long, adhesive may remain on a polarizer even after a protection film is peeled off. Besides, in case that a module is stored at higher temperature and/or higher humidity, adhesive may remain on a polarizer. The remained adhesive may cause non-uniformity of display image.

(4) The adhesive can be removed easily with Normal-Hexane. The remained adhesive or its vestige on the polarizer should be wiped off with absorbent cotton or other soft materials such as chamois slightly contained Normal-Hexane.

10.7 Safety

(1) Since a IPS-TFT cell and lamps are made of glass, handling to the broken module should be taken care sufficiently in order not to be injured. Hands touched liquid crystal from a broken cell should be washed sufficiently.

(2) The module should not be taken apart during operation so that back-light drives by high voltage.

10.8 Environmental protection

(1) The IPS-TFT module contains cold cathode fluorescent lamps. Please follow local ordinance or regulations for its disposal.

(2) Flexible printed circuits and printed circuits board used in a module contain small amount of lead. Please follow local ordinance or regulations for its disposal.

10.9 Use restrictions and limitations

(1) This product is not authorized for use in life support devices or systems, military applications or other applications which pose a significant risk of personal injury.

(2) In no event shall IPS Alpha Technology, Ltd., be liable for any incidental, indirect or consequential damages in connection with the installation or use of this product, even if informed of the possibility thereof in advance. These limitations apply to all causes of action in the aggregate, including without limitation breach of contact, breach of warranty, negligence, strict liability, misrepresentation and other torts.
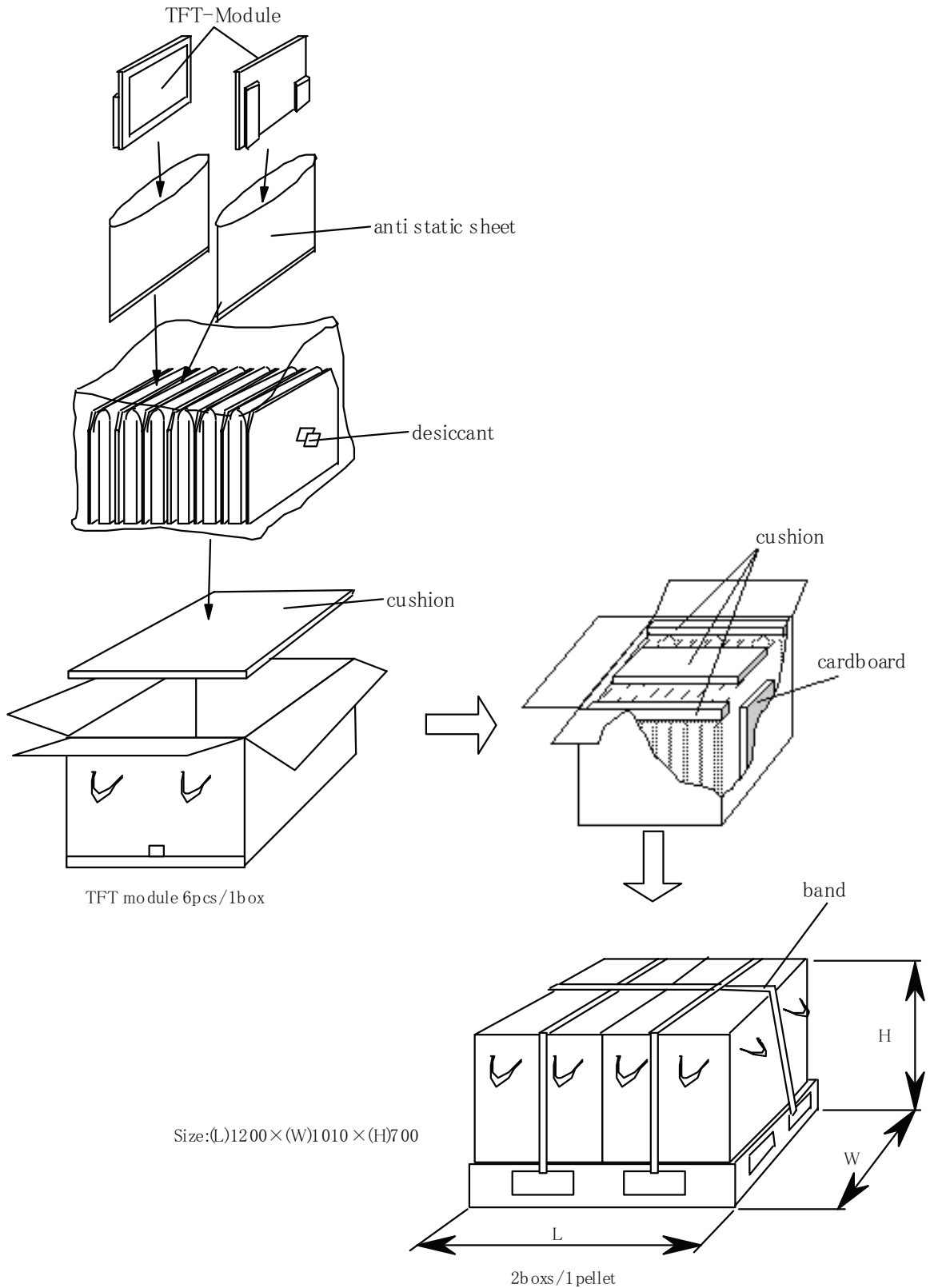
10.10 Others

(1) Electrical components which may not affect electrical performance are subjective to change without notice because of their availability.

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11.Packing

Type1 : TFT module 12pcs/pallet

TFT-Module

anti static sheet

desiccant

cushion

cushion

cardboard

cushion

TFT module 6pcs/1box

TFT module 6pcs/1box

band

Size:(L)1200×(W)1010×(H)700

H

W

L

2boxs/1pellet

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Type2 : TFT module 16pcs/pallet

Upper cover
(corrugated paper)

TFT module

Turns on the back
of the TFT module.

Anti-static
electricity
bag

Upper buffer material
(PS Foam)

TFT module
(front side)

TFT module
(back side)

Parting strip
(corrugated paper)

Parting strip
(corrugated paper)

TFT module

Under buffer material
(PS Foam)

State of packing (top side view)

PE bag

Frame
(corrugated paper)

Under cover
(corrugated paper)

Plastic palett

Band

H

(L)1220mm×(W)1000mm×(H)720mm

W

L

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Type1   Type2

Production ship

Address

IPS Alpha Technology,Ltd

F0000032R01   A1

AX094F002A   6 pcs

07-03-06-1   REV.A   R01

6FA160630   6FA160630

6FA160630   F0000032R01   00002   A

Label Size(102×70)

Production ship (ex.)

AX094F002A

10005603   5ヶ口

10005603
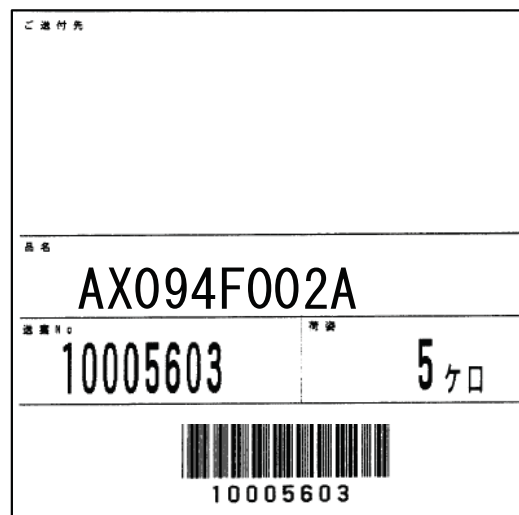
Label Size(100×100)   Label Size(100×100)

Address label(ex.)

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Packing Reliability

Judgement : No problem after testings under below conditions.

	Test Item	Condition	
1	Vibration test with carton	Waveform :	Sine wave
		Vibration level :	11.76 m/s2 (1.2G)
		Bandwidth :	7-100Hz (1 cycle = 20 minutes)
		Time :	160 minutes (8 cycle)
		Direction :	Y direction
2	Slant drop test with caton	Angel :	10°
		Slide distance :	1 m
		Direction :	X , Z (Three times each direction)
3	Static load strength of a carton (Wc)	$Wc \geqq [Wp \times N \div n] \times 3 = 684$ (kgw/carton)	
		where ;	
		Wp : Weight of a packed palette (152 kg/palette)	
		N : Layers Limit (3 pallets)	
		n : Number of cartons on a palette (2 cartons)	
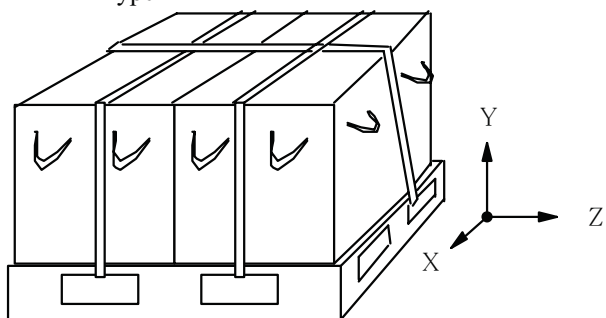
Type1   Type2
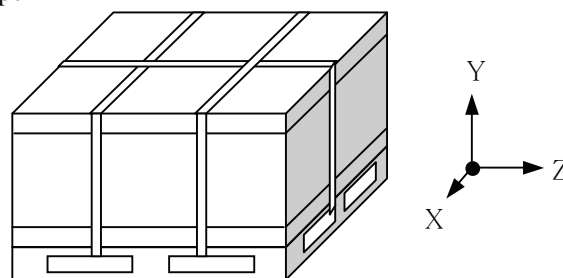
Definition of direction

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