



## AX2036\_48PIN 封装图

### 1. 引脚定义

#### 1.1 引脚分配

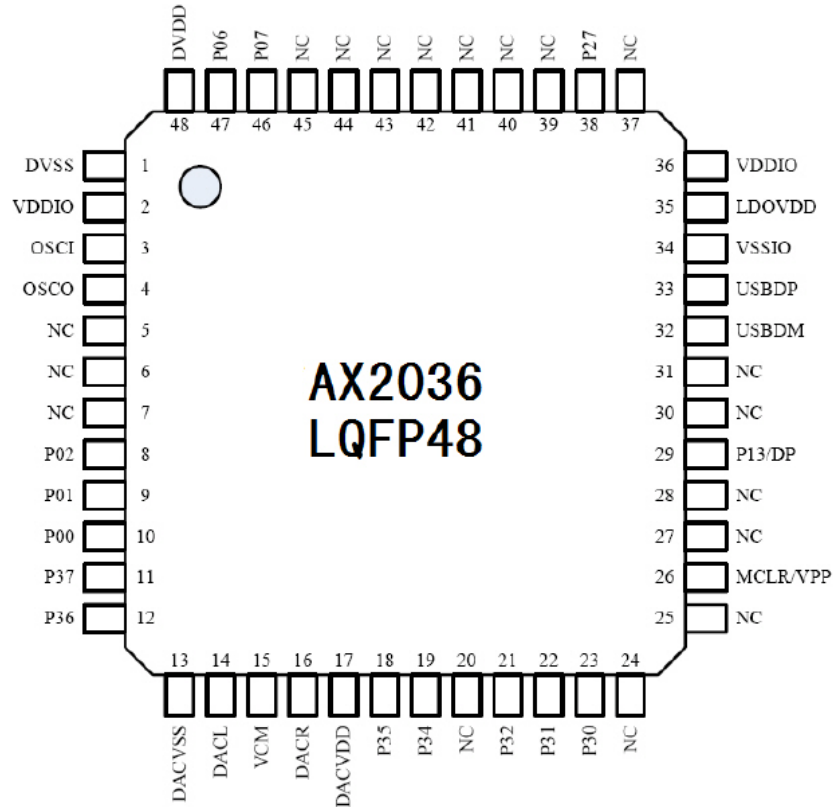


图 1 AX2036\_48PIN 引脚分配

#### 1.2 引脚描述

PIN #	Name	I/O Type	Drive (mA)	Function	Other Function
1	DVSS	P	/	Core Ground	
2	VDDIO	P	/	IO Power 3.3V	
3	OSCI	I	/	Oscillator Input	
4	OSCO	I/O	/	Oscillator Output	ISP/ISD DO
5	NC				
6	NC				
7	NC				
8	P02	I/O	8	GPIO ADC Output	T0CKIN: Timer0 Clock In SPI0DOA: SPI0 Data Out(A)
9	P01	I/O	8	GPIO ADC Output	T2CAP: Timer2 Capture Pin SPI0CLKA: SPI0 Clock(A)
10	P00	I/O	8	GPIO ADC Output	LVD: Low voltage detect input
11	P37	I/O	8	GPIO AUXL0	UARTRX1: UART Data In(B) T3PWM: Timer3 PWM Output
12	P36	I/O	8	GPIO AUXL1	SDDATA3: SD Data 3



13	DACVSS	P	/	DAC Ground	
14	DACL	O	/	DAC Left Channel	
15	VCM	O	/	DAC Bandgap voltage reference	
16	DACR	O	/	DAC Right Channel	
17	DACVDD	P	/	DAC Power 3.3V	
18	P35	I/O	8	GPIO AUXR0	UARTTX1: UART Data Out(B)
19	P34	I/O	8	GPIO AUXR1	SDDATA2: SD Data 2
20	NC		/		
21	P32	I/O	8	GPIO	SDDATA0: ISD Data 0
22	P31	I/O	8	GPIO	SDCMD: SD Command
23	P30	I/O	8	GPIO	SDCLK: SD Clock
24	NC				
25	NC				
26	MCLR/VPP	I	/	Master Clear, Low Active	OTP Programming Power
27	NC				
28	NC				
29	P13/DP	I/O	8	GPIO	DP: Debug Pin
30	NC				
31	NC				
32	USBDM	I/O	8	USB Negative Data	
33	USBDP	I/O	8	USB Positive Data	
34	VSSIO	P	/	IO Ground	
35	LDOVDD	P	/	LDO Power	
36	VDDIO	P	/	IO Power 3.3V	
37	NC				
38	P27	I/O	8	GPIO	PPD7: Parallel Port Data 7
39	NC				
40	NC				
41	NC				
42	NC				
43	NC				
44	NC				
45	NC				
46	P07	I/O	8	GPIO Port Interrupt/Wakeup	T1CAP: Timer1 Capture Pin
47	P06	I/O	8	GPIO Port Interrupt/Wakeup	T0CAP: Timer0 Capture Pin UART0RX: UART0 Data In
48	DVDD	P	/	Core Power 1.8V	

说明：1、P----Power Supply    2、I----Input    3、O----Output    4、I/O----Bi-direction



1.3 引脚封装

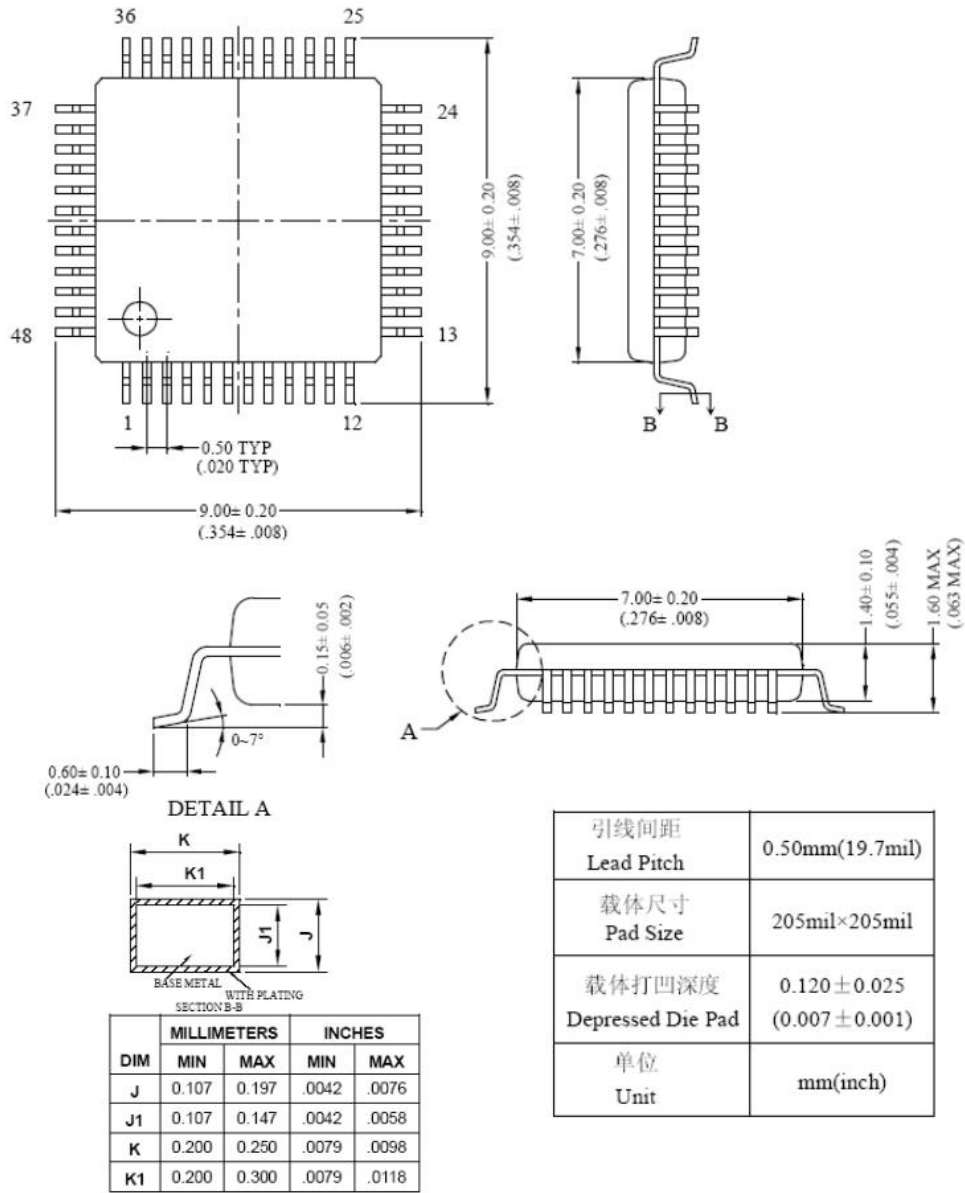


图 2 AX2036 LQFP48-7\*7mm