

1.2MHz, 2A Synchronous Step-Down Converter

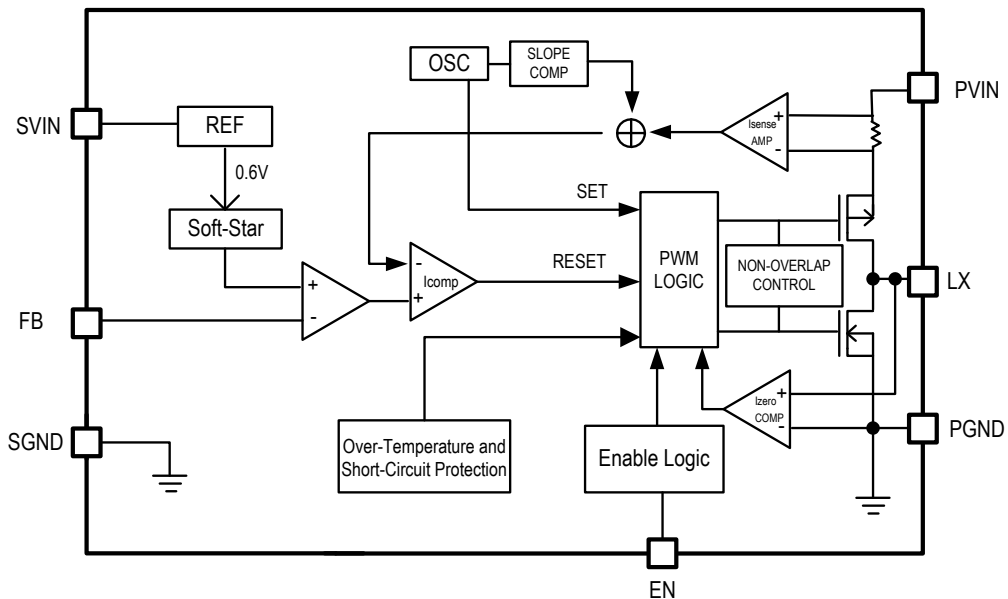
❖ GENERAL DESCRIPTION

The AX3514/A/B is a 1.2MHz constant frequency current mode PWM step-down converter. It is ideal for portable equipment requiring very high current up to 2A from single-cell Lithium-ion batteries while still achieving over 90% efficiency during peak load conditions. The AX3514/A/B also can run at 100% duty cycle for low dropout operation, extending battery life in portable systems while light load operation provides very low output ripple for noise sensitive applications. The AX3514/A/B can supply up to 2A output load current from a 2.6V to 5.5V input voltage and the output voltage can be regulated as low as 0.6V. The high switching frequency minimizes the size of external components while keeping switching losses low. The internal slope compensation setting allows the device to operate with smaller inductor values to optimize size and provide efficient operation. The AX3514/A/B is available in adjustable (0.6V to VIN) output voltage. The device is available in SOP-8L Pb-free and TDFN-10L packages.

❖ FEATURES

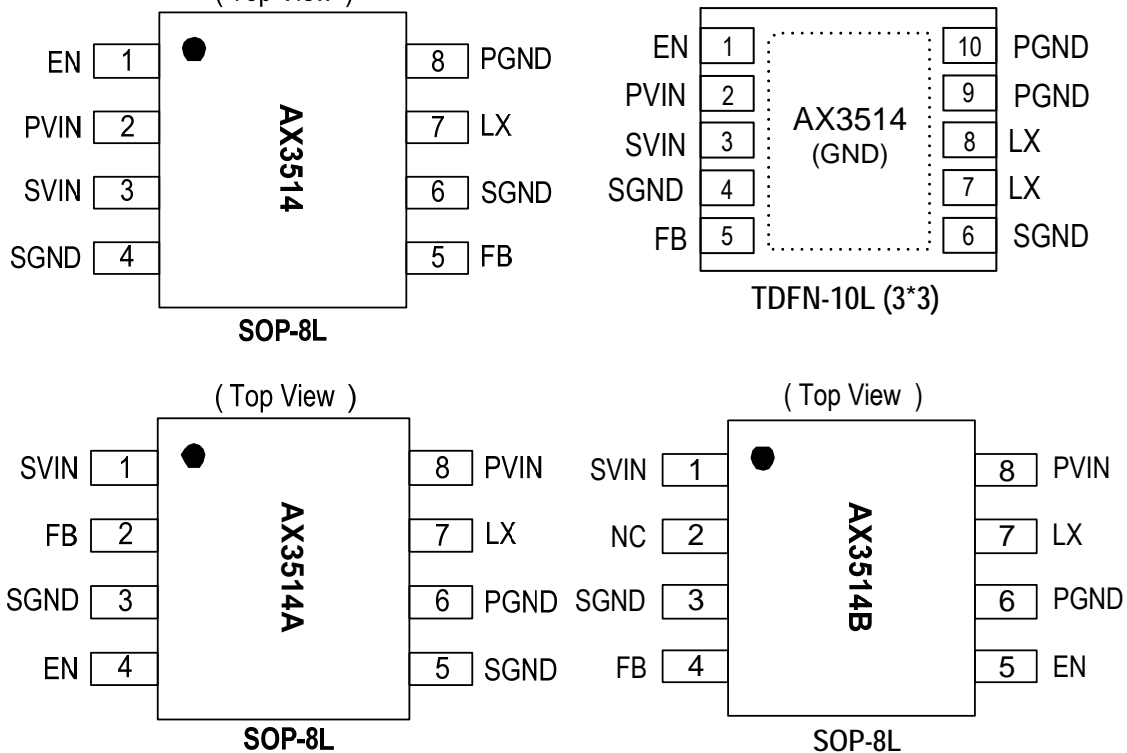
- 2.6V to 5.5V Input Voltage Range
- Output Voltages from 0.6V to VIN
- High Efficiency: Up to 96%
- 1.2MHz Constant Frequency Operation
- Up to 2A Output Current
- No Schottky Diode Required
- Low $R_{DS(ON)}$ Internal Switches: 0.15Ω
- Current Mode Operation for Excellent Line and Load Transient Response
- Current limit, Enable function
- Short Circuit Protect (SCP)
- Build-in Soft Start function
- $\leq 1\mu A$ Shutdown Current
- SOP-8L Pb-Free and TDFN-10L packages

❖ BLOCK DIAGRAM



❖ PIN ASSIGNMENT

The packages of AX3514/A/B are SOP-8L and TDFN-10L; the pin assignment is given by:
(Top View)



Name	Description
EN	Enable pin H : normal operation L : Shutdown
PVIN	Power Supply Input Pin
SVIN	Signal Supply Input Pin
LX	Switch output pin. Connect external inductor here. Minimize trace area at this pin to reduce EMI.
SGND	Signal Ground Pin
PGND	Power Ground Pin
FB	Output Feedback pin
NC	No connect pin

❖ ORDER/MARKING INFORMATION

Order Information	
AX3514 X XXX X	
<p>Pin Define</p> <p>Blank: AX3514</p> <p>A: AX3514 (SOP-8L Only)</p> <p>B: AX3514 (SOP-8L Only)</p>	<p>Package Type</p> <p>S: SOP-8L</p> <p>J10: TDFN-10L (3*3)</p>
<p>Packing</p> <p>Blank : Taping</p> <p>A : Taping</p>	
Top Marking (SOP-8L)	Top Marking (TDFN-10L)
<p>Logo ← AX 3 5 1 4 → Part number</p> <p>X Y Y W W X → ID code: internal</p> <p>WW: 01~52</p> <p>Year: 10=2010</p> <p>11=2011</p> <p>Blank:AX3514</p> <p>A : AX3514A (SOP8 Only)</p> <p>B : AX3514B (SOP8 Only)</p>	<p>3 5 1 4 → Part number</p> <p>Y Y W W X → ID code: internal</p> <p>WW: 01~52</p> <p>Year: 10=2010</p> <p>11=2011</p>

❖ ABSOLUTE MAXIMUM RATINGS (at T_A=25°C)

Characteristics	Symbol	Rating	Unit
PVIN, SVIN Pin Voltage	V _{IN}	V _{SS} - 0.3 to V _{SS} + 6	V
Feedback Pin Voltage	V _{FB}	V _{SS} - 0.3 to V _{IN} + 0.3	V
EN Pin Voltage	V _{EN}	V _{SS} - 0.3 to V _{IN} + 0.3	V
Switch Pin Voltage	V _{LX}	V _{SS} - 0.3 to V _{IN} + 0.3	V
Power Dissipation	PD	(T _J -T _A) / θ _{JA}	mW
Storage Temperature Range	T _{ST}	-40 to +150	°C
Operating Temperature Range	T _{OP}	-40 to +85	°C
Junction Temperature	T _J	+125	°C
Thermal Resistance from Junction to case	SOP-8L	40	°C/W
	TDFN-10L		
Thermal Resistance from Junction to ambient	SOP-8L	120	°C/W
	TDFN-10L		

Note: θ_{JA} is measured with the PCB copper area of approximately 1 in²(Multi-layer). That need connect to LX pin or Exposed pad of the AX3514.

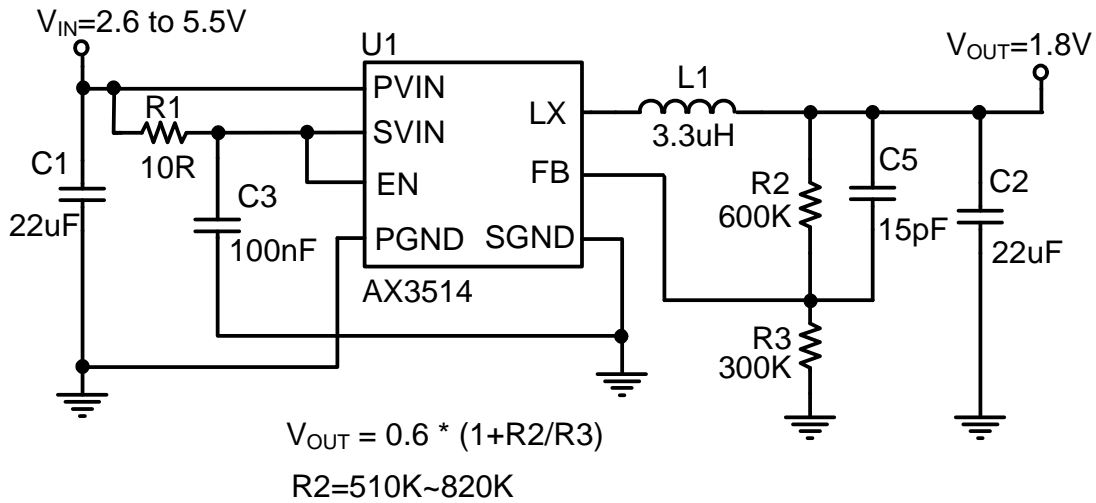
❖ ELECTRICAL CHARACTERISTICS

(V_{IN} = V_{EN}=3.6V, T_A =25°C, unless otherwise specified)

Characteristics	Symbol	Conditions	Min	Typ	Max	Units
Supply Voltage Range	V _{IN}		2.6	-	5.5	V
Feedback Voltage	V _{FB}	T _A = +25°C	0.5880	0.6000	0.6120	V
		T _A = 0°C ≤ T _A ≤ 85°C	0.5865	0.6000	0.6135	
		T _A = -40°C ≤ T _A ≤ 85°C	0.5820	0.6000	0.6180	
Feedback Bias Current	I _{FB}	V _{FB} =0.65V	-	-	±30	nA
Quiescent Current	I _{CCQ}	V _{FB} =0.8V	-	250	400	uA
Shutdown Supply Current	I _{SD}	V _{EN} =0V	-	0.1	1	uA
Switching Current Limit	I _{LIMIT}		2.2	2.5	-	A
Line Regulation	ΔV _{OUT} /V _{OUT}	V _{IN} = 2.6V~5.5V	-	0.04	0.4	%/V
Load Regulation	ΔV _{OUT} /V _{OUT}	I _{OUT} = 0.01 to 2A	-	0.5	1	%
Oscillation Frequency	F _{OSC}	LX pin	1	1.2	1.4	MHz
R _{DS(ON)} of P-CH MOSFET	R _{DS(ON)}	V _{FB} =0V, I _{OUT} =1A	-	0.15	0.25	Ω
R _{DS(ON)} of N-CH MOSFET	R _{DS(ON)}	(Note1)	-	0.11	0.20	Ω
EN pin logic input threshold voltage	V _{ENL}		-	-	0.4	V
	V _{ENH}		1.5	-	-	
EN Pin Input Current	I _{EN}		-	±0.1	±1	uA
Efficiency	EFFI	V _{IN} =5V, V _{OUT} =3.3V, I _{OUT} =1.5A	-	91	-	%
Thermal Shutdown	T _{SD}		-	140	-	°C
Thermal Shutdown Hysteresis	T _{SH}		-	30	-	°C

Note1: Guaranteed by design.

❖ APPLICATION CIRCUIT



❖ FUNCTION DESCRIPTIONS

Operation

AX3514/A/B is a monolithic switching mode Step-Down DC-DC converter. It utilizes internal MOSFETs to achieve high efficiency and can generate very low output voltage by using internal reference at 0.6V. It operates at a fixed switching frequency, and uses the slope compensated current mode architecture. This Step-Down DC-DC Converter supplies 2000mA output current at input voltage range from 2.6V to 5.5V.

Current Mode PWM Control

Slope compensated current mode PWM control provides stable switching and cycle-by-cycle current limit for excellent load and line responses and protection of the internal main switch (P-CH MOSFET) and synchronous rectifier (N-CH MOSFET). During normal operation, the internal P-CH MOSFET is turned on for a certain time to ramp the inductor current at each rising edge of the internal oscillator, and switched off when the peak inductor current is above the error voltage. The current comparator, I_{COMP} limits the peak inductor current. When the main switch is off, the synchronous rectifier will be turned on immediately and stay on until either the inductor current starts to reverse, as indicated by the current reversal comparator, I_{ZERO} , or the beginning of the next clock cycle.

❖ APPLICATION INFORMATION

Setting the Output Voltage

Application circuit item shows the basic application circuit with AX3514/A/B adjustable output version. The external resistor sets the output voltage according to the following equation:

$$V_{OUT} = 0.6V \times \left(1 + \frac{R2}{R3}\right)$$

Table 1: Resistor select for output voltage setting

V _{OUT}	R3	R2
1.2V	680K	680K
1.5V	420K	630K
1.8V	300K	600K
2.5V	180K	560K
3.3V	150K	680K

Inductor Selection

For most designs, the AX3514/A/B operates with inductors of 2.2μH to 4.7μH. Low inductance values are physically smaller but require faster switching, which results in some efficiency loss. The inductor value can be derived from the following equation:

$$L = \frac{V_{OUT} \times (V_{IN} - V_{OUT})}{V_{IN} \times \Delta I_L \times f_{OSC}}$$

Where is inductor Ripple Current. Large value inductors lower ripple current and small value inductors result in high ripple currents. Choose inductor ripple current approximately 15% of the maximum load current 2000mA, ΔI_L=300mA.

Table 2 Inductor select for output voltage setting (V_{IN}=3.6V)

V _{OUT}	1.2V	1.5V	1.8V	2.5V
Inductor	3.3uH	3.3uH	3.3uH	2.2uH
Part Number WE-TPC	744062003	744062003	744062003	744043022

Note: Part type L (www.we-online.com)

For output voltages above 2.0V, when light-load efficiency is important, the minimum recommended inductor is 2.2μH. For optimum voltage-positioning load transients, choose an inductor with DC series resistance in the 30mΩ to 100mΩ range. For higher efficiency at heavy loads (above 200mA), or minimal load regulation (but some transient overshoot), the resistance should be kept below 100mΩ. The DC current rating of the inductor should be at least equal to the maximum load current plus half the ripple current to prevent core saturation (2000mA+350mA).

Input Capacitor Selection

The input capacitor reduces the surge current drawn from the input and switching noise from the device. The input capacitor impedance at the switching frequency shall be less than input source impedance to prevent high frequency switching current passing to the input. A low ESR input capacitor sized for maximum RMS current must be used. Ceramic capacitors with X5R or X7R dielectrics are highly recommended because of their low ESR and small temperature coefficients. A 22 μ F ceramic capacitor for most applications is sufficient.

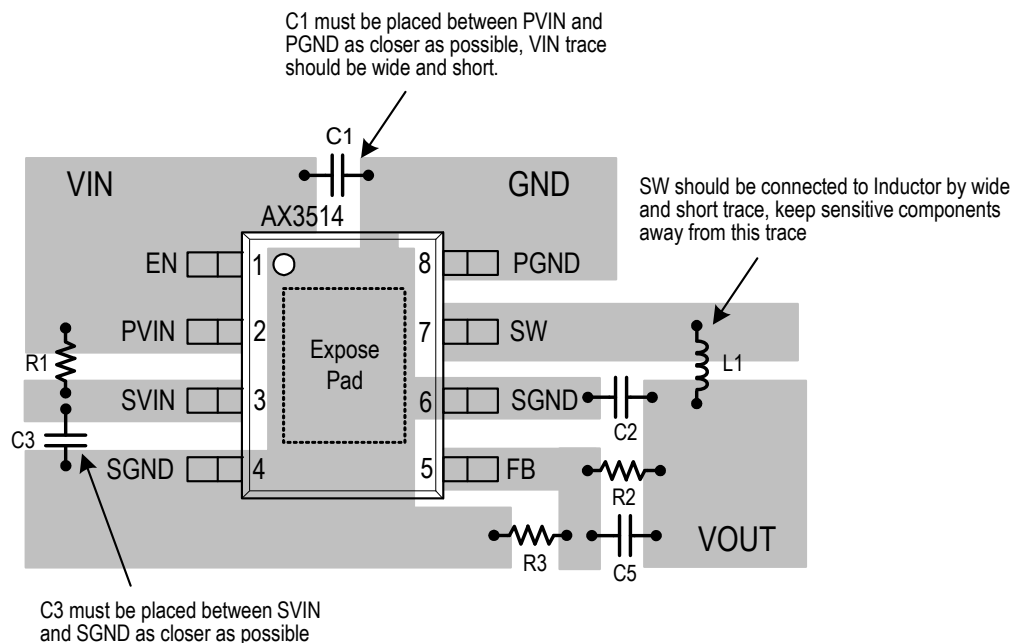
Output Capacitor Selection

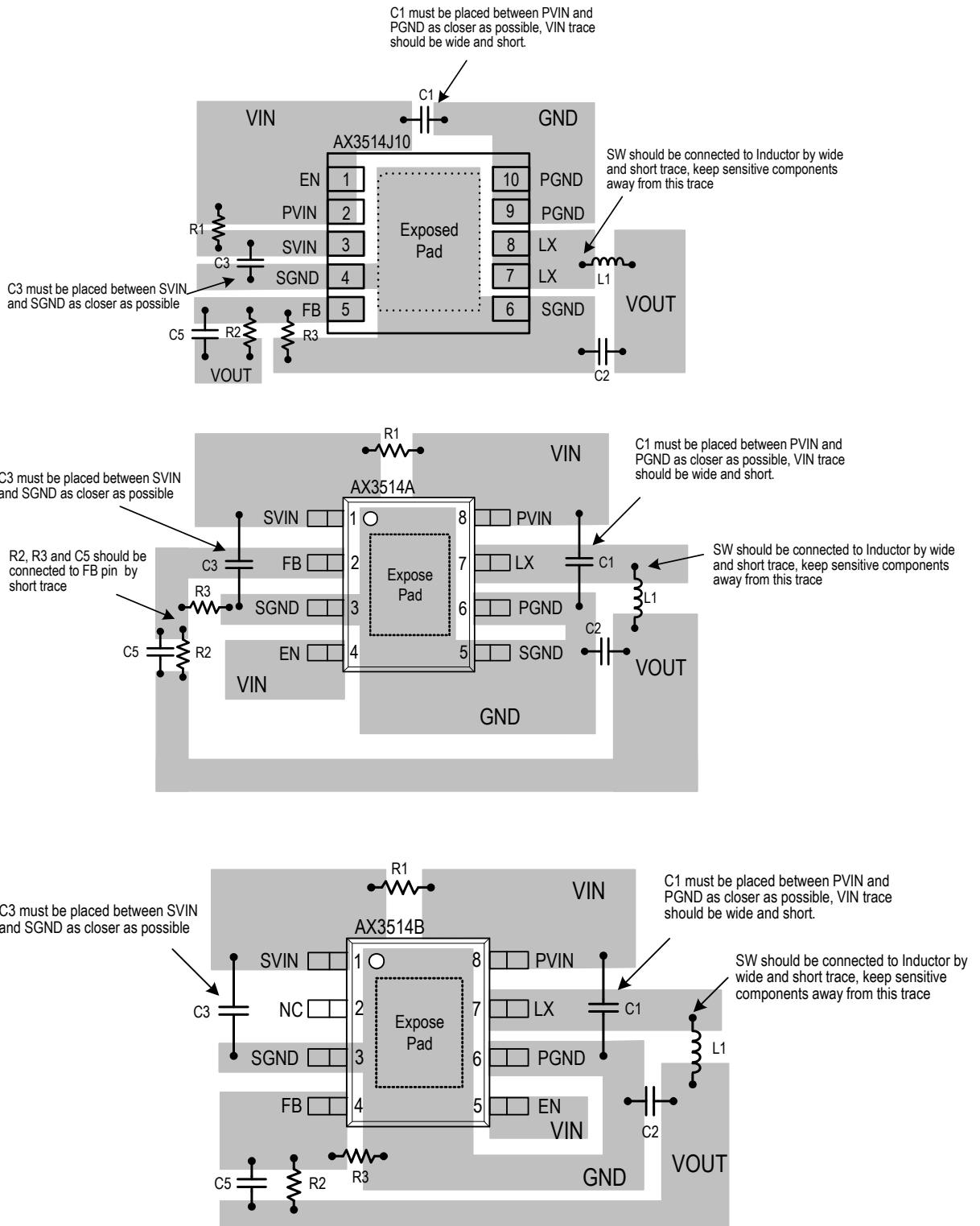
The output capacitor is required to keep the output voltage ripple small and to ensure regulation loop stability. The output capacitor must have low impedance at the switching frequency. Ceramic capacitors with X5R or X7R dielectrics are recommended due to their low ESR and high ripple current.

Compensation Capacitor Selection

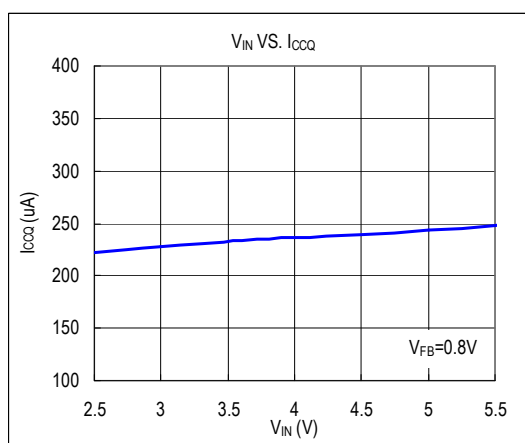
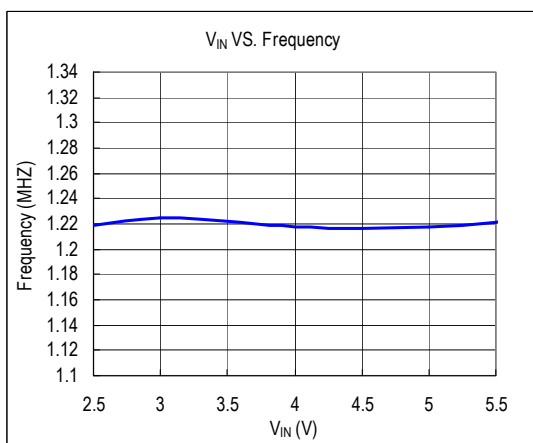
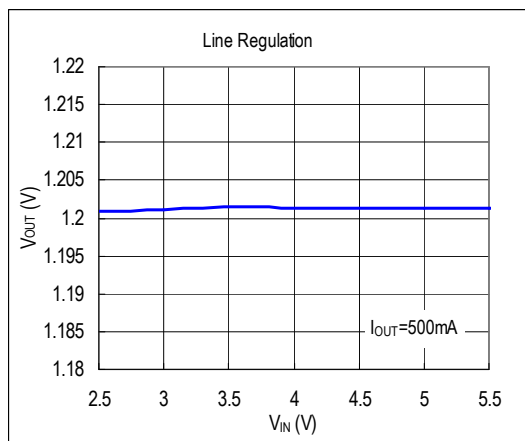
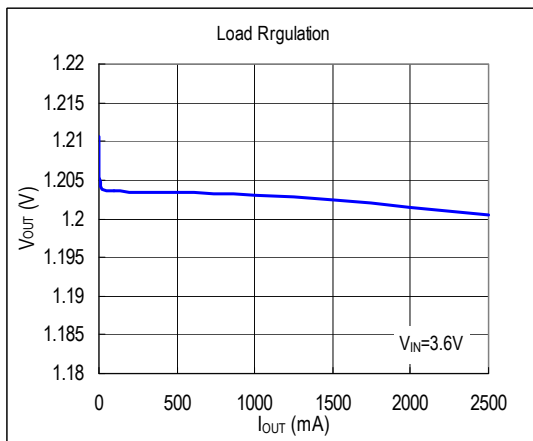
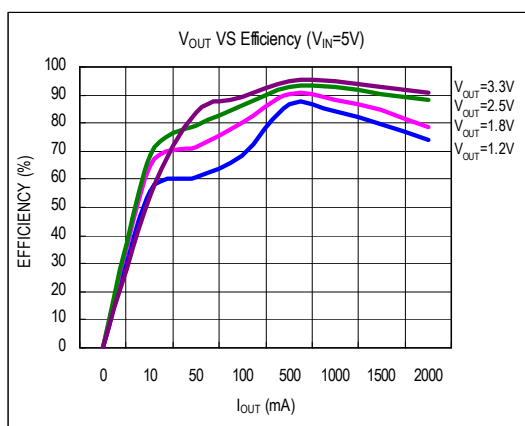
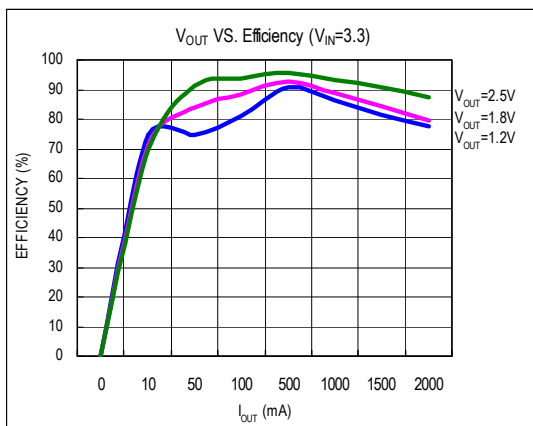
The compensation capacitor (C5) for improving phase margin provides additional stability. Refer to Demo Board Schematic. The optimum value is 15pF for all conditions.

Layout Guide

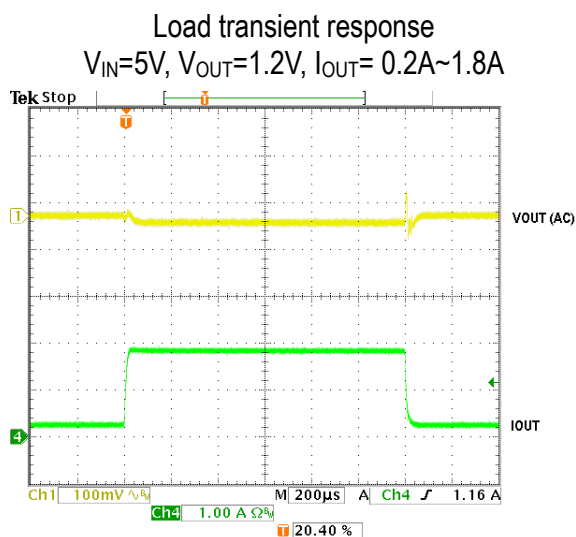
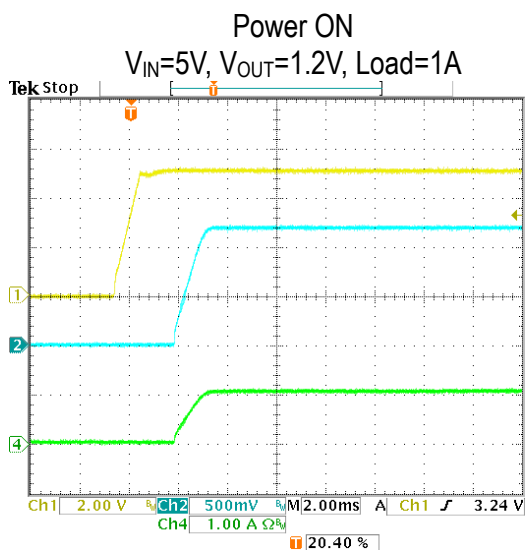
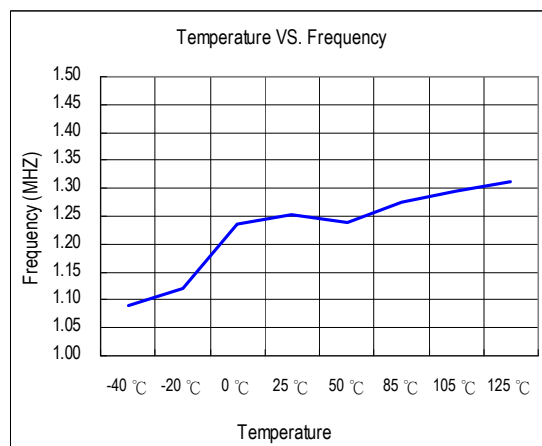
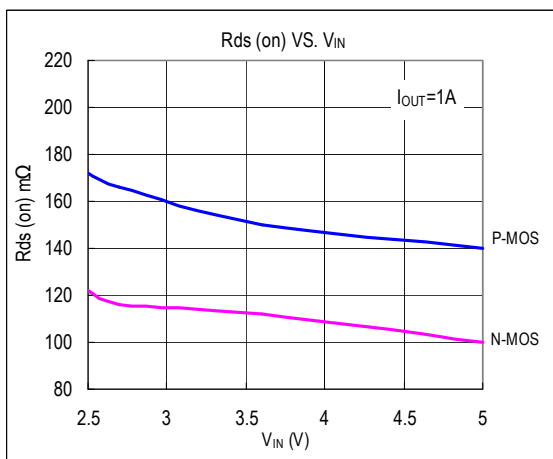
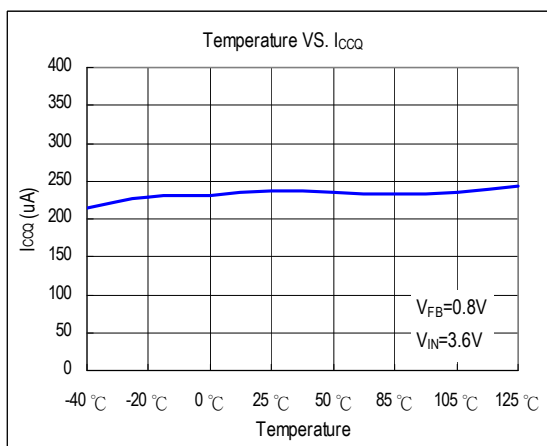
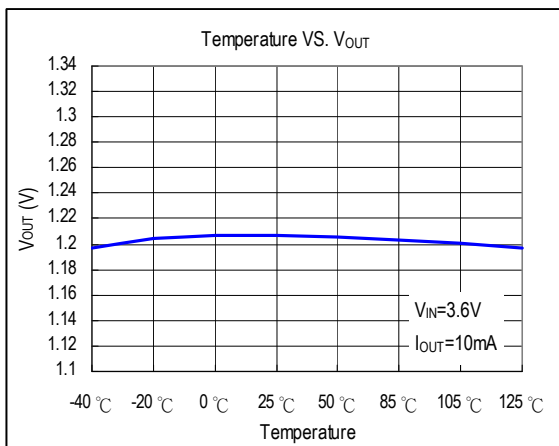




❖ TYPICAL CHARACTERISTICS

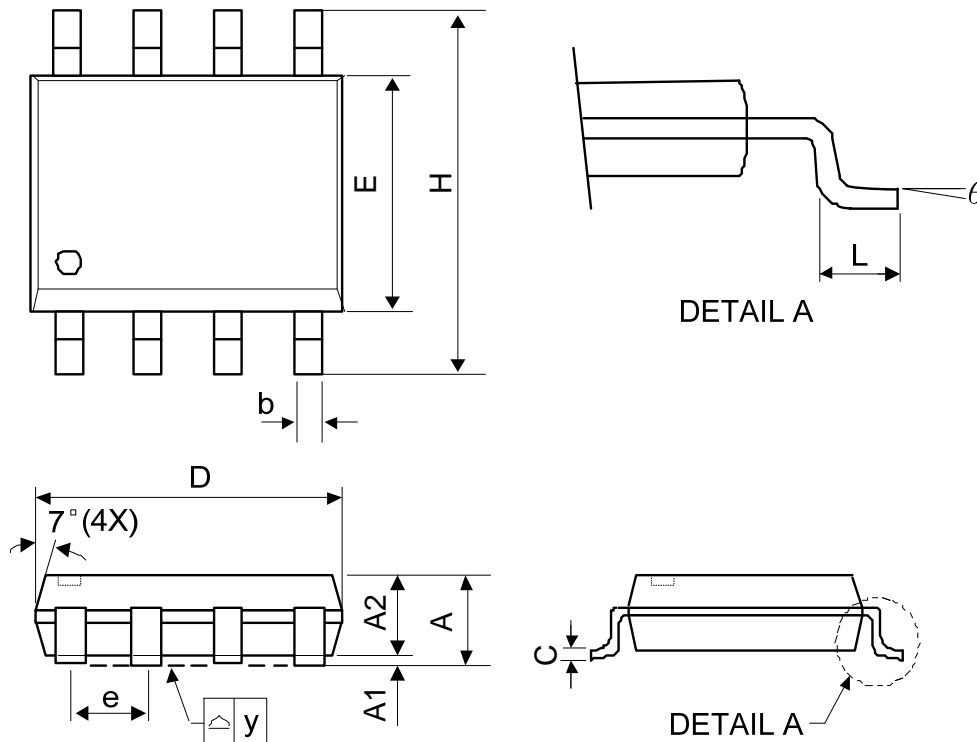


❖ TYPICAL CHARACTERISTICS (CONTINUOUS)



❖ PACKAGE OUTLINES

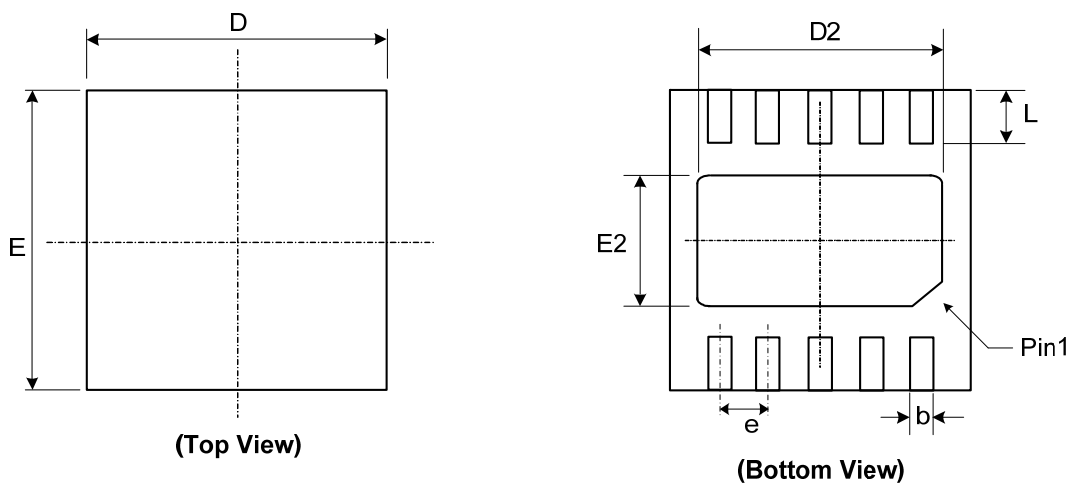
(1) SOP-8L



Symbol	Dimensions in Millimeters			Dimensions in Inches		
	Min.	Nom.	Max.	Min.	Nom.	Max.
A	-	-	1.75	-	-	0.069
A1	0	-	0.15	0	-	0.06
A2	1.25	-	-	0.049	-	-
C	0.1	0.2	0.25	0.0075	0.008	0.01
D	4.7	4.9	5.1	0.185	0.193	0.2
E	3.7	3.9	4.1	0.146	0.154	0.161
H	5.8	6	6.2	0.228	0.236	0.244
L	0.4	-	1.27	0.015	-	0.05
b	0.31	0.41	0.51	0.012	0.016	0.02
e	1.27 BSC			0.050 BSC		
y	-	-	0.1	-	-	0.004
X	-	2.34	-	-	0.092	-
Y	-	2.34	-	-	0.092	-
θ	0°	-	8°	0°	-	8°

Mold flash shall not exceed 0.25mm per side
JEDEC outline: MS-012 BA

(2) TDFN-10L (3*3*0.75mm)



Symbol	Dimensions in Millimeters			Dimensions in Inches		
	Min.	Nom.	Max.	Min.	Nom.	Max.
A	0.70	0.75	0.80	0.028	0.030	0.031
A1	0.00	0.02	0.05	0.000	0.001	0.002
A3	0.20 REF.			0.008 REF.		
b	0.18	0.25	0.30	0.007	0.010	0.012
D	2.90	3.00	3.10	0.114	0.118	0.122
D2	2.20	2.40	2.50	0.087	0.094	0.098
E	2.90	3.00	3.10	0.114	0.118	0.122
E2	1.50	1.60	1.70	0.059	0.063	0.070
e	0.50 BSC.			0.020 BSC.		
L	0.30	0.40	0.50	0.012	0.016	0.020