

LCD-Module SPECIFICATION

Approved by Customer

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AXSG12864D

APPROVED BY	CHECKED BY	ORGANIZED BY
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RECORD OF REVISION

Revision Date	Page	Contents
2004/03/03	-	New Release

1 FEATURES

- (1) Display format : 128 × 64 dot-matrix, 1/64 duty.
- (2) Construction : LCD, Bezel, Zebra , and PCB.
- (3) Optional Edge LED or EL backlight.
- (4) Controller: T6963C.
- (5) 5V single power input. Built-in DC/DC converter for LCD driving.
- (6) Normal / Extended temperature type.

2 NUMBERING SYSTEM

AX__ -12864D ____ - __
 1 2 3 4 5 6 7

No	Code Value	Description	Remark
2	T	TN	LCD type
	H	HTN	
	S	STN yellow-green	
	G	STN Gray	
	B	STN Blue(Negative)	
	F	FSTN	
	C	STN-color	
	A	A-TFT	
	L	Ltps-TFT	
	O	OLED	
3	C	Character-COB	Display type
	D	Character-TAB	
	E	Character-COG	
	F	Character-COF	
	G	Graphic-COB	
	H	Graphic-TAB	
	K	Graphic-COG	
	L	Graphic-COF	
3	A	Reflective type / 6:00 view	Polarizer / Viewing Angle
	B	Reflective type / 12:00 view	
	C	Transflective type / 6:00 view	
	D	Transflective type / 12:00 view	
	E	Transmissive type / 6:00 view	

	F	Transmissive type / 12:00 view	
	G	Negative type / 6:00 view	
	H	Negative type / 12:00 view	
	I	Negative type / 3:00 view	
4	N	None backlight	Backlight type
	L	LED array	
	Q	LED edge	
	E	EL	
	C	CCFL	
5	None	Without backlight	Backlight color
	A	Amber	
	B	Blue	
	G	Green	
	L	Yellow	
	O	Orange	
	R	Red	
	Y	Yellow-green	
	W	White	
6	N	Normal temperature type	LCM temperature type
	H	Extended temperature type	
7	00~99		Version code

3 MECHANICAL DATA

Parameter	Stand Value	Unit
Dot size	0.40(W) × 0.56(H)	mm
Dot pitch	0.44(W) × 0.60(H)	mm
Viewing area	62.0(W) × 44.0(H)	mm
Module size	78.0(W) × 70.0(H) × 10.5 max (T)	mm
Module size (w/ LED back-light)	78.0(W) × 70.0(H) × 13.0 max (T)	mm

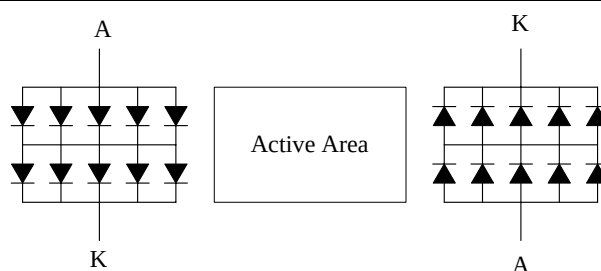
4 ABSOLUTE MAXIMUM RATINGS

Parameter		Symbol	Min	Max	Unit
Logic Circuit Supply Voltage		VDD-VSS	-0.3	7.0	V
LCD Driving Voltage		VDD-VO	-0.3	16.0	V
Input Voltage		VI	-0.3	VDD+0.3	V
Normal temp. type	Operating Temp.	TOP	0	50	°C
	Storage Temp.	TSTG	-20	70	°C
Extended temp. type	Operating Temp.	TOP	-20	70	
	Storage Temp.	TSTG	-30	80	

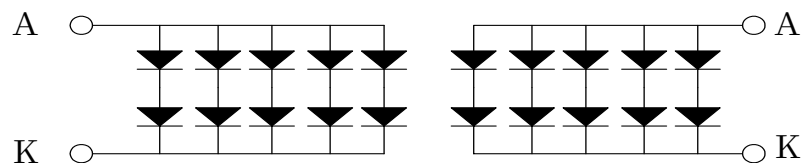
5 ELECTRO-OPTICAL CHARACTERISTICS

Parameter	Symbol	Condition	Min	Typ	Max	Unit	Note
----- Electronic Characteristics -----							
Logic Circuit Supply Voltage	VDD-VSS	--	4.5	5.0	5.5	V	
LCD Driving Voltage	VDD-VO	-20 °C	8.4	9.2	10.0	V	0 ~ 50 °C for Normal Temp. type -20 ~ 70 °C for Extended Temp. type
		0 °C	8.4	9.2	10.0		
		25 °C	8.4	9.2	10.0		
		50 °C	8.4	9.2	10.0		
		70 °C	8.4	9.2	10.0		
Input Voltage	VIH	--	0.7 VDD	--	VDD	V	
	VIL	--	VSS	--	0.3 VDD	V	
Logic Supply Current	IDD	VDD = 5V	--	3	6	mA	
----- Optical Characteristics -----							
Contrast	CR	STN type	--	5	--		Note 1
		FSTN type		8			
Rise Time	tr	25°C	--	110	170	ms	Note 2
Fall Time	tf	25°C	--	110	170	ms	
Viewing Angle Range	θ f	25°C & CR≥2	--	40	--	Deg.	Note 3
	θ b		--	35	--		
	θ l		--	35	--		
	θ r		--	35	--		
Frame Frequency	fF	25°C	--	64	--	Hz	
----- Yellow-green LED Back-light Characteristics -----							
Forward Voltage	VF	--	--	4.05	4.3	V	Supply Voltage between A&K
Forward Current	IF	VF=4.05V	--	100	--	mA	
Bare LED Luminous intensity		VF=4.05V	--	20	--	cd/m ²	
LCM Luminous intensity		VF=4.05V	--	7	--	cd/m ²	

* LED Dice number = 2x10=20



----- White Edge LED Back-light Characteristics -----							
Forward Voltage	VF	--	7.2	7.6	8.0	V	Supply Voltage between A&K
Forward Current	IF	VF=7.6V	--	100	--	mA	
Bare LED Luminous intensity	VF=7.6V	--	30	--	cd/m ²		
LCM Luminous intensity	VF=7.6V	--	10	--	cd/m ²		

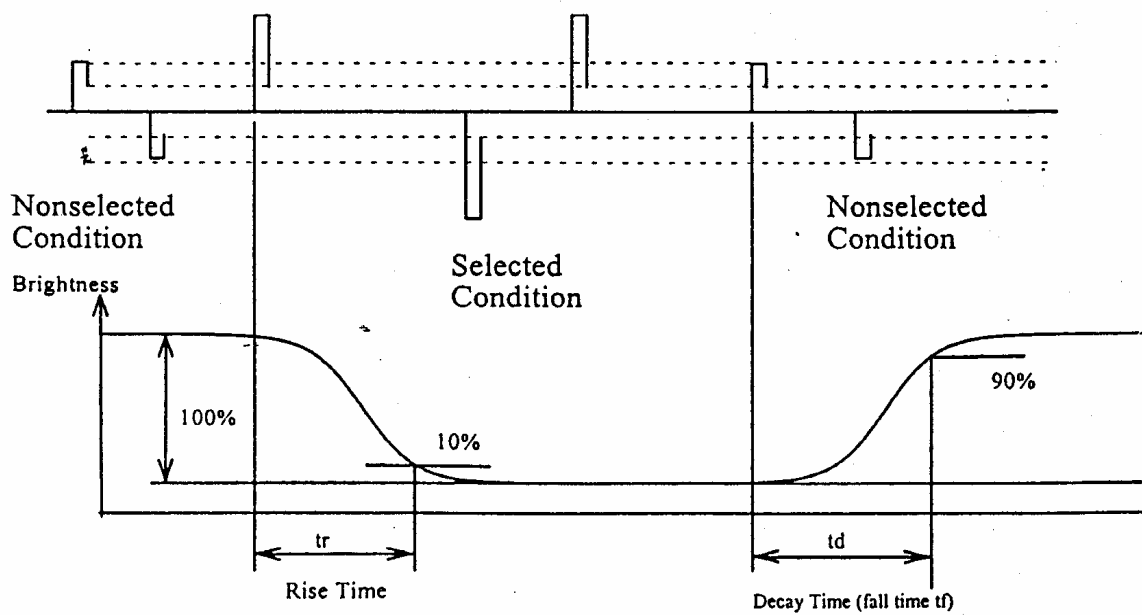


Parameter	Min	Typ	Max	Unit	Note
----- EL Back-light Characteristics -----					
Driving Voltage	--	110	--	Vrms	
Frequency	--	400	--	Hz	
Bare EL Luminous intensity	--	45	--	cd/m ²	
LCM Luminous intensity	--	15	--	cd/m ²	

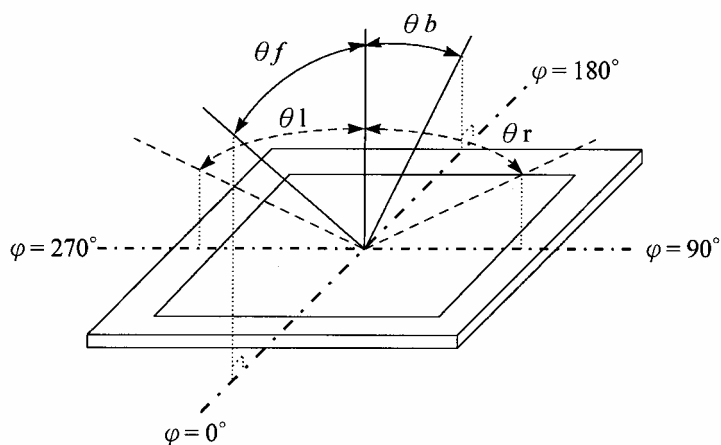
(NOTE 1) Contrast ratio :

CR = (Brightness in OFF state) / (Brightness in ON state)

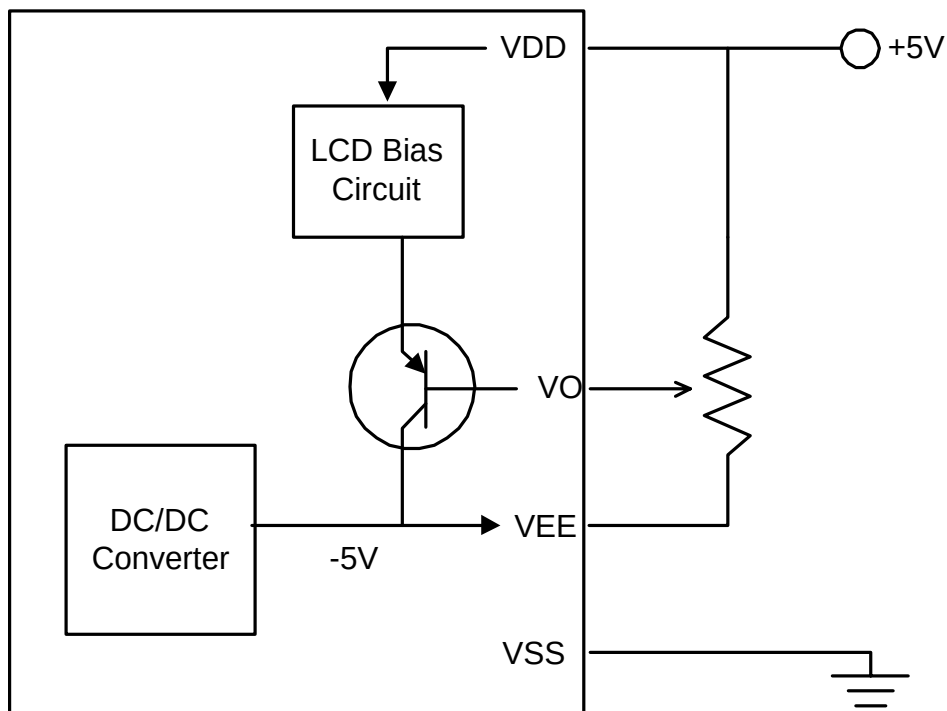
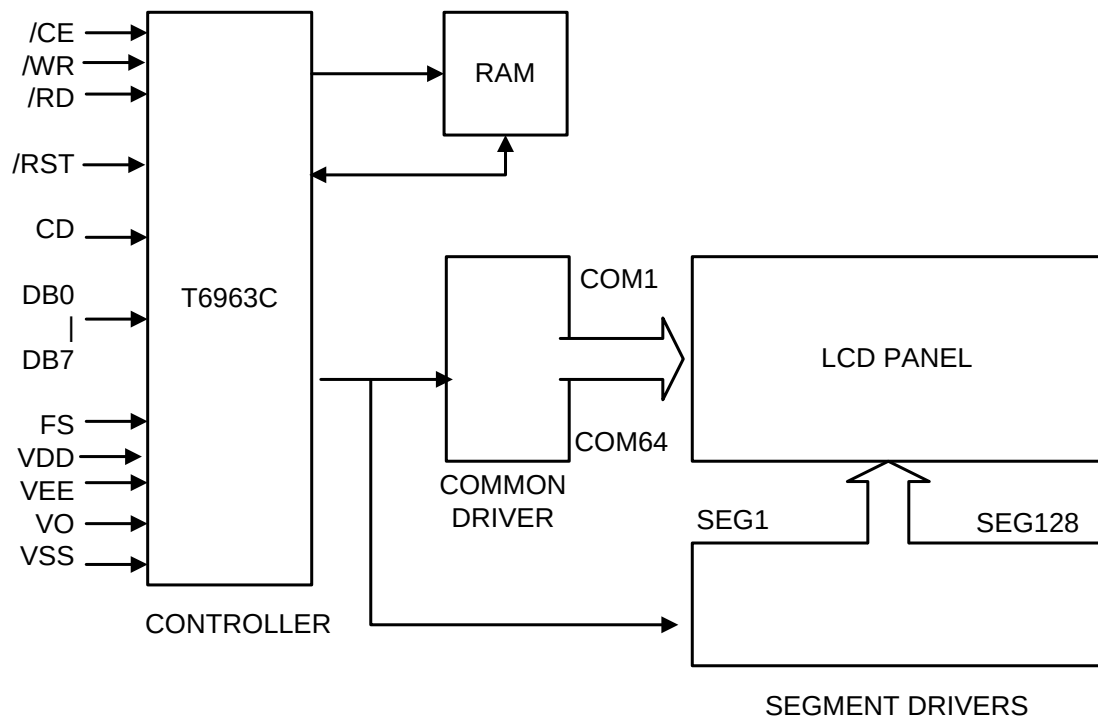
(NOTE 2) Response time :



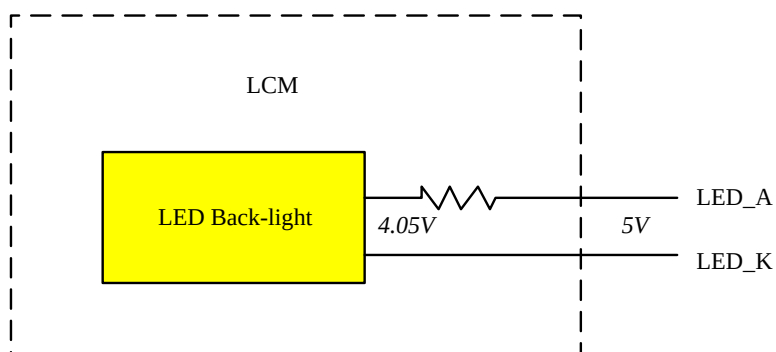
(NOTE 3) Viewing angle



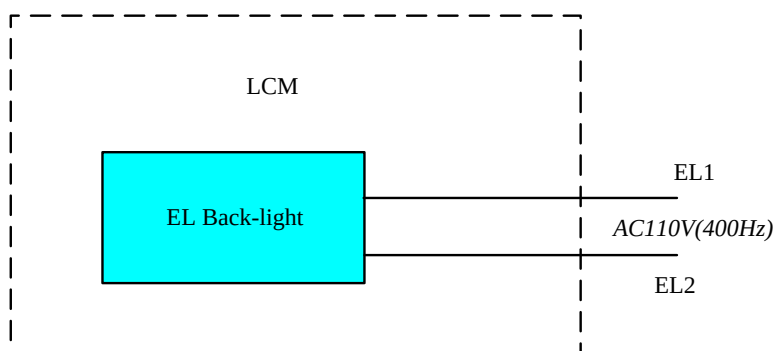
6 BLOCK DIAGRAM & POWER SUPPLY



Using LED Back-light



Using EL Back-light

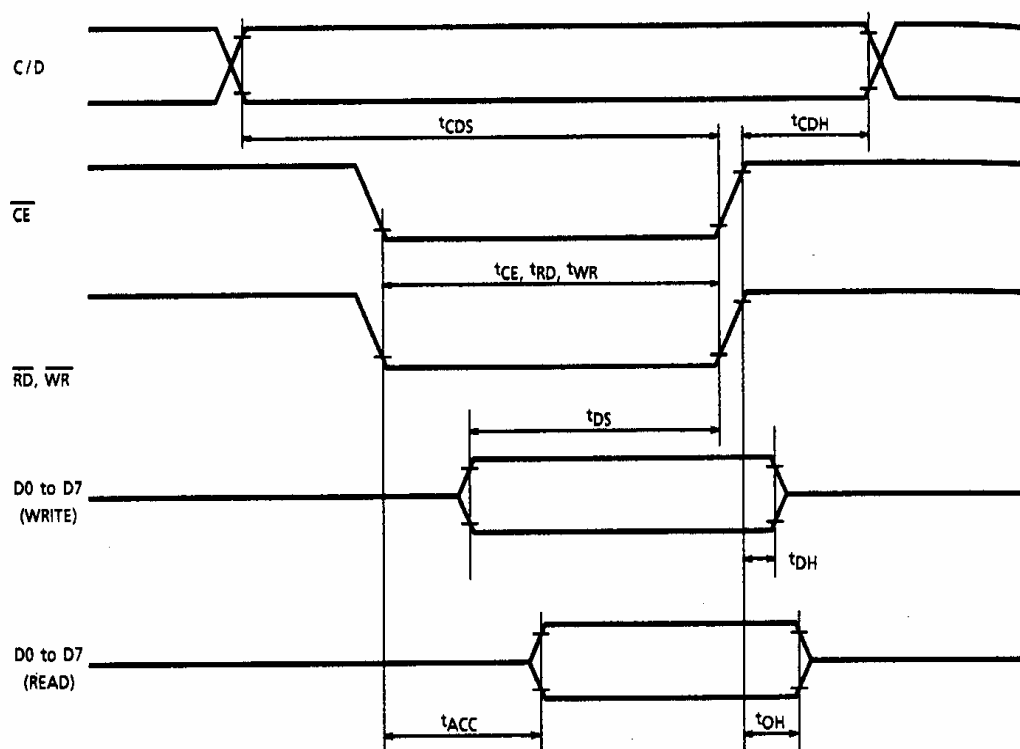


7 PIN CONNECTIONS

No.	Symbol	Function
1	VEE	Negative Power Supply Output (-5V)
2	VSS	Ground (0V)
3	VDD	Supply Voltage for Logic (+5V)
4	VO	Constant adjustment
5	/WR	Data Write
6	/RD	Data Read
7	/CE	Chip Enable
8	C/D	Code/Data
9	/RST	Reset
10-17	DB0-DB7	Data Bus Line
18	FS	Font Select H: 6x8 (default) L : 8x8
19	LEDA	LED Supply Voltage + (5V)
20	LEDK	LED Supply Voltage - (5V)

8 TIMING CHARACTERISTICS

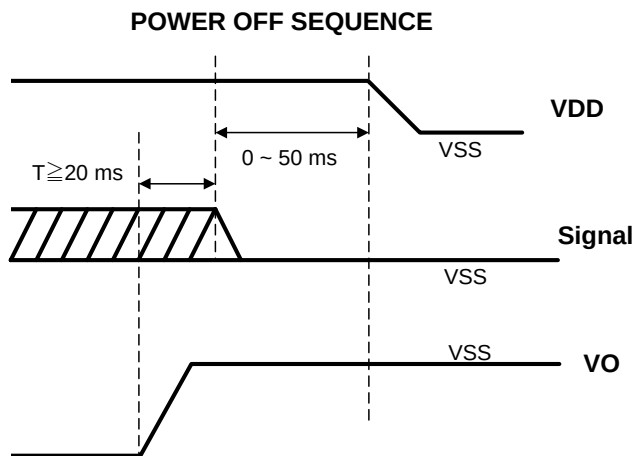
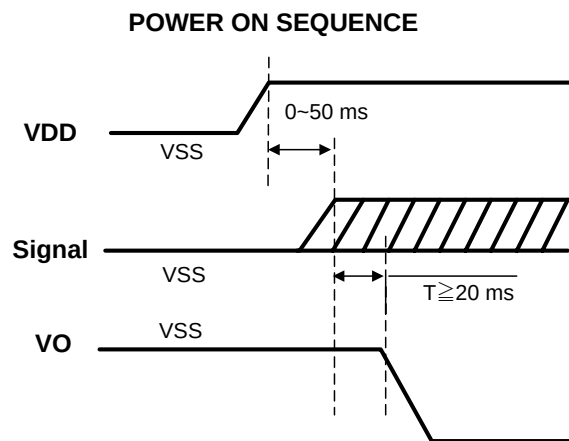
Bus Timing



TEST CONDITIONS (Unless otherwise noted, $V_{DD} = 5.0V \pm 10\%$, $V_{SS} = 0V$, $T_a = -20$ to $75^\circ C$)

ITEM	SYMBOL	TEST CONDITIONS	MIN	MAX	UNIT
C/D Set-up Time	t_{CDS}	—	100	—	ns
C/D Hold Time	t_{CDH}	—	10	—	ns
CE, RD, WR Pulse Width	t_{CE} , t_{RD} , t_{WR}	—	80	—	ns
Data Set-up Time	t_{DS}	—	80	—	ns
Data Hold Time	t_{DH}	—	40	—	ns
Access Time	t_{ACC}	—	—	150	ns
Output Hold Time	t_{OH}	—	10	50	ns

8.1 Power ON/OFF Sequence



9 QUALITY AND RELIABILITY

9.1 TEST CONDITIONS

Tests should be conducted under the following conditions :

Ambient temperature : $25 \pm 5^{\circ}\text{C}$

Humidity : $60 \pm 25\% \text{ RH}$.

9.2 SAMPLING PLAN

Sampling method shall be in accordance with MIL-STD-105E , level II, normal single sampling plan .

9.3 ACCEPTABLE QUALITY LEVEL

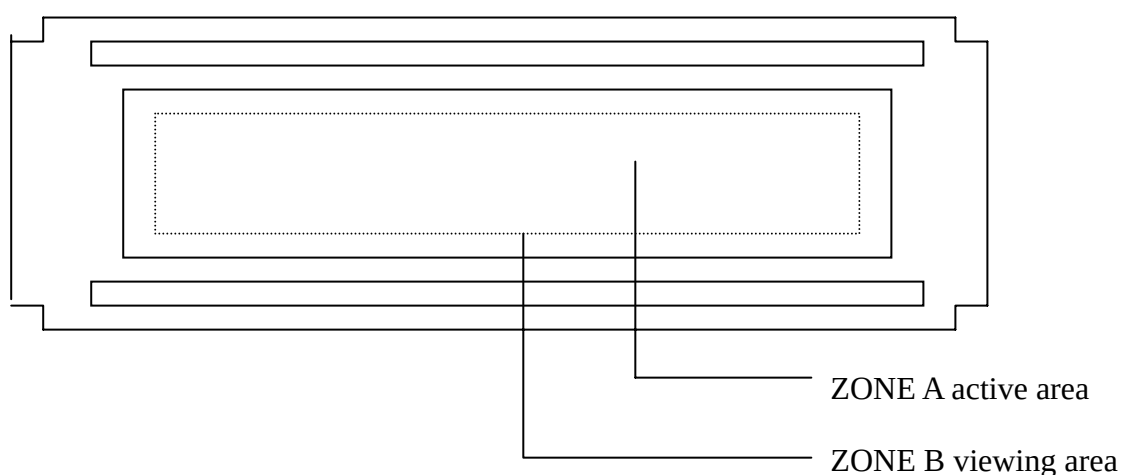
A major defect is defined as one that could cause failure to or materially reduce the usability of the unit for its intended purpose. A minor defect is one that does not materially reduce the usability of the unit for its intended purpose or is an infringement from established standards and has no significant bearing on its effective use or operation.

9.4 APPEARANCE

An appearance test should be conducted by human sight at approximately 30 cm distance from the LCD module under fluorescent light. The inspection area of LCD panel shall be within the range of following limits.

9.5 INSPECTION QUALITY CRITERIA

Item	Description of defects				Class of Defects	Acceptable level (%)
Function	Short circuit or Pattern cut				Major	0.65
Dimension	Deviation from drawings				Major	1.5
Black spots	Ave . dia . D	area A	area B		Minor	2.5
	D≤0.2	Disregard				
	0.2<D≤0.3	3	4			
	0.3<D≤0.4	2	3			
	0.4<D	0	1			
Black lines	Width W, Length L		A	B	Minor	2.5
	W≤0.03		disregard			
	0.03<W≤0.05		3	4		
	0.05<W≤0.07 , L≤3.0		1	1		
	See line criteria					
Bubbles in polarizer	Average diameter D 0.2 < D < 0.5 mm for N = 4 , D > 0.5 for N = 1				Minor	2.5
Color uniformity	Rainbow color or newton ring.				Minor	2.5
Glass Scratches	Obvious visible damage.				Minor	2.5
Contrast ratio	See note 1				Minor	2.5
Response time	See note 2				Minor	2.5
Viewing angle	See note 3				Minor	2.5



9.6 RELIABILITY

Test Item	Test Conditions	Note
	Extended Temp. type	
High Temperature Operation	70±3°C , t=96 hrs	
Low Temperature Operation	-20±3°C , t=96 hrs	
High Temperature Storage	80±3°C , t=96 hrs	1,2
Low Temperature Storage	-30±3°C , t=96 hrs	1,2
Thermal Shock Test	-30°C ~ 25°C ~ 80°C 30 min. 5 min. 30 min. (1 cycle) Total 5 cycle	1,2
Humidity Test	40 °C, Humidity 90%, 96 hrs	1,2
Vibration Test (Packing)	Sweep frequency : 10 ~ 55 ~ 10 Hz/1min Amplitude : 0.75mm Test direction : X.Y.Z/3 axis Duration : 30min/each axis	2

Note 1 : Condensation of water is not permitted on the module.

Note 2 : The module should be inspected after 1 hour storage in normal conditions
(15-35°C , 45-65%RH).

Definitions of life end point :

- Current drain should be smaller than the specific value.
- Function of the module should be maintained.
- Appearance and display quality should not have degraded noticeably.
- Contrast ratio should be greater than 50% of the initial value.

10 HANDLING PRECAUTIONS

- (1) An LCD module is a fragile item and should not be subjected to strong mechanical shocks.
- (2) Avoid applying pressure to the module surface. This will distort the glass and cause a change in colour.
- (3) Under no circumstances should the position of the bezel tabs or their shape be modified.
- (4) Do not modify the display PCB in either shape or positioning of components.
- (5) Do not modify or move location of the zebra or heat seal connectors.
- (6) The device should only be soldered to during interfacing. Modification to other areas of the board should not be carried out.
- (7) In the event of LCD breakage and resultant leakage of fluid do not inhale, ingest or make contact with the skin. If contact is made rinse immediately.
- (8) When cleaning the module use a soft damp cloth with a mild solvent, such as Isopropyl or Ethyl alcohol. The use of water, ketone or aromatic is not permitted.
- (9) Prior to initial power up input signals should not be applied.
- (10) Protect the module against static electricity and observe appropriate anti-static precautions.

INTERFACE																			
1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
VEE	VSS	VDD	VO	/WR	/RD	/CE	C/D	/RST	DB0	DB1	DB2	DB3	DB4	DB5	DB6	DB7	FS	A/EL1	K/EL2

