



Features

- ESD Protect for 1 Line with Uni-directional.
- Provide ESD protection for the protected line to IEC 61000-4-2 (ESD) $\pm 20\text{kV}$ (air), $\pm 12\text{kV}$ (contact)
IEC 61000-4-4 (EFT) 40A (5/50ns)
IEC 61000-4-5 (Lightning) 7A (8/20 μs)
Cable Discharge Event (CDE)
- Ultra-small SOD-523 package saves board space.
- Protect one I/O line or one power line
- Fast turn-on and Low clamping voltage
- For low operating voltage applications: 5V, 4.2V, 3.3V, 2.5V
- Solid-state silicon-avalanche and active circuit triggering technology.

Applications

- Computer Interfaces Protection
- Microprocessors Protection
- Serial and Parallel Ports Protection
- Control Signal Lines Protection
- Power lines on PCB Protection
- Latchup Protection

Description

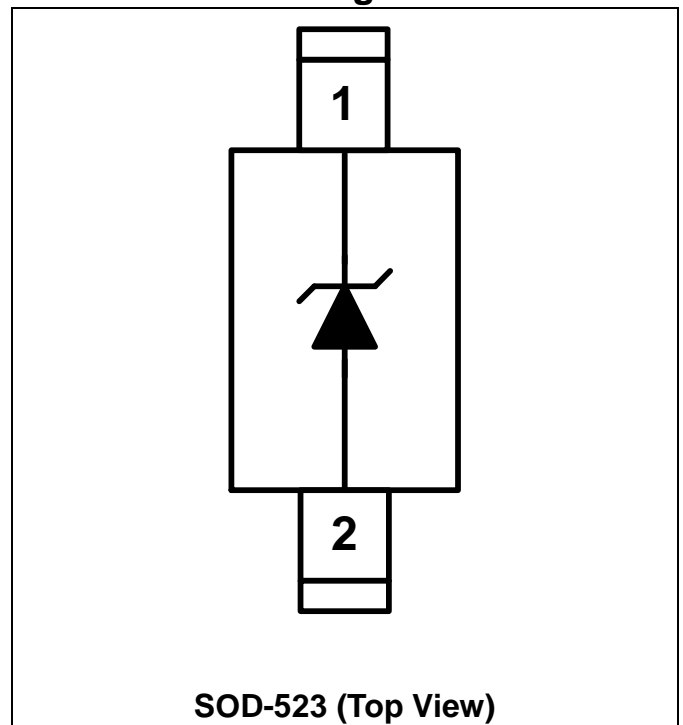
AZ2015-01H is a design which includes a unidirectional surge rated clamping cell to protect one power line, or one control line, or one low speed data line in an electronic systems. The AZ2015-01H has been specifically designed to protect sensitive components which are connected to power and control lines from over-voltage damage and latch-up caused by Electrostatic Discharging (ESD), Electrical Fast Transients (EFT), Lightning, and Cable Discharge Event (CDE).

AZ2015-01H is a unique design which includes proprietary clamping cell in a single package.

During transient conditions, the proprietary clamping cell prevents over-voltage on the power line or control/data lines, protecting any downstream components.

AZ2015-01H may be used to meet the ESD immunity requirements of IEC 61000-4-2, Level 4 ($\pm 15\text{kV}$ air, $\pm 8\text{kV}$ contact discharge)

Circuit Diagram / Pin Configuration



SPECIFICATIONS

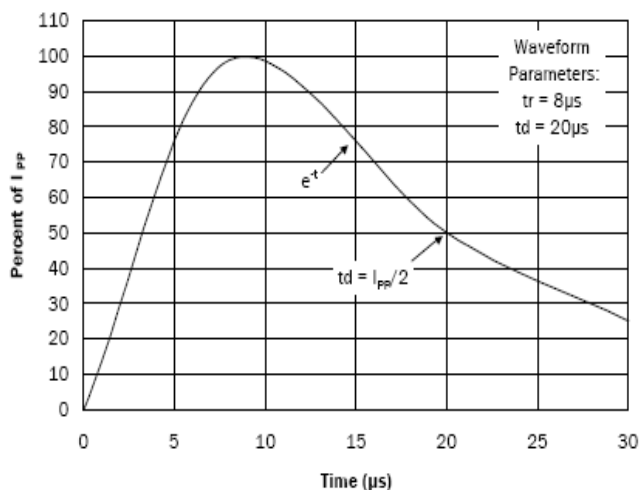
ABSOLUTE MAXIMUM RATINGS			
PARAMETER	PARAMETER	RATING	UNITS
Peak Pulse Current (tp =8/20us)	I_{PP}	8.5	A
Operating Supply Voltage (pin-1,-2 to pin-3)	V_{DC}	6	V
ESD per IEC 61000-4-2 (Air)	V_{ESD}	23	kV
ESD per IEC 61000-4-2 (Contact)		15	
Lead Soldering Temperature	T_{SOL}	260 (10 sec.)	°C
Operating Temperature	T_{OP}	-55 to +125	°C
Storage Temperature	T_{STO}	-55 to +150	°C

ELECTRICAL CHARACTERISTICS						
PARAMETER	SYMBOL	CONDITIONS	MINI	TYP	MAX	UNITS
Reverse Stand-Off Voltage	V_{RWM}	$T=25^{\circ}C$.			5	V
Reverse Leakage Current	I_{Leak}	$V_{RWM} = 5V, T=25^{\circ}C$.			2.5	μA
Reverse Breakdown Voltage	V_{BV}	$I_{BV} = 1mA, T=25^{\circ}C$.	6.1		9	V
Forward Voltage	V_F	$I_F = 15mA, T=25^{\circ}C$	0.6	0.8	1	V
Clamping Voltage 1	V_{CL}	$I_{PP}=5A, tp=8/20us, T=25^{\circ}C$.		7	8	V
Clamping Voltage 2	V_{CL}	$I_{PP}=7A, tp=8/20us, T=25^{\circ}C$.		8	9	V
ESD Holding Voltage	V_{hold}	IEC 61000-4-2 6kV, $T=25^{\circ}C$, Contact mode.		9		V
Channel Input Capacitance	C_{IN}	$V_R = 0V, f = 1MHz, T=25^{\circ}C$.		20	25	pF

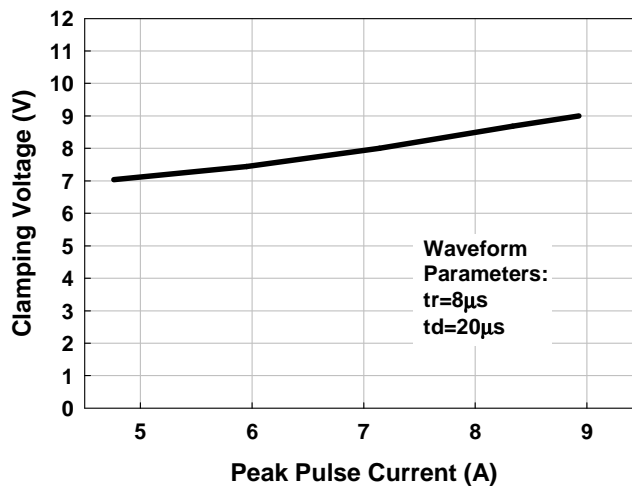


Typical Characteristics

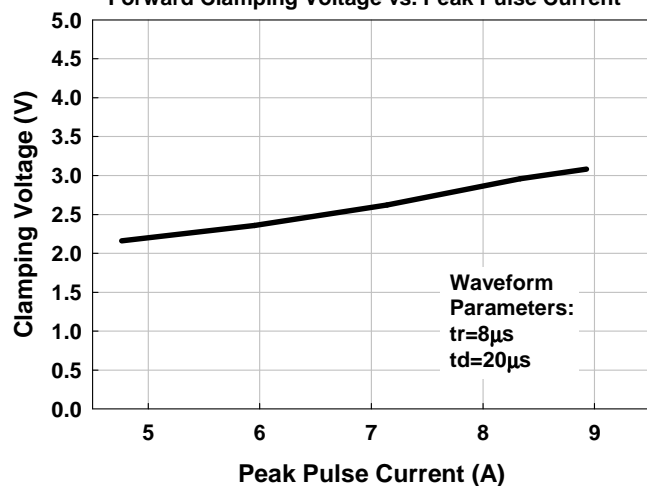
Pulse Waveform



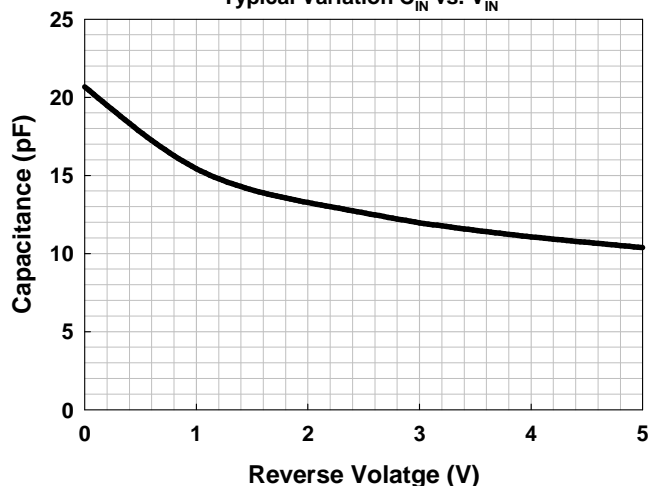
Clamping Voltage vs. Peak Pulse Current



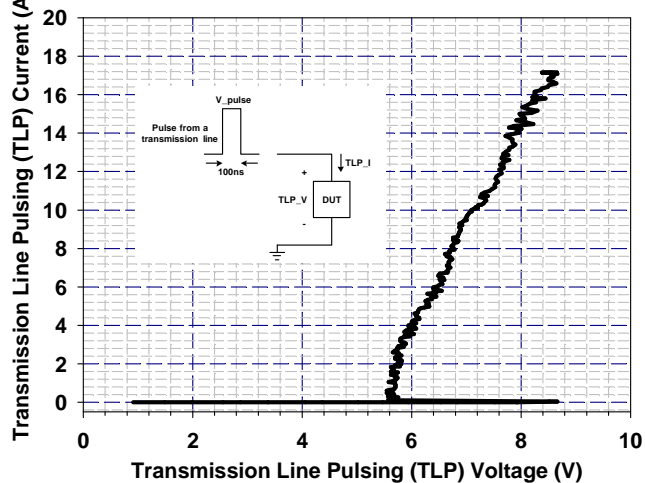
Forward Clamping Voltage vs. Peak Pulse Current



Typical Variation C_{IN} vs. V_{IN}



Transmission Line Pulsing (TLP) Measurement



Applications Information

The AZ2015-01H is designed to protect one line against System ESD/EFT/Lightning pulses by clamping them to an acceptable reference.

The usage of the AZ2015-01H is shown in Fig. 1. Protected lines, such as data lines, control lines, or power lines, are connected at pin 1. The pin 2 should be connected directly to a ground plane on the board. All path lengths connected to the pins of AZ2015-01H should be kept as short as possible to minimize parasitic inductance in the board traces.

In order to obtain enough suppression of ESD induced transient, good circuit board is critical.

Thus, the following guidelines are recommended:

- Minimize the path length between the protected lines and the AZ2015-01H.
- Place the AZ2015-01H near the input terminals or connectors to restrict transient coupling.
- The ESD current return path to ground should be kept as short as possible.
- Use ground planes whenever possible.
- NEVER route critical signals near board edges and near the lines which the ESD transient easily injects to.

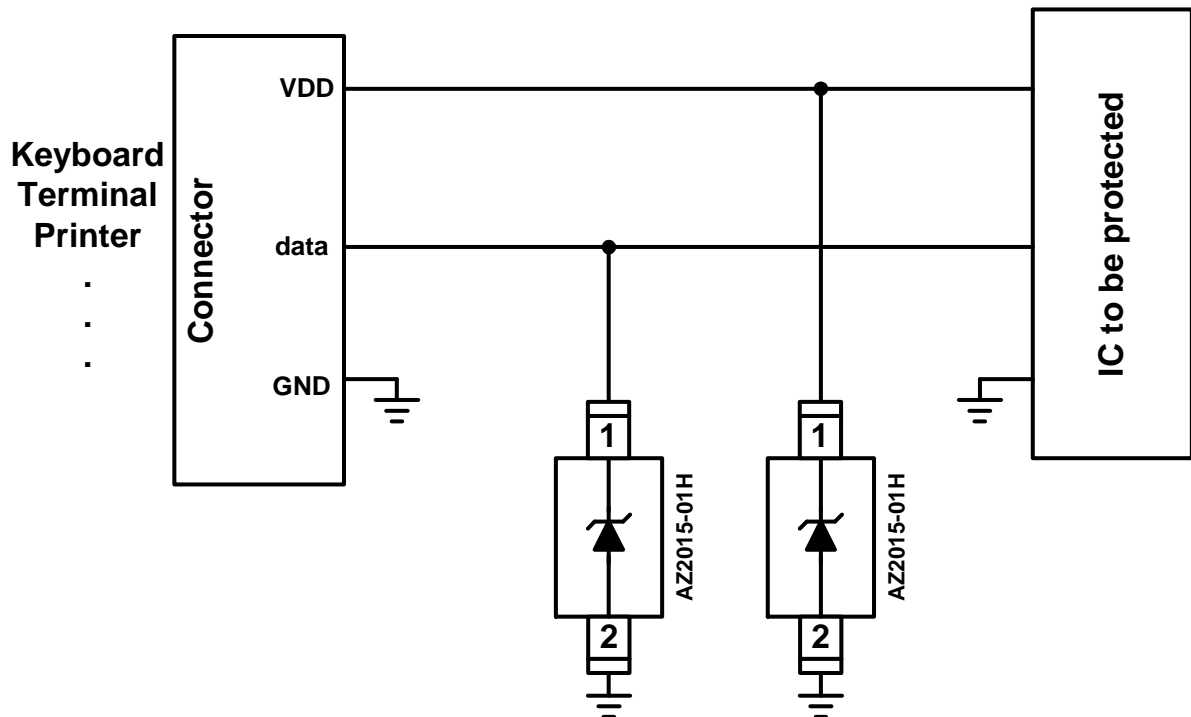


Fig. 1



Fig. 2 shows another simplified example of using AZ2015-01H to protect the control lines, low speed data lines, and power lines from ESD transient stress.

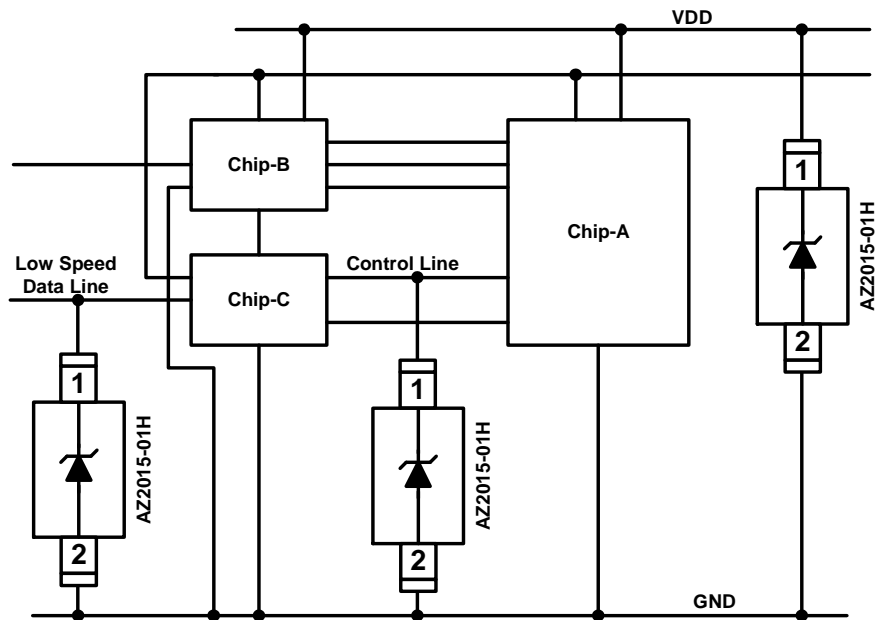


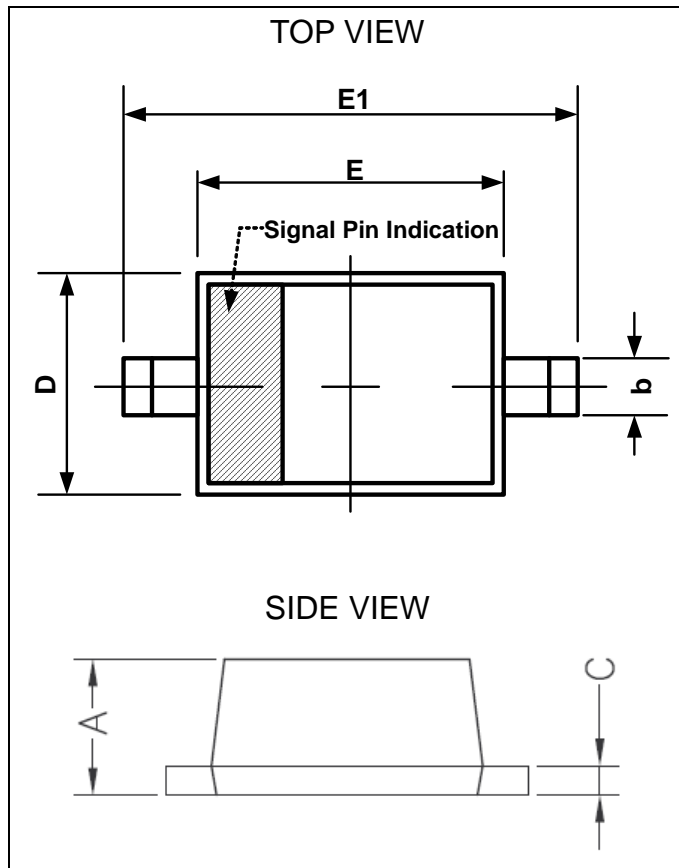
Fig. 2



Mechanical Details

SOD-523

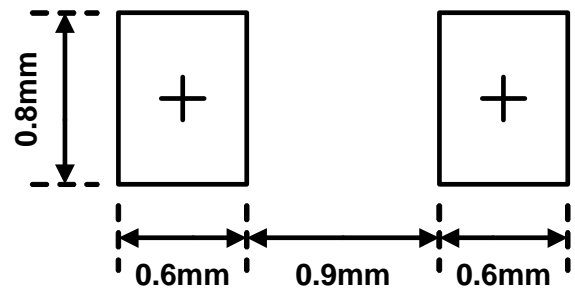
PACKAGE DIAGRAMS



PACKAGE DIMENSIONS

Symbol	Millimeters		Inches	
	MIN.	MAX.	MIN.	MAX.
A	0.5	0.77	0.020	0.030
B	0.25	0.35	0.010	0.014
C	0.08	0.2	0.003	0.008
D	0.7	0.9	0.028	0.035
E	1.1	1.3	0.043	0.051
E1	1.5	1.7	0.059	0.067

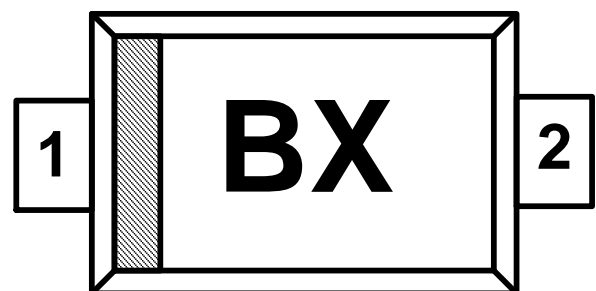
LAND LAYOUT



Notes:

This LAND LAYOUT is for reference purposes only. Please consult your manufacturing partners to ensure your company's PCB design guidelines are met.

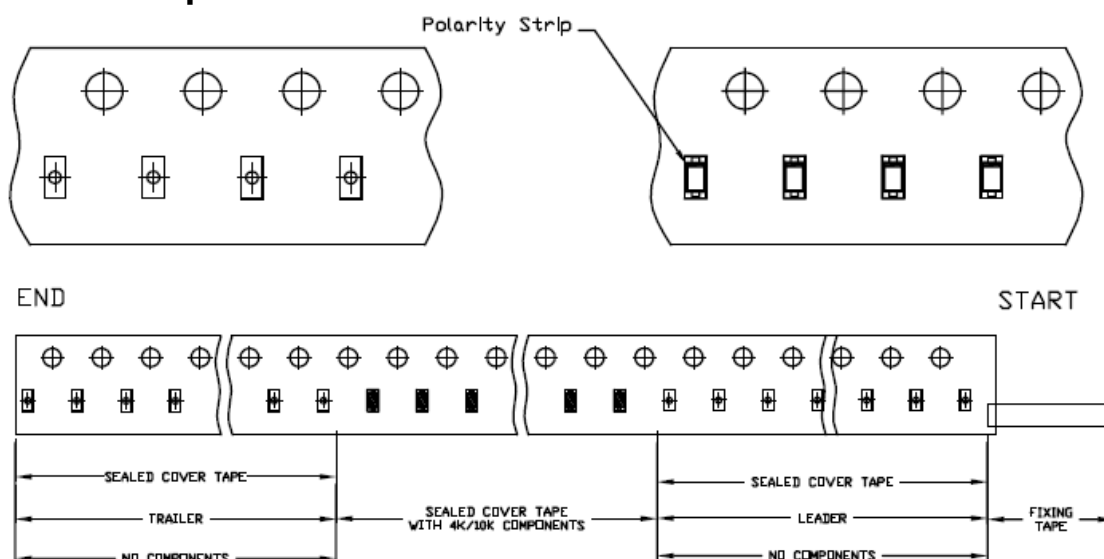
MARKING CODE



B = Device Code
X = Date Code

Part Number	Marking Code
AZ2015-01H (Rohs part)	BX
AZ2015-01H (Green part)	bX

Tape and Reel Specification



Ordering Information

PN#	ROHS/ Green	Type	Reel size	MOQ	MOQ/interal box	MOQ/carton
AZ2015-01H.R7F	ROHS	T/R	7 inch	4,000/reel	4 reel = 16,000/box	6 box =96,000/carton
AZ2015-01H.R7G	Green	T/R	7 inch	4,000/reel	4 reel = 16,000/box	6 box =96,000/carton

Revision History

Revision	Modification Description
Revision 2006/11/10	Original Release.
Revision 2006/12/08	1. Change the clamping cell symbol for easy understanding. 2. Add the TLP characterization. 3. Add the ESD holding voltage characterization under IEC61000-4-2 6kV contact mode.
Revision 2007/02/05	1. Change the Characterization Temp of V_{RWM} , V_{Leak} , and V_{BV} from 125°C to 25°C.
Revision 2007/05/15	Change Qty per Reel from 3000 to 4000 in the Ordering Information.
Revision 2007/07/16	Remove the Ordering Information. Add Tape and Reel Specification.
Revision 2008/03/24	Modify the description in the Features, from "Low operating voltage: 5V" to "For low operating voltage applications: 5V, 4.2V, 3.3V, 2.5V".
Revision 2008/04/10	Add marking code for the Green part.
Revision 2008/12/16	Add the Ordering Information
Revision 2009/12/31	Change the description of "Anode Indication" at PACKAGE DIAGRAMS to be the "Signal Pin Indication".
Revision 2010/1/11	Update the PACKAGE DIMENSIONS.
Revision 2011/06/18	Update the Company Logo.