(V) Preliminary Specifications
(   ) Final Specifications

Module	12.1" WXGA Color TFT-LCD with LED Backlight design
Model Name	B121EW10 V2 **(HW: 0A)**
Note ( )	*LED Backlight with driving circuit design*

Customer　　　　**Date**	**Approved by**　　　　**Date**
Checked & Approved by　　**Date**	**Prepared by**
Note: This Specification is subject to change without notice.	**NBBU Marketing Division / AU Optronics corporation**

Product Specification
AU OPTRONICS CORPORATION

Contents

Record of Revision

Version and Date	Page	Old description	New Description	Remark
0.1 2008/06/10	All	First Edition for Customer		
0.2 2008/6/23	5	Module Thickness 3.5mm max.	Module Thickness modified to 3.65mm max.	
	5	Module weight 250g max	Module weight 260g max.	
	29,30, 31,32	Module 2D Drawing	(1) Modified CN location & Module thickness. (2) PCBA component limited height	
0.3 2008/11/14	5	B141EW05 V0	B121EW10 V2	
	6	Chromaticity Coodinates: TBD	Chromaticity Coodinates: fill in Spec	
	15	LED Power consumption: 4W	LED Power consumption: 2.856W	

1. Handling Precautions

1) Since front polarizer is easily damaged, pay attention not to scratch it.

2) Be sure to turn off power supply when inserting or disconnecting from input connector.

3) Wipe off water drop immediately. Long contact with water may cause discoloration or spots.

4) When the panel surface is soiled, wipe it with absorbent cotton or other soft cloth.

5) Since the panel is made of glass, it may break or crack if dropped or bumped on hard surface.

6) Since CMOS LSI is used in this module, take care of static electricity and insure human earth when handling.

7) Do not open nor modify the Module Assembly.

8) Do not press the reflector sheet at the back of the module to any directions.

9) At the insertion or removal of the Signal Interface Connector, be sure not to rotate nor tilt the Interface Connector of the TFT Module.

11) After installation of the TFT Module into an enclosure (Notebook PC Bezel, for example), do not twist nor bend the TFT Module even momentary. At designing the enclosure, it should be taken into consideration that no bending/twisting forces are applied to the TFT Module from outside. Otherwise the TFT Module may be damaged.

12) Small amount of materials having no flammability grade is used in the LCD module. The LCD module should be supplied by power complied with requirements of Limited Power Source (IEC60950 or UL1950), or be applied exemption.

13) Disconnecting power supply before handling LCD modules, it can prevent electric shock, DO NOT TOUCH the electrode parts, cables, connectors and LED circuit part of TFT module that a LED light bar build in as a light source of back light unit. High voltage is supplied to these parts when power turn on.

2. General Description

B121EW10 V2 is a Color Active Matrix Liquid Crystal Display composed of a TFT LCD panel, a driver circuit, and LED backlight system. The screen format is intended to support the WXGA (1280(H) x 800(V)) screen and 262k colors (RGB 6-bits data driver) with LED backlight driving circuit. All input signals are LVDS interface compatible.

B121EW10 V2 is designed for a display unit of notebook style personal computer and industrial machine.

2.1 General Specification

The following items are characteristics summary on the table at 25 ℃ condition:

Items	Unit	Specifications			
Screen Diagonal	[mm]	12.1"			
Active Area	[mm]	261.12 x 163.2			
Pixels H x V		1280x3(RGB) x 800			
Pixel Pitch	[mm]	0.204			
Pixel Format		B.G.R. Vertical Stripe			
Display Mode		Normally White			
White Luminance (I_LED=20mA) Note: I_LED is LED current	[cd/m^2]	220 typ. (5 points average) 187 min. (5 points average) (Note1)			
Luminance Uniformity		1.25 max. (5 points)			
Contrast Ratio		500 typ			
Response Time	[ms]	16 typ			
Nominal Input Voltage VDD	[Volt]	+3.3 typ.			
Power Consumption	[Watt]	4.5 max. (Include Logic and Blu power) (Note1)			
Weight	[Grams]	260 max.			
Physical Size w/ bracket & PCBA	[mm]		Min.	Typ.	Max.
		Length	283.42	283.92	284.42
		Width	189	189.5	190
		Thickness			3.65
Electrical Interface		1 channel LVDS			
Surface Treatment		Glare, Hardness 3H,			

Support Color		262K colors (RGB 6-bit)

Temperature Range
  Operating
  Storage (Non-Operating)
	[^{o}C]
	[^{o}C]
		0 to +50
		-20 to +65

RoHS Compliance		RoHS Compliance

Note 1. Total power consumption including LED power efficiency <4.9W max.

2.2 Optical Characteristics

The optical characteristics are measured under stable conditions at 25℃ (Room Temperature) :

Item	Symbol	Conditions	Min.	Typ.	Max.	Unit	Note
White Luminance **I$_{LED}$=20mA**		**5 points average**	**187**	**220**	**-**	**cd/m^2**	1, 4, 5.
Viewing Angle	θ R θ L	**Horizontal (Right)** **CR = 10 (Left)**	**40** **40**	**45** **45**	**-** **-**	**degree**	4, 9
	ϕ H ϕ L	**Vertical (Upper)** **CR = 10 (Lower)**	**10** **30**	**15** **35**	**-** **-**		
Luminance Uniformity	δ 5P	**5 Points**	**-**	**-**	**1.25**		1, 3, 4
Luminance Uniformity	δ 13P	**13 Points**	**-**	**-**	**1.50**		2, 3, 4
Contrast Ratio	**CR**		**400**	**500**	**-**		4, 6
Cross talk	**%**				**4**		4, 7
Response Time	**T$_r$**	**Rising**	**-**	**TBD**	**-**	**msec**	4, 8
	T$_f$	**Falling**	**-**	**TBD**	**-**		
	T$_{RT}$	**Rising + Falling**	**-**	**16**			
Color / Chromaticity Coodinates	**Red x**	**CIE 1931**	**0.570**	**0.600**	**0.630**		4
	Red y		**0.310**	**0.340**	**0.370**		
	Green x		**0.290**	**0.320**	**0.350**		
	Green y		**0.520**	**0.550**	**0.580**		
	Blue x		**0.120**	**0.150**	**0.180**		
	Blue y		**0.110**	**0.140**	**0.170**		
	White x		**0.263**	**0.313**	**0.363**		
	White y		**0.279**	**0.329**	**0.379**		
NTSC	**%**		**-**	**45**	**-**		

Note 1: 5 points position (Ref: Active area)
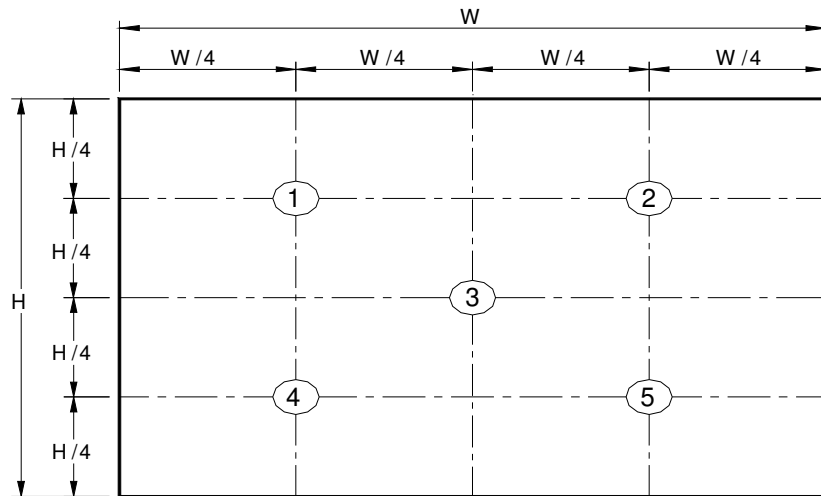

Note 2: 13 points position (Ref: Active area)
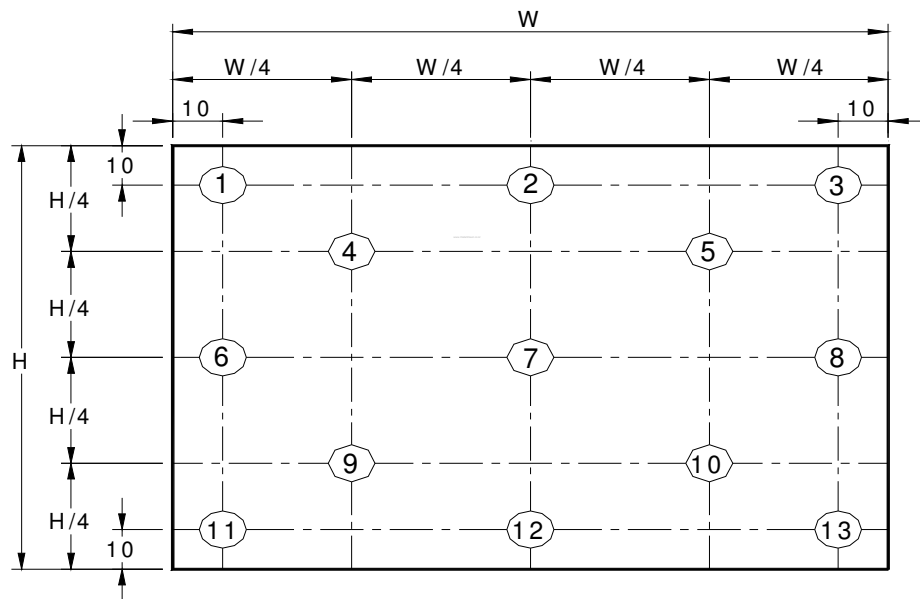
Note 3: The luminance uniformity of 5 or13 points is defined by dividing the maximum luminance values by the minimum test point luminance

$$\delta_{W5} = \frac{\text{Maximum Brightness of five points}}{\text{Minimum Brightness of five points}}$$

$$\delta_{W13} = \frac{\text{Maximum Brightness of thirteen points}}{\text{Minimum Brightness of thirteen points}}$$

Note 4: Measurement method

The LCD module should be stabilized at given temperature for 30 minutes to avoid abrupt temperature change during measuring. In order to stabilize the luminance, the measurement should be executed after lighting Backlight for 30 minutes in a stable, windless and dark room, , and it should be measured in the center of screen.

Center of the screen
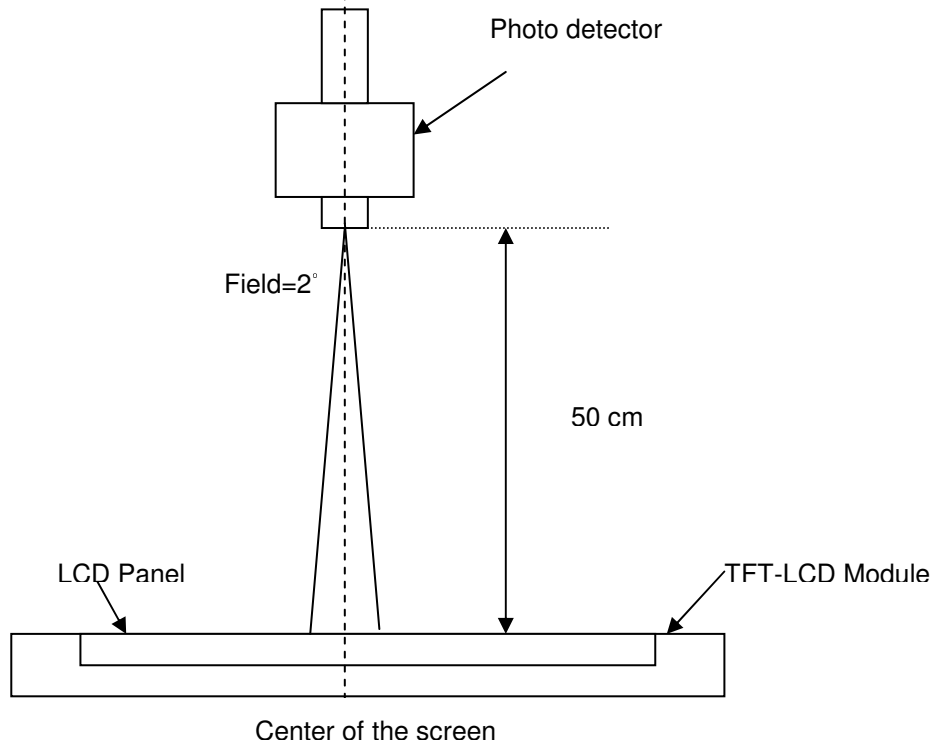
Note 5： Definition of Average Luminance of White (Y_L):

Measure the luminance of gray level 63 at 5 points，Y_L = [L (1)+ L (2)+ L (3)+ L (4)+ L (5)] / 5

L (x) is corresponding to the luminance of the point X at Figure in Note (1).

Note 6： Definition of contrast ratio:

Contrast ratio is calculated with the following formula.

$$\text{Contrast ratio (CR)} = \frac{\text{Brightness on the "White" state}}{\text{Brightness on the "Black" state}}$$
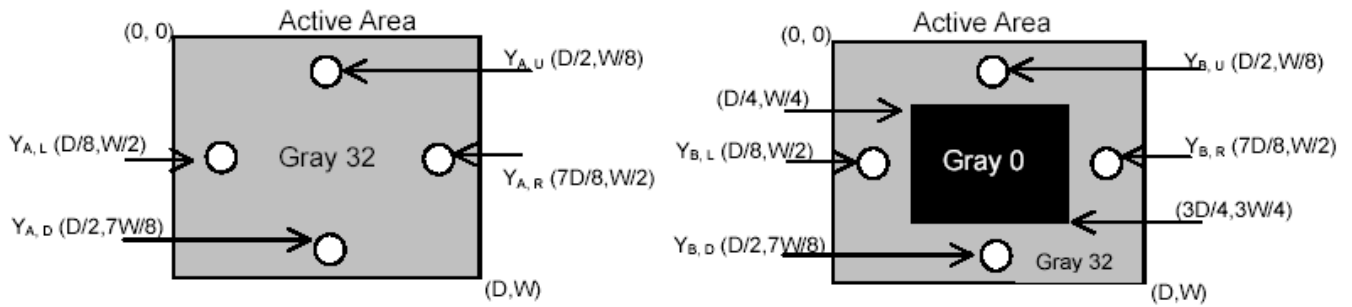
Note 7： Definition of Cross Talk (CT)

CT = | Y_B – Y_A | / Y_A × 100 (%)

Where

Y_A = Luminance of measured location without gray level 0 pattern (cd/m2)

Y_B = Luminance of measured location with gray level 0 pattern (cd/m2)

Note 8: Definition of response time:

The output signals of BM-7 or equivalent are measured when the input signals are changed from "Black" to "White" (falling time) and from "White" to "Black" (rising time), respectively. The response time interval between the 10% and 90% of amplitudes. Refer to figure as below.
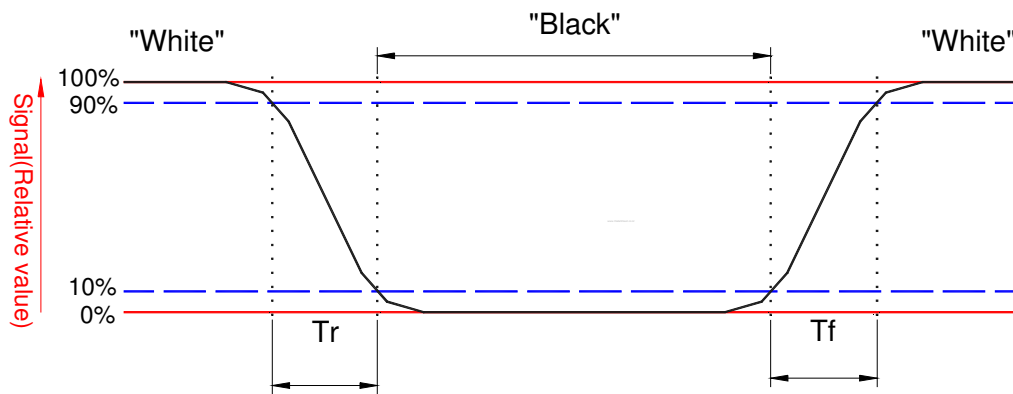
Note 9. Definition of viewing angle

Viewing angle is the measurement of contrast ratio ≥ 10, at the screen center, over a 180° horizontal and 180° vertical range (off-normal viewing angles). The 180° viewing angle range is broken down as follows; 90° (θ) horizontal left and right and 90° (Φ) vertical, high (up) and low (down). The measurement direction is typically perpendicular to the display surface with the screen rotated about its center to develop the desired measurement viewing angle.
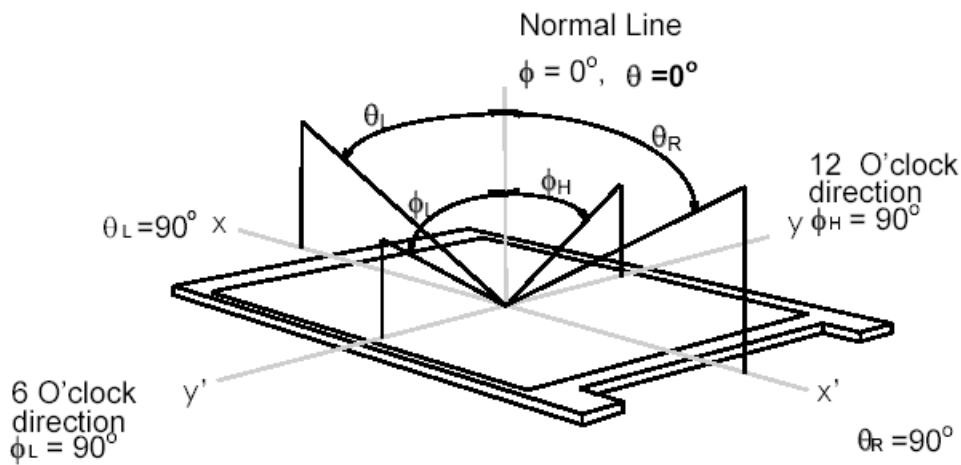
3. Functional Block Diagram

The following diagram shows the functional block of the 12.1 inches wide Color TFT/LCD 40 Pin (One ch/connector Module:
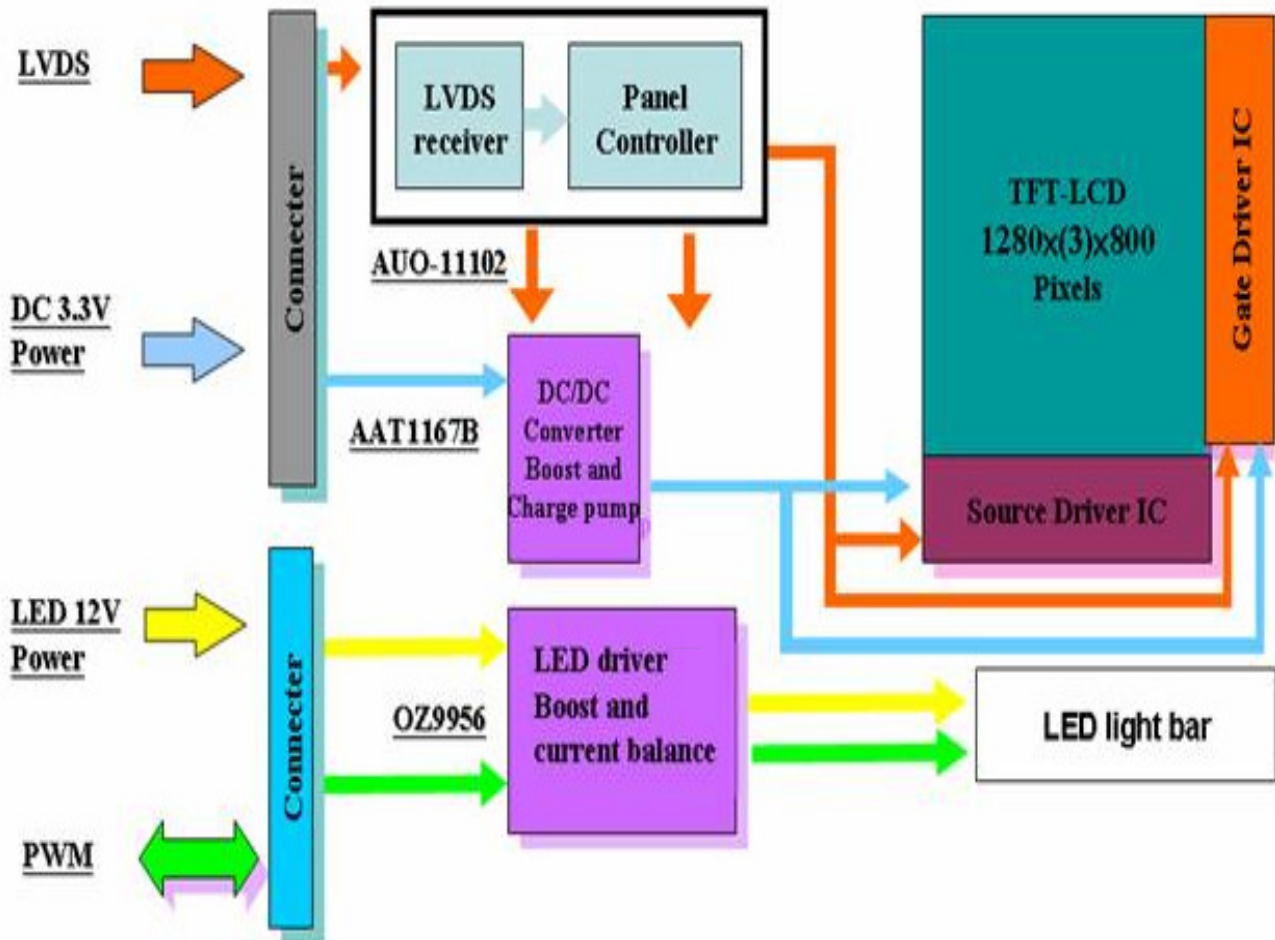
4. Absolute Maximum Ratings

An absolute maximum rating of the module is as following:

4.1 Absolute Ratings of TFT LCD Module

Item	Symbol	Min	Max	Unit	Conditions
Logic/LCD Drive Voltage	Vin	-0.3	+4.0	[Volt]	Note 1,2

4.2 Absolute Ratings of Backlight Unit

Item	Symbol	Min	Max	Unit	Conditions
LED Driving Voltage	V_{LED}	-	36 (Row Output)	[Volt]	Note 1,2,3
LED Driving Current	I_{LED}	-	30 (Row Output)	[mA] rms	Note 1,2,3

4.3 Absolute Ratings of Environment

Item	Symbol	Min	Max	Unit	Conditions
Operating Temperature	TOP	0	+50	[^{o}C]	Note 4
Operation Humidity	HOP	8	95	[%RH]	Note 4
Storage Temperature	TST	-20	+65	[^{o}C]	Note 4
Storage Humidity	HST	5	95	[%RH]	Note 4

Note 1: At Ta (25℃)

Note 2: Permanent damage to the device may occur if exceed maximum values

Note 3: LED specification refer to section 5.2

Note 4: For quality performance, please refer to AUO IIS (Incoming Inspection Standard).
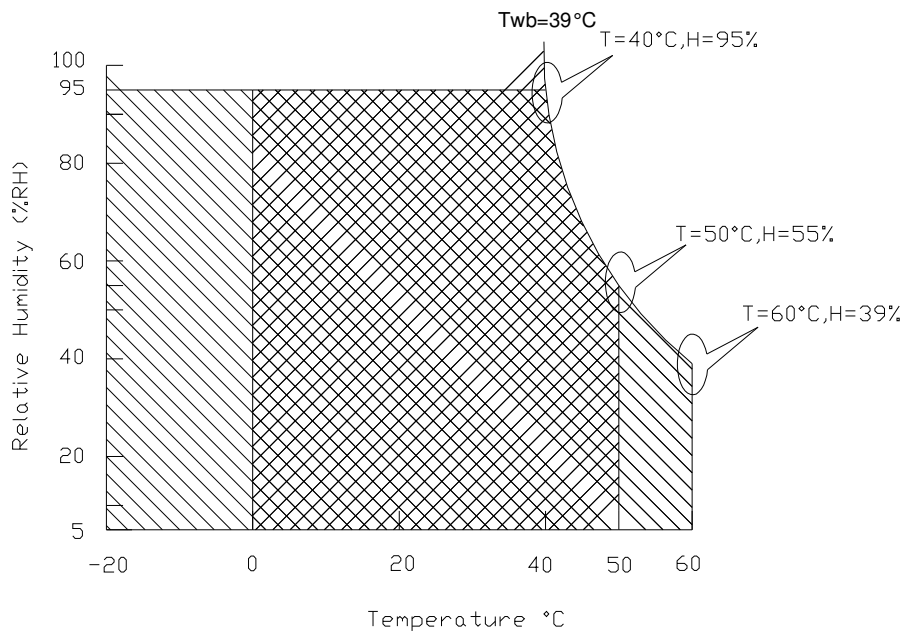
Operating Range   Storage Range

5. Electrical characteristics

5.1 TFT LCD Module

5.1.1 Power Specification

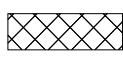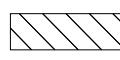Input power specifications are as follows;

The power specification are measured under 25℃ and frame frenquency under 60Hz

Symble	Parameter	Min	Typ	Max	Units	Note
VDD	Logic/LCD Drive Voltage	2.8	3.3	3.8	[Volt]	
PDD	VDD Power	0.88	0.93	0.97	[Watt]	Note 1/2
IDD	IDD Current	255	282	314	[mA]	Note 1/2
IRush	Inrush Current	-	-	540	[mA]	Note 3
VDDrp	Allowable Logic/LCD Drive Ripple Voltage	-	-	200	[mV] p-p	

Note 1 : Maximum Measurement Condition：Black Pattern

Note 2：Typical Measurement Condition: Black Pattern

Note 3：Measure Condition

Vin rising time
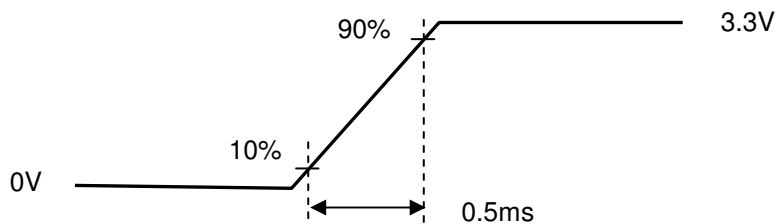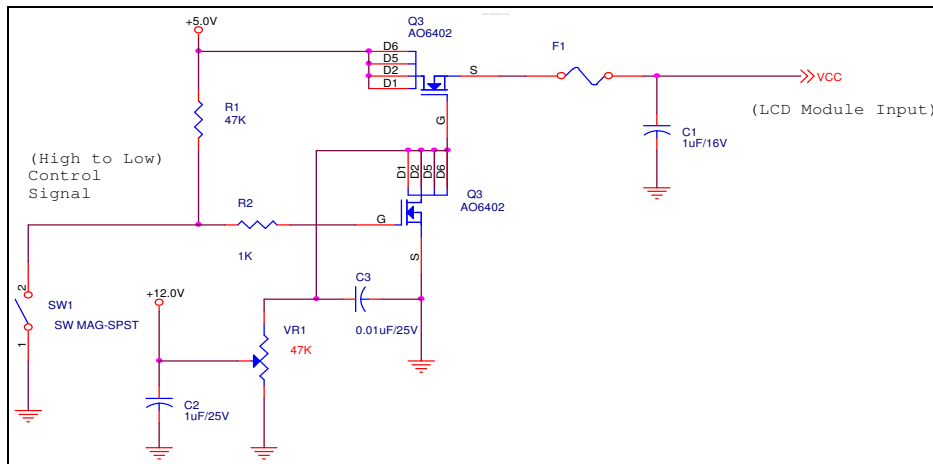
5.1.2 Signal Electrical Characteristics

Input signals shall be low or High-impedance state when VDD is off.

It is recommended to refer the specifications of THC63LVDF84A (Thine Electronics Inc.) in detail.

Signal electrical characteristics are as follows;

Parameter	Condition	Min	Max	Unit
Vth	Differential Input High Threshold (Vcm=+1.2V)	-	100	[mV]
Vtl	Differential Input Low Threshold (Vcm=+1.2V)	-100	-	[mV]
Vcm	Differential Input Common Mode Voltage	1.125	1.375	[V]

Note: LVDS Signal Waveform
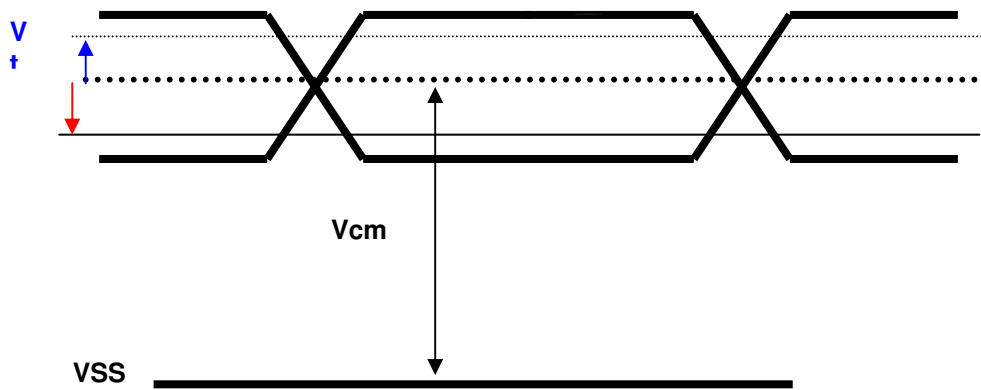
5.2 Backlight Unit

LED Parameter guideline for LED driving selection (Ref. Remark 1)

Parameter	Symbol	Min	Typ	Max	Units	Condition
LED Forward Voltage	V_F	3.0		3.4	[Volt]	(Ta=25℃)
LED Forward Current	I_F			20	[mA]	(Ta=25℃)
LED Power consumption	P_{LED}			2.856	[Watt]	(Ta=25℃) Note 1
LED Life-Time	N/A	10,000			Hour	(Ta=25℃) I_F=20 mA Note 2
Output PWM frequency	F_{PWM}	100	200	20K	Hz	
Duty ratio	--	4.3		96	%	

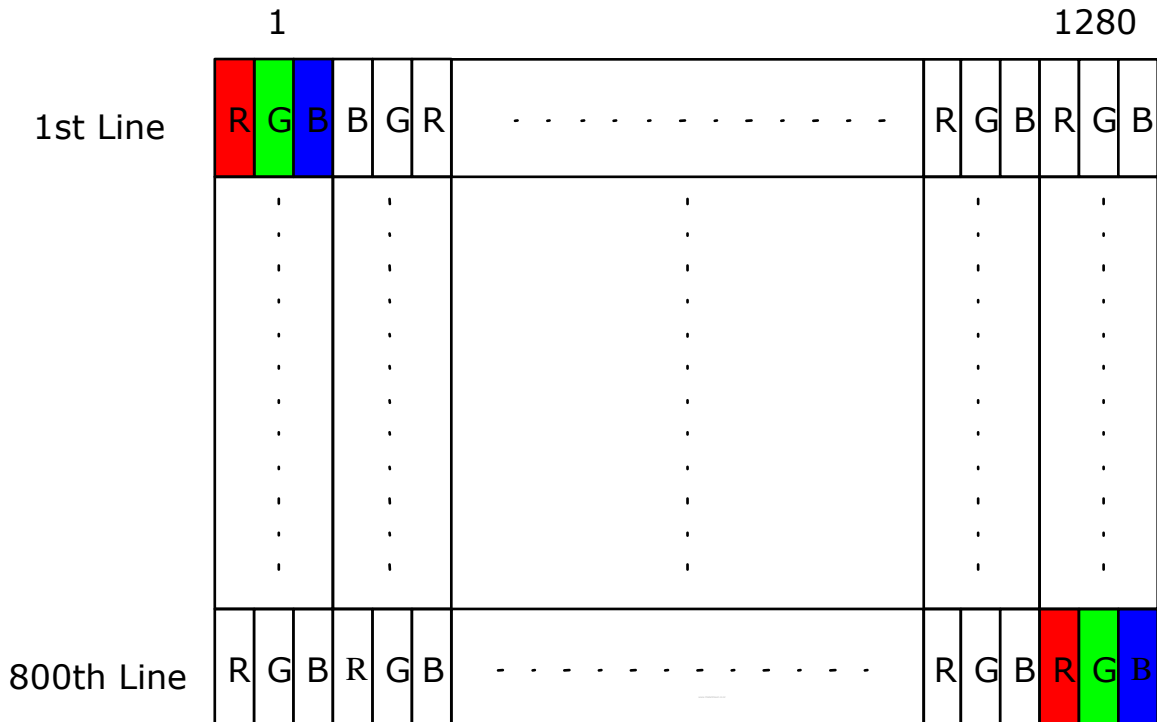**Note 1:** Calculator value for reference $I_F \times V_F = P$

Note 2: The LED life-time define as the estimated time to 50% degradation of initial luminous.

6. Signal Characteristic

6.1 Pixel Format Image

Following figure shows the relationship of the input signals and LCD pixel format.

6.2 The input data format

Signal Name	Description	
R5 R4 R3 R2 R1 R0	Red Data 5 (MSB) Red Data 4 Red Data 3 Red Data 2 Red Data 1 Red Data 0 (LSB) Red-pixel Data	Red-pixel Data Each red pixel's brightness data consists of these 6 bits pixel data.
G5 G4 G3 G2 G1 G0	Green Data 5 (MSB) Green Data 4 Green Data 3 Green Data 2 Green Data 1 Green Data 0 (LSB) Green-pixel Data	Green-pixel Data Each green pixel's brightness data consists of these 6 bits pixel data.
B5 B4 B3 B2 B1 B0	Blue Data 5 (MSB) Blue Data 4 Blue Data 3 Blue Data 2 Blue Data 1 Blue Data 0 (LSB) Blue-pixel Data	Blue-pixel Data Each blue pixel's brightness data consists of these 6 bits pixel data.
RxCLKIN	Data Clock	The signal is used to strobe the pixel data and DE signals. All pixel data shall be valid at the falling edge when the DE signal is high.
DE	Display Timing	This signal is strobed at the falling edge of RxCLKIN. When the signal is high, the pixel data shall be valid to be displayed.
VS	Vertical Sync	The signal is synchronized to RxCLKIN .
HS	Horizontal Sync	The signal is synchronized to RxCLKIN .

Note: Output signals from any system shall be low or High-impedance state when VDD is off.

6.3 Signal Description/Pin Assignment

LVDS is a differential signal technology for LCD interface and high speed data transfer device.

Pin	Signal	Description
**B121EW10 V2**	
1	NC	No Connection (Reserve)
2	VDD	PowerSupply,3.3V(typical)
3	VDD	PowerSupply,3.3V(typical)
4	DVDD	DDC 3.3Vpower
5	NC	No Connection (Reserve)
6	SCL	DDCClock
7	SDA	DDCData
8	Rin0-	-LVDSdifferential data input(R0-R5,G0)
9	Rin0+	+LVDSdifferential data input(R0-R5,G0)
10	GND	Ground
11	Rin1-	-LVDSdifferential data input(G1-G5,B0-B1)
12	Rin1+	+LVDSdifferential data input(G1-G5,B0-B1)
13	GND	Ground
14	Rin2-	-LVDSdifferential data input(B2-B5,HS,VS,DE)
15	Rin2+	+LVDSdifferential data input(B2-B5,HS,VS,DE)
16	GND	Ground
17	ClkIN-	-LVDSdifferential clock input
18	ClkIN+	+LVDSdifferential clock input
19	GND	Ground
20	NC	No Connection (Reserve)
21	NC	No Connection (Reserve)
22	GND	Ground
23	NC	No Connection (Reserve)
24	NC	No Connection (Reserve)
25	GND	Ground–Shield
26	NC	No Connection (Reserve)
27	NC	No Connection (Reserve)
28	GND	Ground–Shield
29	NC	No Connection (Reserve)
30	NC	No Connection (Reserve)
31	VLED_GND	LED Ground
32	VLED_GND	LED Ground
33	VLED_GND	LED Ground
34	NC	No Connection (Reserve)
35	PWM	System PWM Signal Input

36	LED_EN	LED enable pin(+3V Input)
37	NC	No Connection (Reserve)
38	VLED	LED Power Supply 7V-20V
39	VLED	LED Power Supply 7V-20V
40	VLED	LED Power Supply 7V-20V

Note1:

REAR VIEW

PCBA
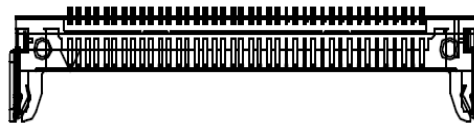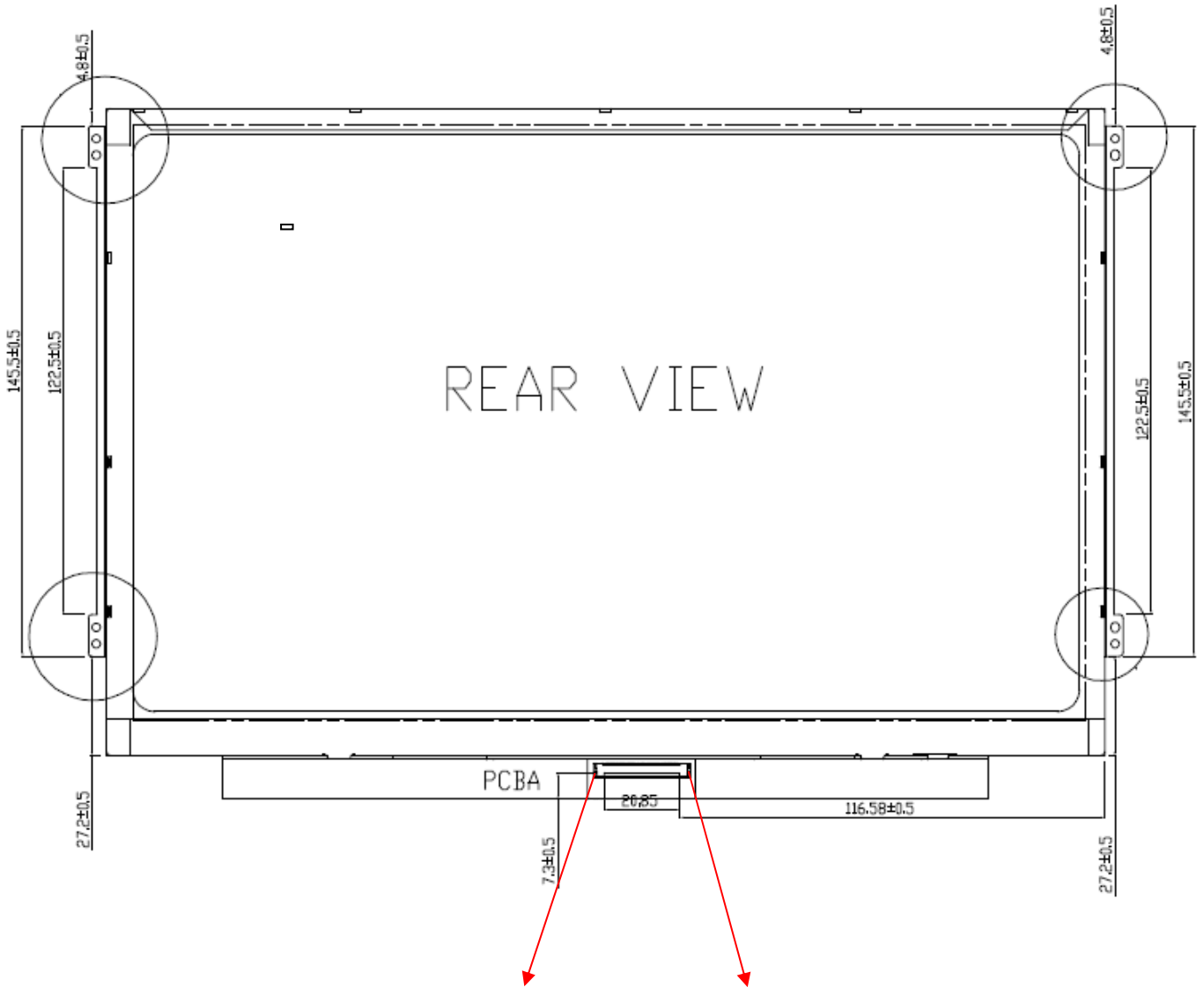
Signal Pin40

Signal Pin1
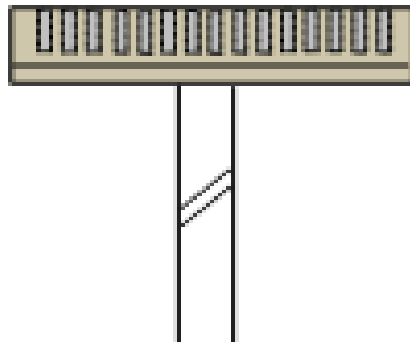
Note2: Input signals shall be low or High-impedance state when VDD is off.

internal circuit of LVDS inputs are as following.

The module uses a 100ohm resistor between positive and negative data lines of each receiver input

Signal Input

LVDS Receiver

Pin No.

8   RxIN0-

9   RxIN0+

11 RxIN1-

12 RxIN1+

14 RxIN2-

15 RxIN2+

17 RxCLKIN-

18 RxCLKIN+

6.4 Interface Timing

6.4.1 Timing Characteristics

Basically, interface timings should match the 1280x800 /60Hz   manufacturing guide line timing.
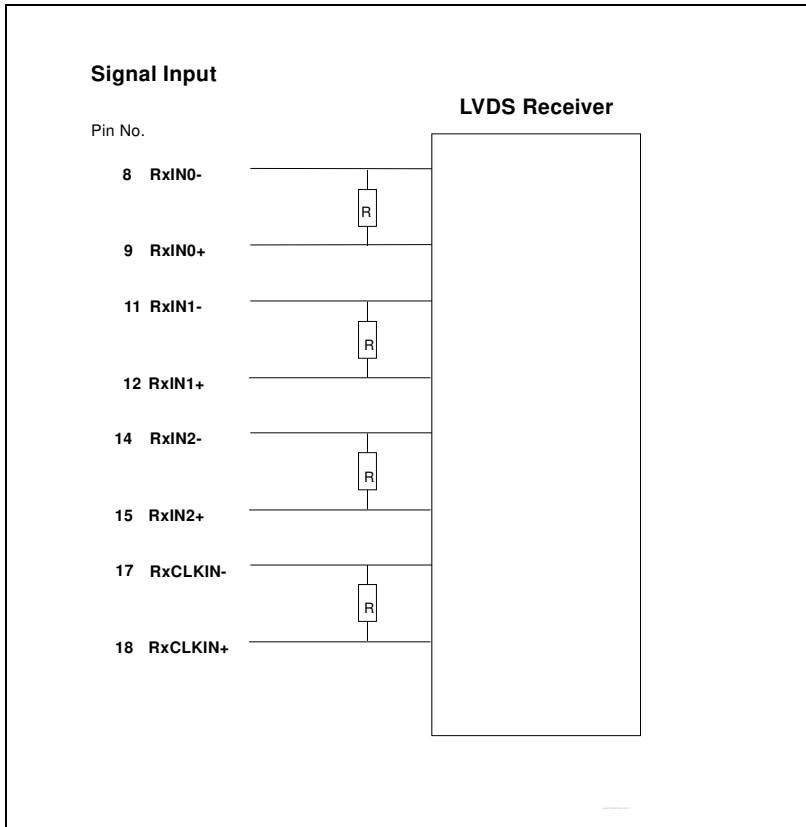
Parameter		Symbol	Min.	Typ.	Max.	Unit
Frame Rate		-		60		Hz
Clock frequency		1/ T_{Clock}		68.9		MHz
Vertical Section	Period	T_V				T_{Line}
	Active	T_{VD}		1280		
	Blanking	T_{VB}		126		
Horizontal Section	Period	T_H				T_{Clock}
	Active	T_{HD}		800		
	Blanking	T_{HB}		16		

Note : DE mode only

6.4.2 Timing diagram

Input Timing Definition (DE Mode)
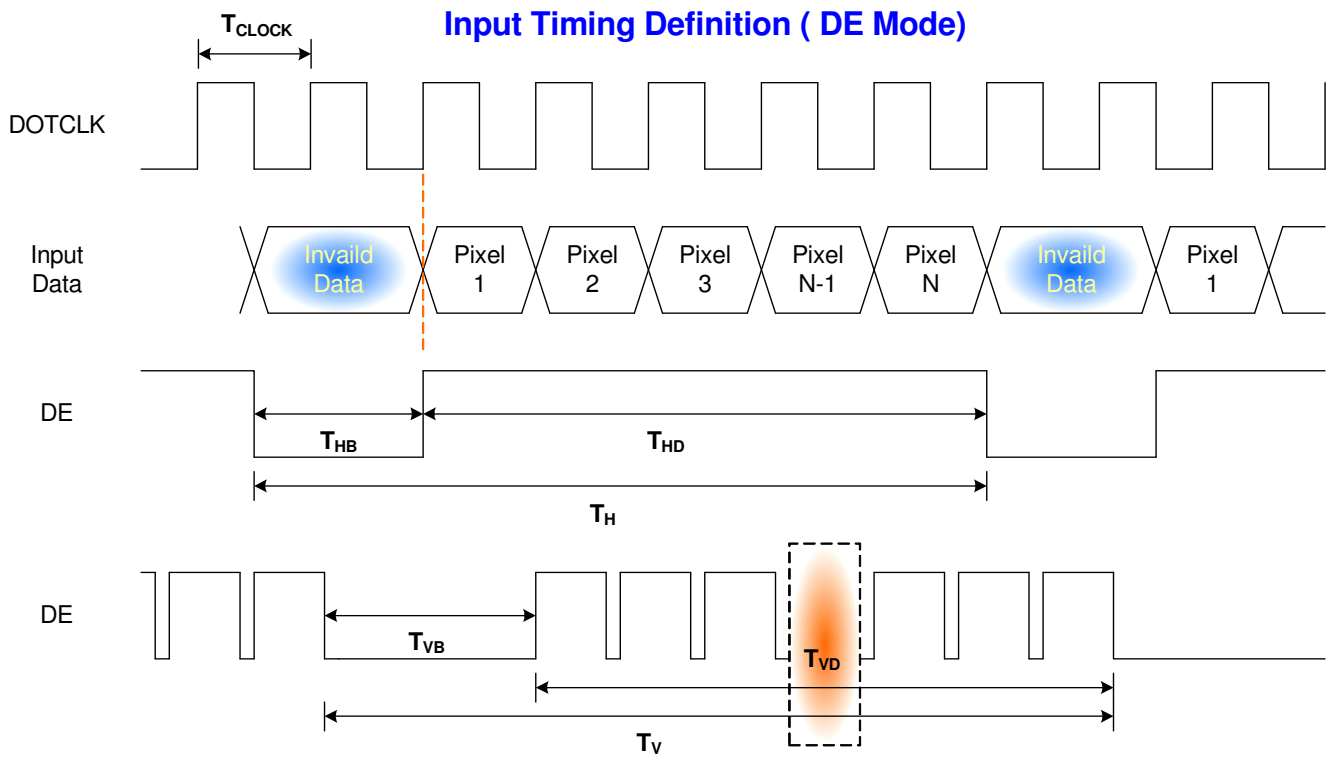
6.5 Power ON/OFF Sequence

VDD power on/off sequence is as follows. Interface signals are also shown in the chart. Signals from any system shall be Hi-Z state or low level when VDD is off
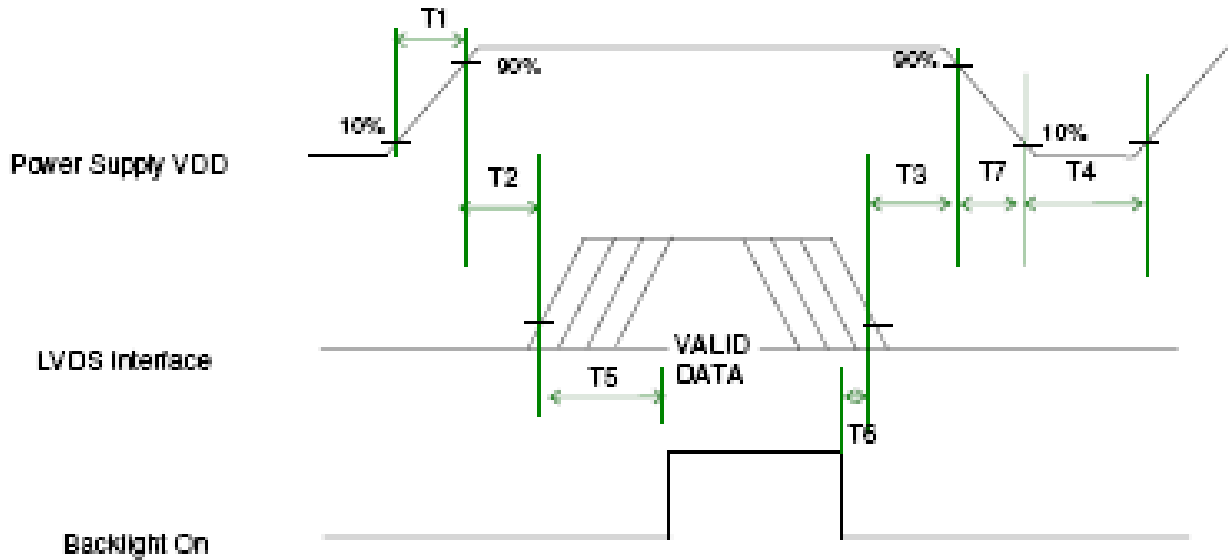
Power Sequence Timing

Parameter	Value			Units
	Min.	Typ.	Max.	
T1	0.5	-	10	(ms)
T2	0	-	50	(ms)
T3	0	-	50	(ms)
T4	400	-	-	(ms)
T5	200	-	-	(ms)
T6	200	-	-	(ms)
T7	0	-	10	(ms)

LED on/off sequence is as follows. Interface signals are also shown in the chart.
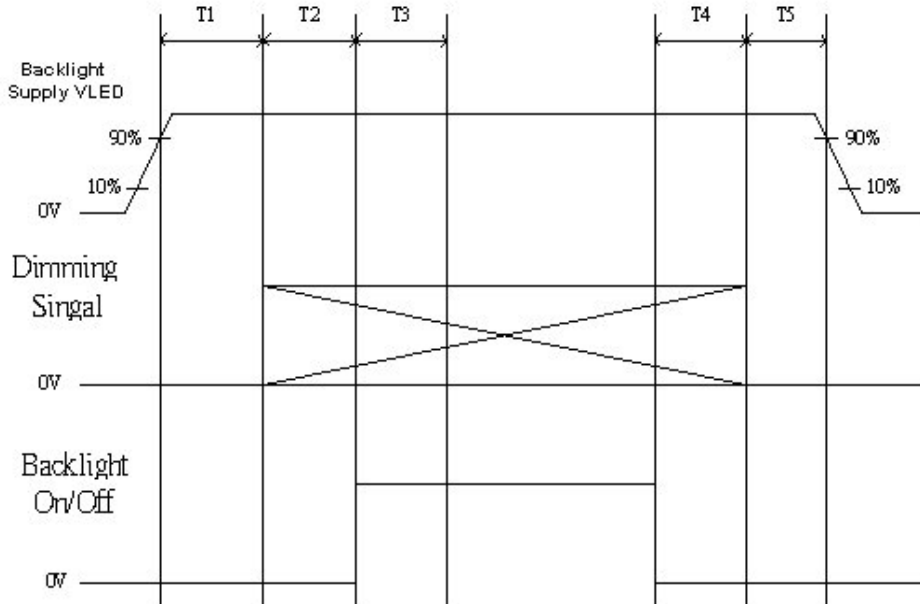
Power Sequence Timing

Symbol	Valus			Unit
	Min	Typ	Max	
T1	10	---	---	ms
T2	10	---	---	ms
T3	50	---	---	ms
T4	0	---	---	ms
T5	10	---	---	ms

Note: The duty of LED dimming signal should be more than 20% in T2 and T3.

7. Connector Description

Physical interface is described as for the connector on module.

These connectors are capable of accommodating the following signals and will be following components.

7.1 TFT LCD Module

Connector Name / Designation	For Signal Connector
Manufacturer	IPEX or compatible
Type / Part Number	IPEX 20455-040E-12
Mating Housing/Part Number	IPEX 20453-040T-11

8. LED Driving Specification

8.1 Connector Description

It is a intergrative interface and comibe into LVDS connector. The type and mating refer to section 7.

8.2 Pin Assignment

Ref. to 6.3

9. Vibration and Shock Test

9.1 Vibration Test

Test Spec:

- Test method: Non-Operation
- Acceleration: 1.5 G , Half sine pulse
- Frequency: 10 - 500Hz Sine wave
- Sweep: 30 Minutes each Axis (X, Y, Z)

9.2 Shock Test Spec:

Test Spec:

- Test method: Non-Operation
- Acceleration: 240 G , Half sine pulse
- Active time: 2 ms
- Pulse: X,Y,Z .one time for each side

10. Reliability

Items	Required Condition	Note
Temperature Humidity Bias	Ta= 40℃, 90%RH, 300h	
High Temperature Operation	Ta= 50℃, Dry, 300h	
Low Temperature Operation	Ta= 0℃, 300h	
High Temperature Storage	Ta= 60℃, 300h	
Low Temperature Storage	Ta= -20℃, 300h	
Thermal Shock Test	Ta=-20℃ to 60℃, Duration at 30 min, 100 cycles	
ESD	Contact : ±8 KV Air : ±15 KV	Note 1

Note1: According to EN 61000-4-2 , ESD class B: Some performance degradation allowed. No data lost

.   Self-recoverable. No hardware failures.

Remark: MTBF (Excluding the LED): 30,000 hours with a confidence level 90%

11. Mechanical Characteristics

11.1 LCM Outline Dimension
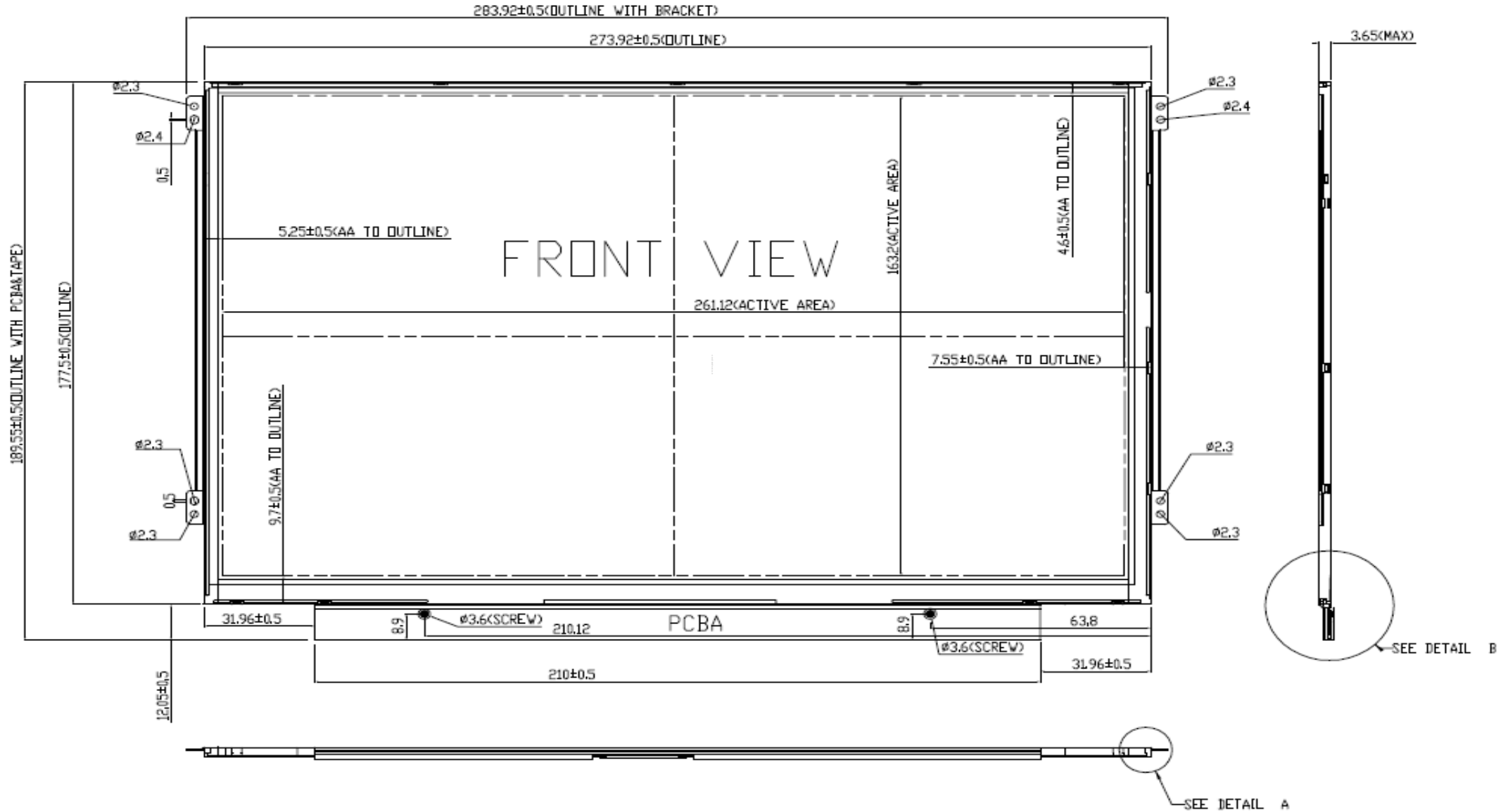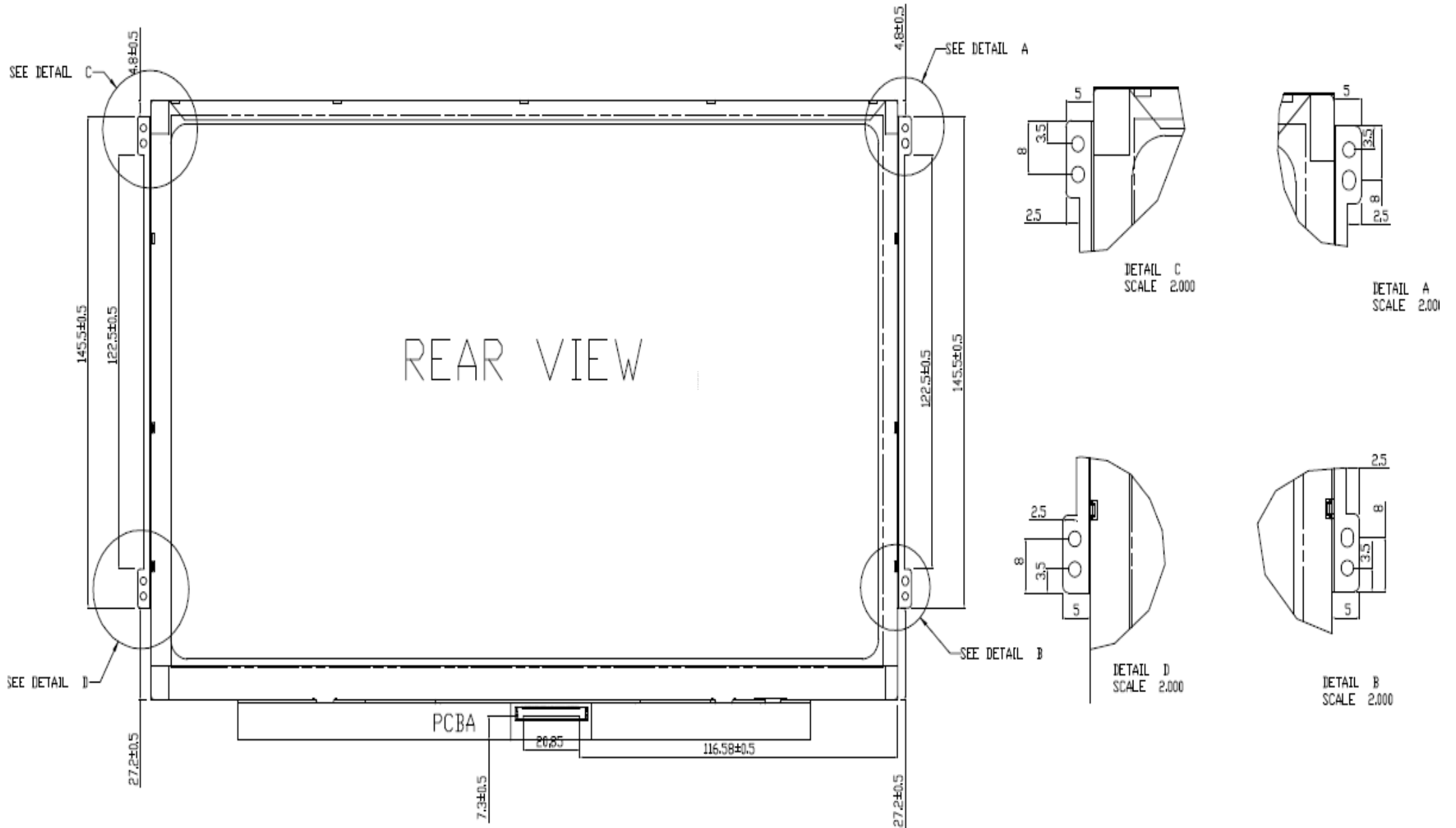
12. Shipping and Package

12.1 Shipping Label Format

Manufactured xx/xx
Model No: B121EW10 V2
AU Optronics   0AXXG
MADE IN TAIWAN (M1)

H/W: 0A F/W:1

* XXXXXXXXXXXX-XXXXXX

AU Optronics   QTY : 35
MODEL NO : B121EW10 V2
PART NO : 97.12B10.200
CUSTOMER NO :
CARTON NO :
Made in Taiwan   *ZM100-0652300205*

12.2 Carton package

The outside dimension of carton is 455 (L)mm x 380 (W)mm x 355 (H)mm
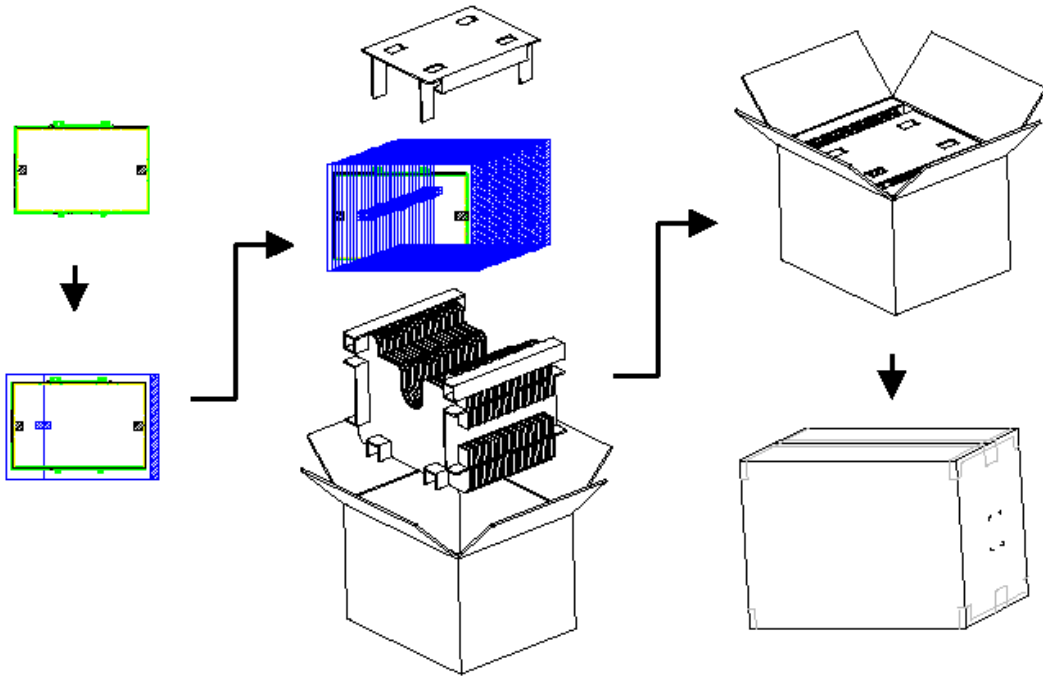
12.3 Shipping package of palletizing sequence

13. Appendix: EDID description

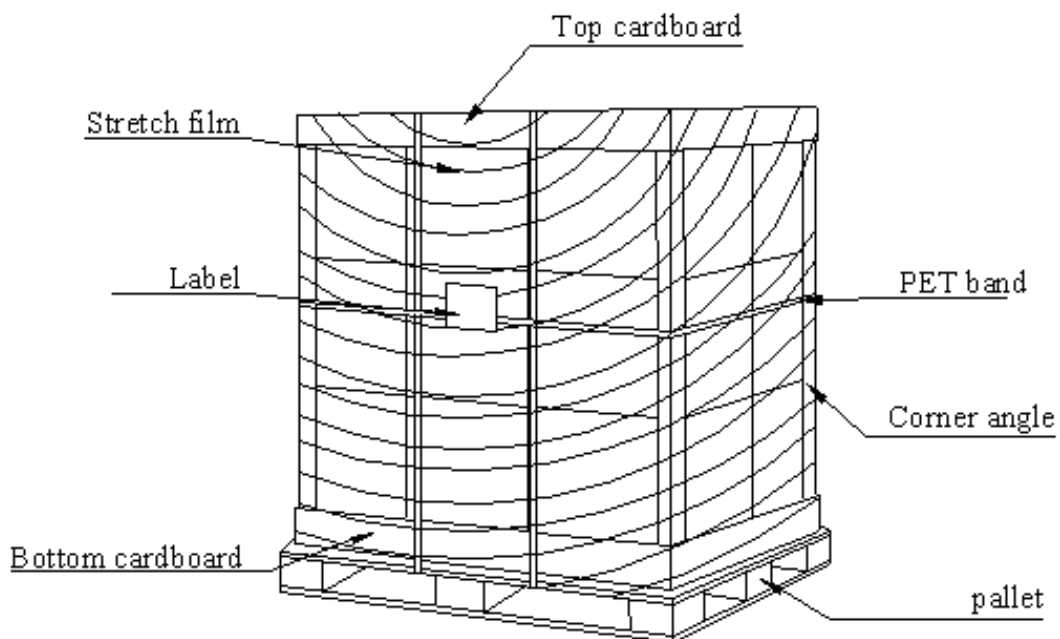| Address	FUNCTION	Value	Value	Value	Note
HEX		HEX	BIN	DEC	
00	Header	00	00000000	0	
01		FF	11111111	255	
02		FF	11111111	255	
03		FF	11111111	255	
04		FF	11111111	255	
05		FF	11111111	255	
06		FF	11111111	255	
07		00	00000000	0	
08	EISA Manuf. Code LSB	06	00000110	6	
09	Compressed ASCII	AF	10101111	175	
0A	Product Code	14	00010100	20	
0B	hex, LSB first	A2	10100010	162	
0C	32-bit ser #	00	00000000	0	
0D		00	00000000	0	
0E		00	00000000	0	
0F		00	00000000	0	
10	Week of manufacture	01	00000001	1	
11	Year of manufacture	12	00010010	18	
12	EDID Structure Ver.	01	00000001	1	
13	EDID revision #	03	00000011	3	
14	**Video input def.** (digital I/P, non-TMDS, CRGB)	80	10000000	128	
15	**Max H image size** (rounded to cm)	1A	00011010	26	
16	**Max V image size** (rounded to cm)	10	00010000	16	
17	**Display Gamma** (=(gamma*100)-100)	78	01111000	120	
18	**Feature support** (no DPMS, Active OFF, RGB, tmg Blk#1)	0A	00001010	10	
19	Red/green low bits **(Lower 2:2:2:2 bits)**	89	10001001	137	
1A	Blue/white low bits **(Lower 2:2:2:2 bits)**	E5	11100101	229	
1B	Red x **(Upper 8 bits)**	94	10010100	148	
1C	Red y/ highER 8 bits	57	01010111	87	
1D	Green x	54	01010100	84	
1E	Green y	93	10010011	147	
1F	Blue x	27	00100111	39	
20	Blue y	22	00100010	34	
21	White x	50	01010000	80	
22	White y	54	01010100	84	
23	Established timing 1	00	00000000	0	
24	Established timing 2	00	00000000	0	
25	Established timing 3	00	00000000	0	
26	Standard timing #1	01	00000001	1	
27		01	00000001	1	
28	Standard timing #2	01	00000001	1	
29		01	00000001	1	
2A	Standard timing #3	01	00000001	1	

Addr	Description	Hex	Binary	Dec	
2B		01	00000001	1	
2C	Standard timing #4	01	00000001	1	
2D		01	00000001	1	
2E	Standard timing #5	01	00000001	1	
2F		01	00000001	1	
30	Standard timing #6	01	00000001	1	
31		01	00000001	1	
32	Standard timing #7	01	00000001	1	
33		01	00000001	1	
34	Standard timing #8	01	00000001	1	
35		01	00000001	1	
36	Pixel Clock/10000 LSB	EA	11101010	234	
37	Pixel Clock/10000 USB	1A	00011010	26	
38	Horz active **Lower 8bits**	00	00000000	0	
39	Horz blanking **Lower 8bits**	7E	01111110	126	
3A	HorzAct:HorzBlnk **Upper 4:4 bits**	50	01010000	80	
3B	Vertical Active **Lower 8bits**	20	00100000	32	
3C	Vertical Blanking **Lower 8bits**	10	00010000	16	
3D	Vert Act : Vertical Blanking **(upper 4:4 bit)**	30	00110000	48	
3E	HorzSync. Offset	30	00110000	48	
3F	HorzSync.Width	20	00100000	32	
40	VertSync.Offset : VertSync.Width	36	00110110	54	
41	Horz&Vert Sync Offset/Width **Upper 2bits**	00	00000000	0	
42	Horizontal Image Size **Lower 8bits**	05	00000101	5	
43	Vertical Image Size **Lower 8bits**	A3	10100011	163	
44	Horizontal & Vertical Image Size **(upper 4:4 bits)**	10	00010000	16	
45	Horizontal Border *(zero for internal LCD)*	00	00000000	0	
46	Vertical Border *(zero for internal LCD)*	00	00000000	0	
47	Signal *(non-intr, norm, no stero, sep sync, neg pol)*	18	00011000	24	
48	Detailed timing/monitor	00	00000000	0	
49	descriptor #2	00	00000000	0	
4A		00	00000000	0	
4B		0F	00001111	15	
4C		00	00000000	0	
4D		00	00000000	0	
4E		00	00000000	0	
4F		00	00000000	0	
50		00	00000000	0	
51		00	00000000	0	
52		00	00000000	0	
53		00	00000000	0	
54		00	00000000	0	
55		00	00000000	0	
56		00	00000000	0	
57		00	00000000	0	
58		00	00000000	0	
59		20	00100000	32	

5A	Detailed timing/monitor	00	00000000	0	
5B	descriptor #3	00	00000000	0	
5C		00	00000000	0	
5D		FE	11111110	254	
5E		00	00000000	0	
5F	Manufacture	41	01000001	65	A
60	Manufacture	55	01010101	85	U
61	Manufacture	4F	01001111	79	O
62		0A	00001010	10	
63		20	00100000	32	
64		20	00100000	32	
65		20	00100000	32	
66		20	00100000	32	
67		20	00100000	32	
68		20	00100000	32	
69		20	00100000	32	
6A		20	00100000	32	
6B		20	00100000	32	
6C	Detailed timing/monitor	00	00000000	0	
6D	descriptor #4	00	00000000	0	
6E		00	00000000	0	
6F		FE	11111110	254	
70		00	00000000	0	
71	Manufacture P/N	42	01000010	66	B
72	Manufacture P/N	31	00110001	49	1
73	Manufacture P/N	32	00110010	50	2
74	Manufacture P/N	31	00110001	49	1
75	Manufacture P/N	45	01000101	69	E
76	Manufacture P/N	57	01010111	87	W
77	Manufacture P/N	31	00110001	49	1
78	Manufacture P/N	30	00110000	48	0
79	Manufacture P/N	20	00100000	32	
7A	Manufacture P/N	56	01010110	86	V
7B	Manufacture P/N	32	00110010	50	2
7C		20	00100000	32	
7D		0A	00001010	10	
7E	Extension Flag	00	00000000	0	
7F	Checksum	B4	10110100	180	