KEMET
CHARGED.

Tantalum Chip Capacitors

Multiple Anode, Ultra-low ESR

Series/Type: **B45396R**
Date: July 2006

RoHS compatible

Construction

- Polar tantalum capacitors with solid electrolyte
- Conventional $TaMnO_2$ technology
- Three parallel low-ESR tantalum capacitors in one case
- Flame-retardant plastic case (UL 94 V-0)
- Tinned terminals

Features

- High volumetric efficiency
- Ultra-low ESR values
- High ripple current capability
- Excellent solderability
- Stable temperature and frequency characteristics
- Low leakage current, low dissipation factor
- Low self-inductance
- High resistance to shock and vibration
- Suitable for use without series resistor (special operating conditions recommended)
- Lead-free and material content compatible with RoHS

Applications

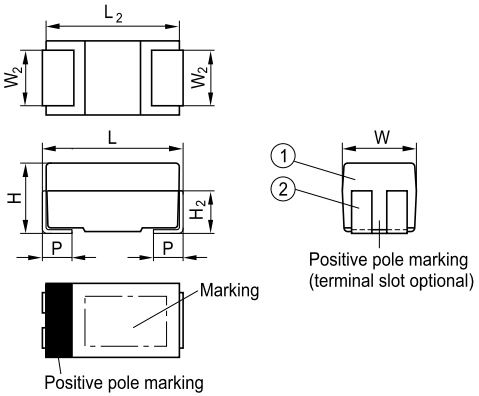
- Telecommunications (e.g. mobile phones, infrastructure)
- Data processing (e.g. laptops, mainframes)
- Measuring and control engineering (e.g. voltage regulators)
- Medical engineering
- DC/DC converters

Soldering

Suitable for reflow soldering (IR and vapor phase, in compliance with JEDEC J-STD-020 C) and wave soldering

Delivery mode

Taped and reeled in accordance with IEC 60286-3



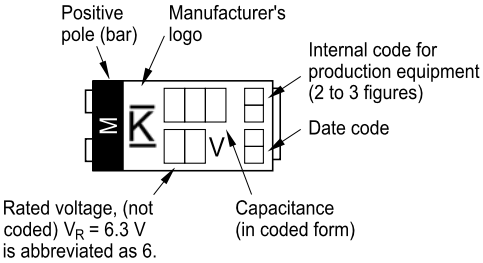
① Encapsulation: molded epoxy resin
② NiFe; tinned surface Sn100

Dimensions

Case size		Dimensions in mm (inches)						
KEMET	EIA/IECQ	L	W	H	L_2 typ.	W_2 ±0.1 ±(.004)	H_2 typ.	P ±0.3 ±(.012)
E	7343-44	7.3 ±0.3 (.287 ±0.012)	4.3 ±0.3 (.169 ±0.012)	4.1 ±0.3 (.161 ±0.012)	7.3 (.287)	2.4 (.094)	1.8 (.071)	1.3 (.051)

Marking

Case size E



Capacitance coding

1st and 2nd digit	Capacitance in pF	
3rd digit	Multiplier:	6 = 10^6 pF 7 = 10^7 pF 8 = 10^8 pF

Date coding

Year	Month	
S = 2004	1 = January	7 = July
T = 2005	2 = February	8 = August
U = 2006	3 = March	9 = September
V = 2007	4 = April	O = October
W = 2008	5 = May	N = November
X = 2009	6 = June	D = December

Specifications and characteristics in brief

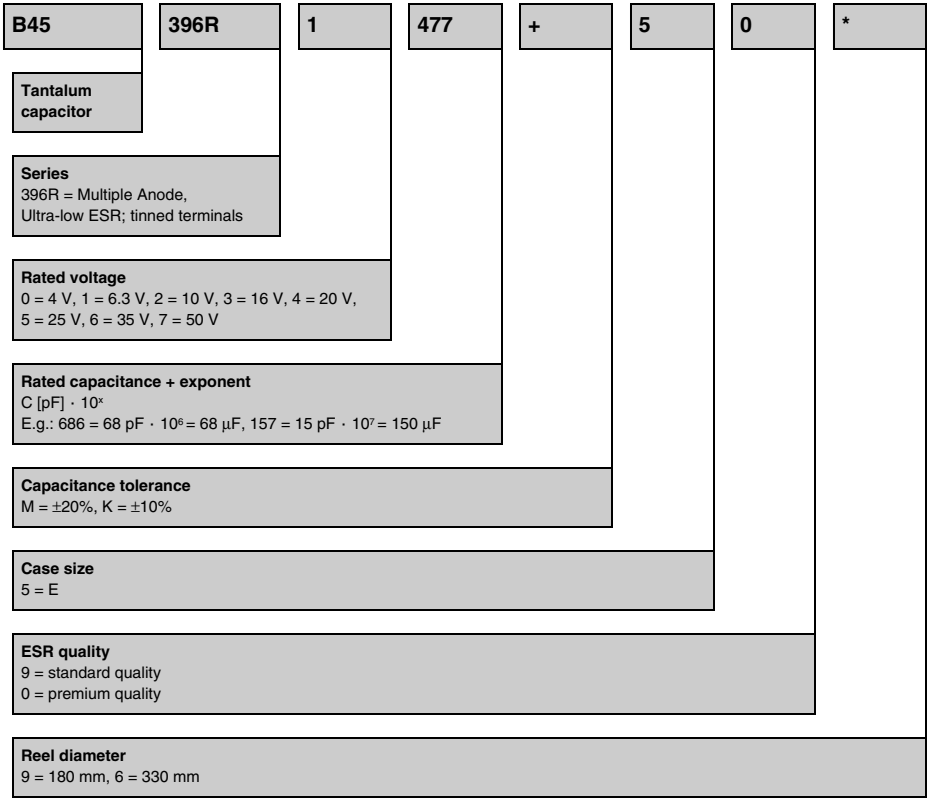
Series			Multiple Anode	
Ordering code			B45396R	
Technology			Ta-MnO_2	
Terminals			Tinned	
Rated voltage	(up to 85 °C)	V_R	4 ... 50	VDC
Rated capacitance	(20 °C, 120 Hz)	C_R	10 ... 1000	µF
Capacitance tolerance			±20%, ±10%	
Maximum equivalent series resistance	(20 °C, 100 kHz)	ESR_{max}	30 ... 150	mΩ
Operating temperature range		T_{op}	−55 ... +125	°C
Failure rate (1 fit = $1 \cdot 10^{-9}$ failures/h) $C_R \cdot V_R > 330\ \mu F \cdot V$	(at 40 °C; ≤ V_R, $R_S \leq 0.1\ \Omega/V$)		≤288	fit
Service life			>500000	h
Leakage current	(V_R, 5 min, 20 °C)	I_{leak}	10	nA/µC
Detail specification	(tinned terminals)		CECC 30801-805	
Climatic category (to IEC 60068-1)	(−55 °C/+125 °C/56 days damp heat test)		55/125/56	

Overview of types

(VDC) (up to 85 °C)	4	6.3	10	16	20	25	35	50
C_R (µF)								
10								E
15								E
22							E	E
33							E	
47						E	E	
68					E	E		
100					E			
150				E				
220				E				
330			E					
470		E	E					
680	E	E						
1000	E							

Upon request

Ordering code structure



Technical data and ordering codes

C_R (20 °C, 120 Hz) µF	Case size	DF_{max} (20 °C, 120 Hz) %	$I_{leak,max}$ (20 °C, V_R, 5 min) µA	ESR_{max} [1] (20 °C, 100 kHz) mΩ	$I_{AC,max}$ (20 °C, 100 kHz) A	Ordering code
V_R (up to 85 °C) = 4 VDC, V_R (up to 125 °C) = 2.5 VDC						
680	E	6	27	30	3.0	B45396R0687+50*
680	E	6	27	35	2.8	B45396R0687+59*
1000	E	6	40	30	3.0	B45396R0108+50*
1000	E	6	40	35	2.8	B45396R0108+59*
V_R (up to 85 °C) = 6.3 VDC, V_R (up to 125 °C) = 4 VDC						
470	E	6	30	30	3.0	B45396R1477+50*
470	E	6	30	35	2.8	B45396R1477+59*
680	E	6	43	30	3.0	B45396R1687+50* ●
680	E	6	43	35	2.8	B45396R1687+59* ●
V_R (up to 85 °C) = 10 VDC, V_R (up to 125 °C) = 6.3 VDC						
330	E	6	33	35	2.8	B45396R2337+50*
330	E	6	33	40	2.6	B45396R2337+59*
470	E	6	47	35	2.8	B45396R2477+50* ●
470	E	6	47	40	2.6	B45396R2477+59* ●
V_R (up to 85 °C) = 16 VDC, V_R (up to 125 °C) = 10 VDC						
150	E	6	24	40	2.6	B45396R3157+50*
150	E	6	24	45	2.4	B45396R3157+59*
220	E	6	35	40	2.6	B45396R3227+50*
220	E	6	35	45	2.4	B45396R3227+59*
V_R (up to 85 °C) = 20 VDC, V_R (up to 125 °C) = 13 VDC						
68	E	6	14	45	2.4	B45396R4686+50*
68	E	6	14	55	2.2	B45396R4686+59*
100	E	6	20	45	2.4	B45396R4107+50*
100	E	6	20	55	2.2	B45396R4107+59*
V_R (up to 85 °C) = 25 VDC, V_R (up to 125 °C) = 16 VDC						
47	E	6	12	45	2.4	B45396R5476+50* ●
47	E	6	12	55	2.2	B45396R5476+59* ●
68	E	6	17	45	2.4	B45396R5686+50* ●
68	E	6	17	55	2.2	B45396R5686+59* ●
V_R (up to 85 °C) = 35 VDC, V_R (up to 125 °C) = 23 VDC						

● Preliminary Data

* = Code number for reel diameter
6 = 330-mm reel
9 = 180-mm reel

\+ = Capacitance tolerance
M = ±20%
K = ±10%

1) Other values upon request

C_R (20 °C, 120 Hz) µF	Case size	DF_{max} (20 °C, 120 Hz) %	$I_{leak,max}$ (20 °C, V_R, 5 min) µA	ESR_{max} [1] (20 °C, 100 kHz) mΩ	$I_{AC,max}$ (20 °C, 100 kHz) A	Ordering code
22	E	6	8	60	2.1	B45396R6226+50* ●
22	E	6	8	100	1.6	B45396R6226+59* ●
33	E	6	12	50	2.3	B45396R6336+50* ●
33	E	6	12	65	2.0	B45396R6336+59* ●
47	E	6	16	55	2.2	B45396R6476+50* ●
47	E	6	16	65	2.0	B45396R6476+59* ●
V_R (up to 85 °C) = 50 VDC, V_R (up to 125 °C) = 33 VDC						
10	E	6	5	120	1.5	B45396R7106+50* ●
10	E	6	5	150	1.3	B45396R7106+59* ●
15	E	6	8	110	1.6	B45396R7156+50* ●
15	E	6	8	140	1.4	B45396R7156+59* ●
22	E	6	11	110	1.6	B45396R7226+50* ●
22	E	6	11	140	1.4	B45396R7226+59* ●

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Cautions and warnings

When using tantalum capacitors with a manganese dioxide coating, the following cautions and warnings should be taken into account:

Polarity

Because tantalum capacitors are *polar capacitors,* it is important to observe their polarity markings (positive pole on the anode, negative pole on the cathode). Any incorrect polarity resulting from the sum of the AC and DC voltage components must be smaller than or equal to the permitted *polarity reversal voltage.* To avoid reducing their reliability, this voltage may only occur for a short time, at most five times for a duration of one minute per hour.

Voltage

The *maximum continuous voltage* depends on the ambient temperature. Within the temperature range of −55 to +85 °C, the rated voltage is equal to the maximum continuous voltage. Between + 85 and + 125 °C the maximum continuous voltage must be reduced linearly from the full rated voltage to 2/3 of it (derating). At 150 °C (only B4519*P series) the voltage must be reduced to 1/2 and at 175 °C (only B4519*T series) to 1/3 of the rated voltage. Operation below the maximum continuous voltage has a positive effect on the capacitor's failure rate. The maximum continuous voltage must not be exceeded.

All unfavourable operating conditions (such as possible line overvoltages, unfavourable tolerances of the transformation ratio of the line transformer in the equipment, repeated overvoltages when the equipment is switched on, high ambient temperatures) must be taken into account when determining the *operating voltage*.

The *surge voltage* is the maximum voltage (peak value) that may be applied to the capacitor for short periods, at most five times for a total duration of up to 1 minute per hour. The surge voltage must not be applied for periodic charging and discharging in the course of normal operation and cannot be part of the operating voltage. The permissible surge voltage for all capacitors in this data book is 1.3 x the rated voltage. The occurrence of voltage impulses (transient voltages) that exceed the surge voltage may lead to irreparable damage.

Capacitance

The actual *capacitance* of a capacitor can deviate from the rated capacitance by as much as the full magnitude of the tolerance at delivery. The capacitance varies with the temperature (at +125 °C +12% up to +20%) and decreases with increasing frequency.

Low-resistance applications and voltage networks

For *low-resistance applications*, KEMET recommends a maximum operating voltage of half the permissible maximum continuous voltage, so that the capacitors have sufficient tolerance to withstand voltage peaks. Depending on the conditions of use, the early failure rate is higher here by a factor of 2 to 20 than in the range with a constant failure rate as specified in the data book.

When operated directly in a *voltage network*, the capacitor should be protected against overvoltage, e.g. by a suppressor diode, and against polarity reversal by a diode. If a capacitor is operated in an unprotected low-impedance circuit and fails because the permissible conditions for the forward DC voltage, reverse DC voltage, surge current, power dissipation or temperature are exceeded, the continued current flow through the overstressed capacitor may produce overheating. The overheated capacitor may damage the surrounding components and the circuit board.

Storage conditions

Capacitors with solid electrolyte may be stored at temperatures down to –80 °C. The upper *storage temperature* must not exceed +85 °C (for blister tape the temperature limit is +40 °C). The storage conditions should not exceed +40 °C and 70% R. H. A drypack is useful to prevent the effects of humidity.

Important notes

The following applies to all products named in this publication:

1. Some parts of this publication contain **statements about the suitability of our products for certain areas of application**. These statements are based on our knowledge of typical requirements that are often placed on our products in the areas of application concerned. We nevertheless expressly point out **that such statements cannot be regarded as binding statements about the suitability of our products for a particular customer application**.
   As a rule, KEMET is either unfamiliar with individual customer applications or less familiar with them than the customers themselves. For these reasons, it is always ultimately incumbent on the customer to check and decide whether a KEMET product with the properties described in the product specification is suitable for use in a particular customer application.
2. We also point out that **in individual cases, a malfunction of passive electronic components or failure before the end of their usual service life cannot be completely ruled out in the current state of the art, even if they are operated as specified**. In customer applications requiring a very high level of operational safety and especially in customer applications in which the malfunction or failure of a passive electronic component could endanger human life or health (e.g. in accident prevention or life-saving systems), it must therefore be ensured by means of suitable design of the customer application or other action taken by the customer (e.g. installation of protective circuitry or redundancy) that no injury or damage is sustained by third parties in the event of malfunction or failure of a passive electronic component.
3. **The warnings, cautions and product-specific notes must be observed**.
4. In order to satisfy certain technical requirements, **some of the products described in this publication may contain substances subject to restrictions in certain jurisdictions (e.g. because they are classed as "hazardous")**. Should you have any more detailed questions please contact our sales offices.
5. We constantly strive to improve our products. Consequently, **the products described in this publication may change from time to time**. The same is true of the corresponding product specifications. Please check therefore to what extent product descriptions and specifications contained in this publication are still applicable before or when you place an order.
   We also **reserve the right to discontinue production and delivery of products**. Consequently, we cannot guarantee that all products named in this publication will always be available.
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