

●Absolute Maximum Ratings (Ta = 25°C)

Parameter	Symbol	Limits	Unit
Power supply voltage	V _{CC}	18 ^{*1}	V
Power dissipation	P _d	1200 ^{*2}	mW
		1300 ^{*3}	
		2000 ^{*4}	
		2000 ^{*4}	
		2000 ^{*4}	
		2000 ^{*4}	
Operating temperature range	T _{opr}	-40 to +105	°C
Ambient storage temperature	T _{stg}	-55 to +150	°C
Maximum junction temperature	T _{jmax}	150	°C

*1 Must not exceed P_d.

*2 Derated at 9.6mW/°C at Ta>25°C when mounted on a glass epoxy board (70 mm × 70 mm × 1.6 mm).

*3 Derated at 10.4mW/°C at Ta>25°C when mounted on a glass epoxy board (70 mm × 70 mm × 1.6 mm).

*4 Derated at 16mW/°C at Ta > 25°C

●Recommended Operating Conditions

Parameter	Symbol	Min.	Max.	Unit
Input power supply voltage	V _{CC} ^{*5}	3.0	16.0	V
Input power supply voltage	V _{CC} ^{*6}	V _o +1.0	16.0	V
Output current	I _o	-	1	A
Variable output voltage setting value	V _o	1.5	12	V

*5 When output voltage is 1.5 V, 1.8 V, or 2.5 V.

*6 When output voltage is 3.0 V or higher.

●Electrical Characteristics

BA□□BC0FP/T/WFP/WT (-V5)

(Unless otherwise specified, Ta = 25°C; V_{CTL} = 3 V; V_{CCDC}^{*7})

Parameter	Symbol	Min.	Typ.	Max.	Unit	Conditions
Output voltage	V _o	V _o (T) × 0.98	V _o (T)	V _o (T) × 1.02	V	I _o = 200mA
Shutdown circuit current	I _{sd}	-	0	10	μA	V _{CTL} = 0 V while in off mode
Minimum I/O voltage difference ^{*8}	ΔV _d	-	0.3	0.5	V	I _o = 200 mA, V _{CC} = 0.95 × V _o
Output current capacity	I _o	1	-	-	A	
Input stability ^{*9}	Reg.I	-	15	35	mV	V _{CC} = V _o +1.0V→16V, I _o = 200mA
Load stability	Reg.L	-	35	75	mV	I _o = 0 mA →1 A
Temperature coefficient of output voltage ^{*10}	T _{cvo}	-	±0.02	-	%/°C	I _o = 5 mA, T _j = 0°C to 125°C

V_o (T): Set output voltage

*7 V_o = 1.5 V, 1.8 V, 2.5 V : V_{CC} = 3.3 V, V_o = 3.0 V, 3.3 V : V_{CC} = 5 V,

V_o = 5.0 V : V_{CC} = 8 V, V_o = 6.0 V : V_{CC} = 9 V, V_o = 8.0 V : V_{CC} = 11 V,

V_o = 9.0 V : V_{CC} = 12 V, V_o = 10.0 V : V_{CC} = 13 V

*8 V_o ≥ 3.3 V

*9 Change V_{CC} from 3.0 V to 6 V if 1.5 V ≤ V_o ≤ 2.5 V.

*10 Operation guaranteed

BA00BC0WFP/WT (-V5)/CP-V5

(Unless otherwise specified, Ta = 25°C, V_{CC} = 3.3 V, V_{CTL} = 3 V, R1 = 30 kΩ, R2 = 30 kΩ^{*11})

Parameter	Symbol	Min.	Typ.	Max.	Unit	Conditions
Shutdown circuit current	I _{sd}	-	0	10	μA	V _{CTL} = 0 V while in OFF mode
Reference voltage	V _c	1.225	1.250	1.275	V	I _o = 50 mA
Minimum I/O voltage difference	ΔV _d	-	0.3	0.5	V	I _o = 500 mA, V _{CC} = 2.5V
Output current capacity	I _o	1	-	-	A	
Input stability	Reg.I	-	15	30	mV	V _{CC} = V _o + 1.0 V→16V, I _o = 200 mA
Load stability	Reg.L	-	35	75	mV	I _o = 0 mA →1A
Temperature coefficient of output voltage ^{*12}	T _{cvo}	-	±0.02	-	%/°C	I _o = 5mA, T _j =0°C to 125°C

*11 V_{OUT} = V_c × (R1 + R2) / R1 (V)

*12 Operation guaranteed

●Electrical Characteristics Curves (Unless otherwise specified, Ta = 25°C, Vcc = 8 V, Vctl = 2 V, Io = 0 mA)

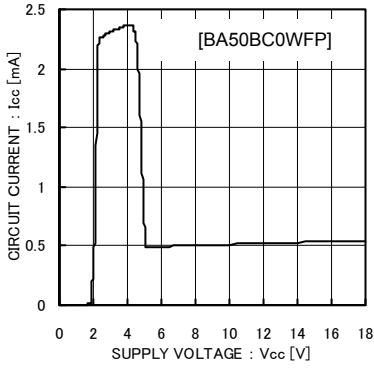


Fig.1 Circuit Current

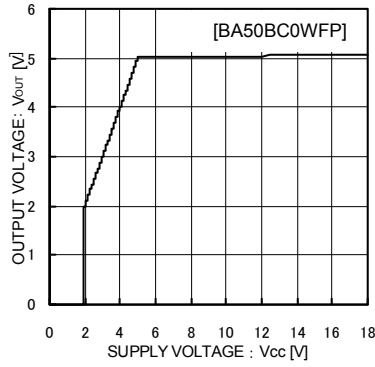


Fig.2 Input Stability(Io=0mA)

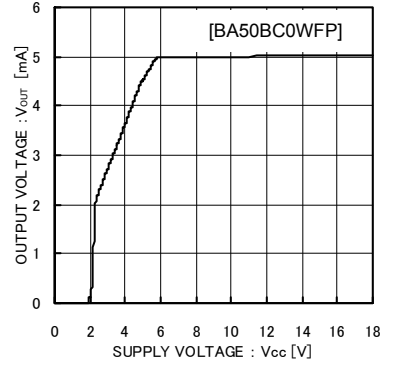


Fig.3 Input Stability(Io = 1 A)

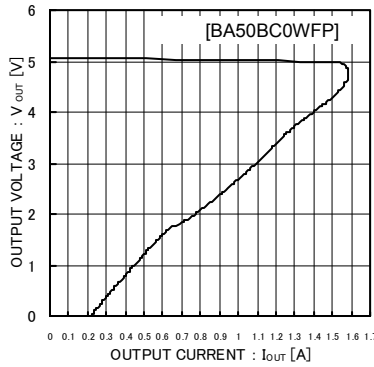


Fig.4 Load Stability

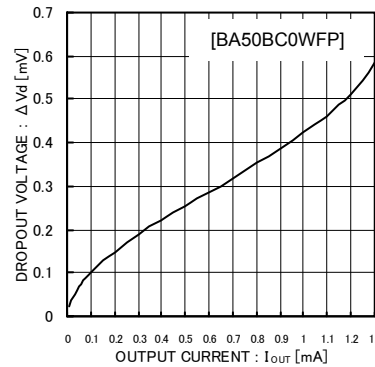


Fig.5 I/O Voltage Difference

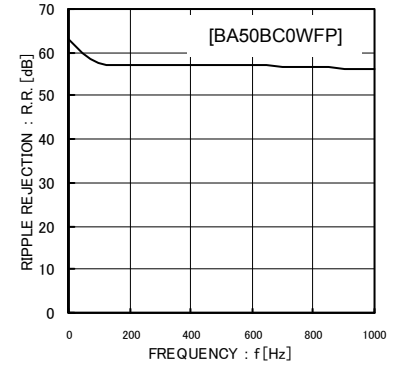


Fig.6 Ripple Rejection

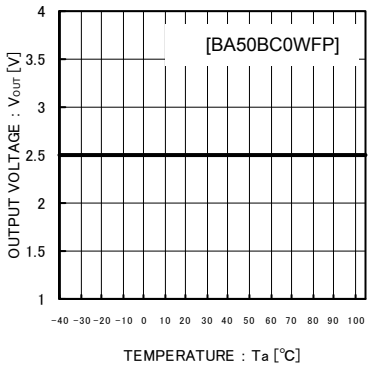


Fig.7 Output Voltage vs Temperature

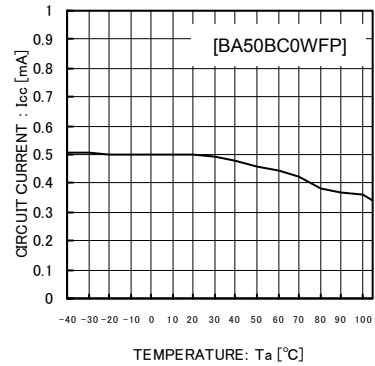


Fig.8 Circuit Current Temperature

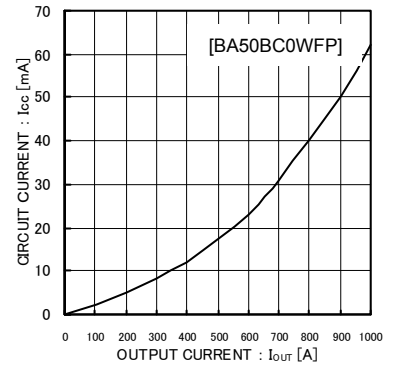


Fig.9 Circuit Current Classified by Load

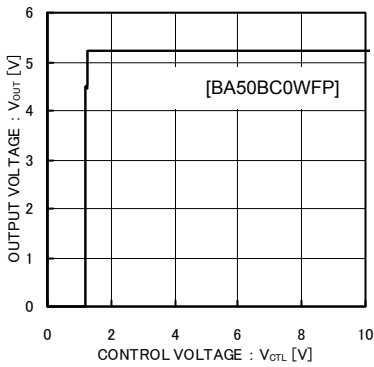


Fig.10 CTL Voltage vs Output Voltage

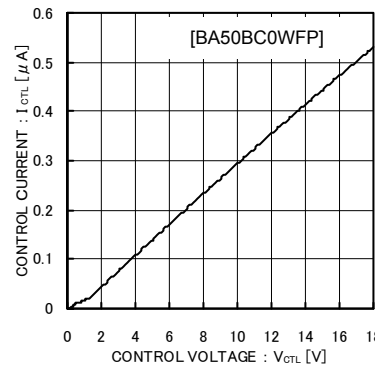


Fig.11 CTL Voltage vs CTL Current

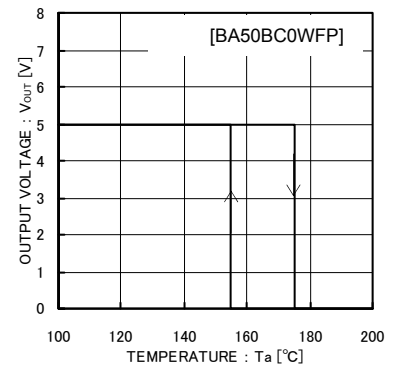


Fig.12 Thermal Shutdown Circuit

●Block Diagrams / Standard Example Application Circuits

[BA□□BC0T] / [BA□□BC0FP]

Fig. 13

TOP VIEW

Pin No.	Pin name	Function
1	Vcc	Supply voltage input
2	N.C./GND	NC pin/GND *1
3	OUT	Voltage output
FIN	GND	GND*2

*1 NC pin for TO252-3 and GND pin for TO220FP-3 and TO220FP-5 (V5).
*2 TO252-3 only.

PIN	External capacitor setting range
Vcc (1 Pin)	Approximately 0.33 µF.
OUT (3 Pin)	22 µF to 1000 µF

[BA□□BC0WT] / [BA□□BC0WT-V5] / [BA□□BC0WFP]

Fig. 14

TOP VIEW

Pin No.	Pin name	Function
1	CTL	Output voltage on/off control
2	Vcc	Supply voltage input
3	N.C./GND	NC pin/GND*1
4	OUT	Power supply output
5	N.C.	NC pin
FIN	GND	GND*2

*1 NC pin for TO252-5 and GND pin for TO220FP-5 and TO220FP-5 (V5).
*2 TO252-5 only.

PIN	External capacitor setting range
Vcc (2 Pin)	Approximately 0.33 µF.
OUT (4 Pin)	22 µF to 1000 µF

[BA00BC0WT] / [BA00BC0WCP-V5] / [BA00BC0WFP] / [BA00BC0WT-V5]

Fig. 15

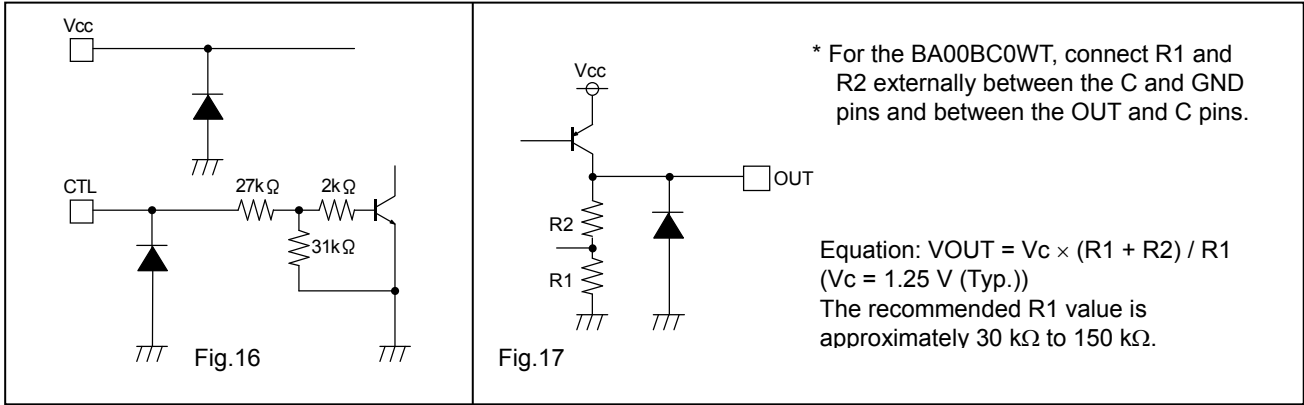
TOP VIEW

Pin No.	Pin name	Function
1	CTL	Output voltage on/off control
2	Vcc	Supply voltage input
3	N.C./GND	NC pin/GND*1
4	OUT	Power supply output
5	C	ADJ pin
FIN	GND	GND*2

*1 NC pin for TO252-5 and GND pin for TO220FP-5 and TO220FP-5 (V5).
*2 TO252-5 only.

PIN	External capacitor setting range
Vcc (2 Pin)	Approximately 0.33 µF.
OUT (4 Pin)	22 µF to 1000 µF

●Input / Output Equivalent Circuit Diagrams



●Thermal Derating Curves

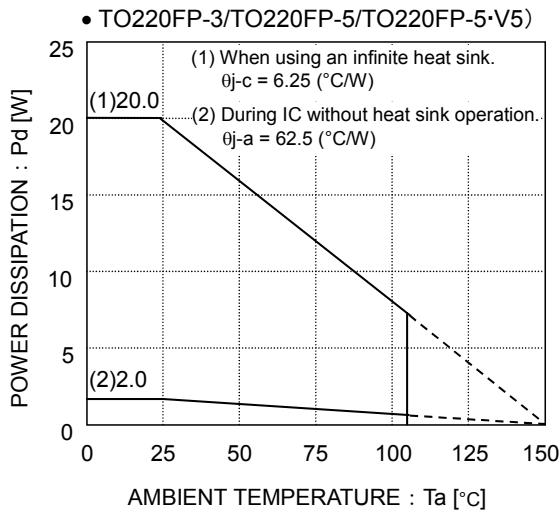


Fig. 18

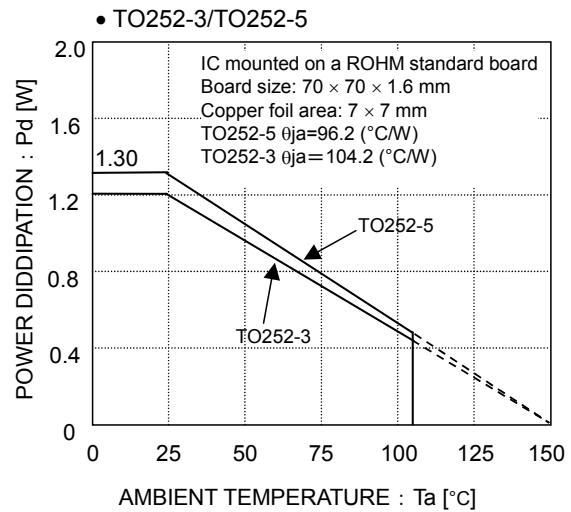


Fig. 19

The characteristics of the IC are greatly influenced by the operating temperature. If the temperature exceeds the maximum junction temperature T_{jmax} , deterioration or damage may occur. Implement proper thermal designs to ensure that power dissipation is within the permissible range in order to prevent instantaneous damage resulting from heat and maintain the reliability of the IC for long-term operation.

The following method is used to calculate the power consumption P_c (W).

$$P_c = (V_{cc} - V_o) \times I_o + V_{cc} \times I_{cca}$$

Power dissipation $P_d \geq P_c$

V_{cc} : Input voltage
 V_o : Output current
 I_o : Load current
 I_{cca} : Circuit current

The load current I_o is calculated:

$$I_o \leq \frac{P_d - V_{cc} \times I_{cca}}{V_{cc} - V_o}$$

Calculation Example:

$V_{cc} = 6.0 \text{ V}$ and $V_o = 5.0 \text{ V}$ at $T_a = 85^\circ\text{C}$

$$\frac{0.676 - 6.0 \times I_{cca}}{6.0 - 5.0}$$

$$\left(\begin{array}{l} \theta_{ja} = 96.2^\circ\text{C/W} \rightarrow -10.4 \text{ mW/}^\circ\text{C} \\ 25^\circ\text{C} = 1300 \text{ mW} \rightarrow 85^\circ\text{C} = 676 \text{ mW} \end{array} \right)$$

$$I_o \leq 550 \text{ mA (} I_{cca} \approx 20 \text{ mA)}$$

Refer to the above and implement proper thermal designs so that the IC will not be used under excessive power dissipation conditions under the entire operating temperature range.

The power consumption P_c of the IC in the event of shorting (i.e. the V_o and GND pins are shorted) can be obtained from the following equation:

$$P_c = V_{cc} \times (I_{cca} + I_{short}) \text{ (} I_{short} \text{ : short current)}$$

●Notes for use

- Vcc pin
Insert a capacitor (0.33 μF approx.) between VCC and GND.
The capacitance will vary depending on the application. Use a suitable capacitance and implement designs with sufficient margins.
- GND pin
Verify that there is no potential difference between the ground of the application board and the IC.
If there is a potential difference, the set voltage will not be output accurately, resulting in unstable IC operation.
Therefore, lower the impedance by designing the ground pattern as wide and as short as possible.

• CTL pin

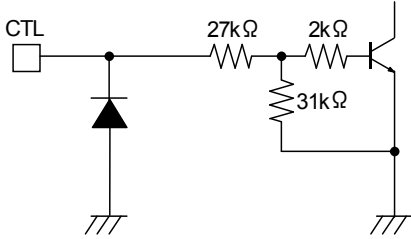


Fig.20 Input Equivalent Circuit

The CTL pin turns on at an operating power supply voltage of 2.0 V or higher and turns off at 0.8 V or lower. There is no particular order when turning the power supply and CTL pins on or off.

●Vo pin

Insert a capacitor between the Vo and GND pins in order to prevent output oscillation.

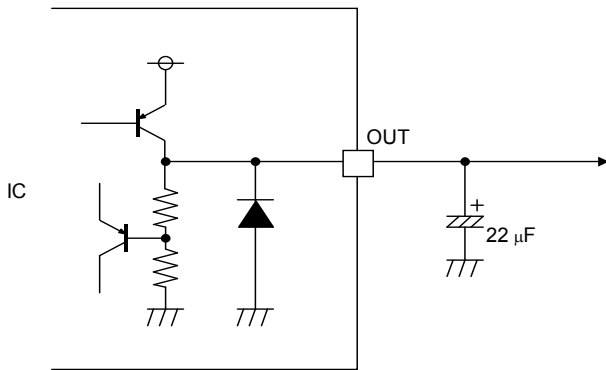


Fig.21 Output Equivalent Circuit

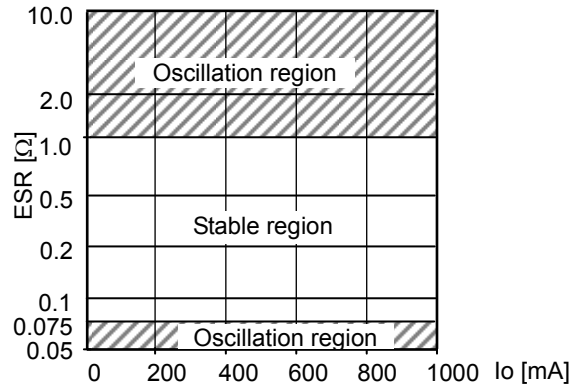


Fig.22 ESR vs IO(22 μ F)

The capacitance may vary greatly with temperature changes, thus making it impossible to completely prevent oscillation. Therefore, use a tantalum aluminum electrolytic capacitor with a low ESR (Equivalent Serial Resistance). The output will oscillate if the ESR is too high or too low, so refer to the ESR characteristics in Fig. 20 and operate the IC within the stable region. Use a capacitor within a capacitance between 22μF and 1,000μF.
Below figure, it is ESR-to-IO stability Area characteristics, measured by 22μ F -ceramic-capacitor and resistor connected in series. This characteristics is not equal value perfectly to 22μ F -aluminum electrolytic capacitor in order to measurement method. Note, however, that the stable range suggested in the figure depends on the IC and the resistance load involved, and can vary with the board's wiring impedance, input impedance, and/or load impedance. Therefore, be certain to ascertain the final status of these items for actual use.
Keep capacitor capacitance within a range of 22μF~1000μF. It is also recommended that a 0.33μF bypass capacitor be connected as close to the input pin-GND as location possible. However, in situations such as rapid fluctuation of the input voltage or the load, please check the operation in real application to determine proper capacitance.

●Notes for use

1. Absolute maximum ratings
An excess in the absolute maximum ratings, such as supply voltage, temperature range of operating conditions, etc., can break down the devices, thus making impossible to identify breaking mode, such as a short circuit or an open circuit. If any over rated values will expect to exceed the absolute maximum ratings, consider adding circuit protection devices, such as fuses.
2. GND voltage
The potential of GND pin must be minimum potential in all operating conditions.
3. Thermal design
Use a thermal design that allows for a sufficient margin in light of the power dissipation (Pd) in actual operating conditions.
4. Inter-pin shorts and mounting errors
Use caution when positioning the IC for mounting on printed circuit boards.
The IC may be damaged if there is any connection error or if pins are shorted together.
5. Actions in strong electromagnetic field
Use caution when using the IC in the presence of a strong electromagnetic field as doing so may cause the IC to malfunction.
6. Testing on application boards
When testing the IC on an application board, connecting a capacitor to a pin with low impedance subjects the IC to stress. Always discharge capacitors after each process or step. Always turn the IC's power supply off before connecting it to or removing it from a jig or fixture during the inspection process. Ground the IC during assembly steps as an antistatic measure. Use similar precaution when transporting or storing the IC.
7. Regarding input pin of the IC
This monolithic IC contains P+ isolation and P substrate layers between adjacent elements in order to keep them isolated. P-N junctions are formed at the intersection of these P layers with the N layers of other elements, creating a parasitic diode or transistor. For example, the relation between each potential is as follows:
When $GND > Pin A$ and $GND > Pin B$, the P-N junction operates as a parasitic diode.
When $GND > Pin B$, the P-N junction operates as a parasitic transistor.
Parasitic diodes can occur inevitable in the structure of the IC. The operation of parasitic diodes can result in mutual interference among circuits, operational faults, or physical damage. Accordingly, methods by which parasitic diodes operate, such as applying a voltage that is lower than the GND (P substrate) voltage to an input pin, should not be used.
8. Ground Wiring Pattern
When using both small signal and large current GND patterns, it is recommended to isolate the two ground patterns, placing a single ground point at the ground potential of application so that the pattern wiring resistance and voltage variations caused by large currents do not cause variations in the small signal ground voltage. Be careful not to change the GND wiring pattern of any external components, either.
9. Thermal shutdown circuit
The IC incorporates a built-in thermal shutdown circuit (TSD circuit). The thermal shutdown circuit (TSD circuit) is designed only to shut the IC off to prevent thermal runaway. It is not designed to protect the IC or guarantee its operation. Do not continue to use the IC after operating this circuit or use the IC in an environment where the operation of this circuit is assumed.
10. Overcurrent Protection Circuit
An overcurrent protection circuit is incorporated in order to prevention destruction due to short-time overload currents. Continued use of the protection circuits should be avoided. Please note that the current increases negatively impact the temperature.
11. Damage to the internal circuit or element may occur when the polarity of the Vcc pin is opposite to that of the other pins in applications. (I.e. Vcc is shorted with the GND pin while an external capacitor is charged.) Use a maximum capacitance of 1000μF for the output pins. Inserting a diode to prevent back-current flow in series with Vcc or bypass diodes between Vcc and each pin is recommended.

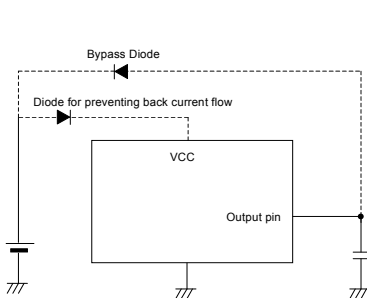


Fig.23 Bypass Diode

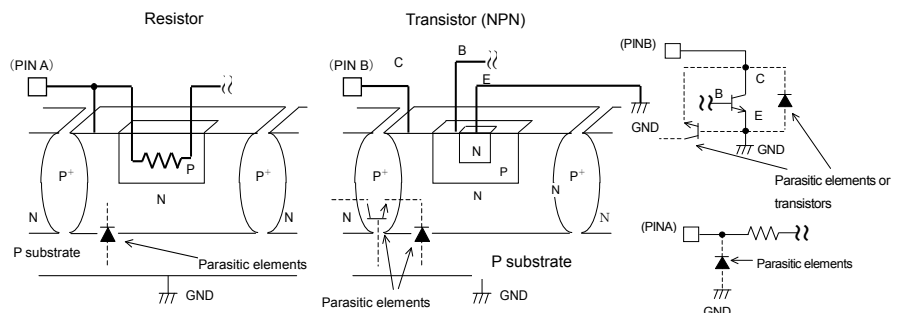
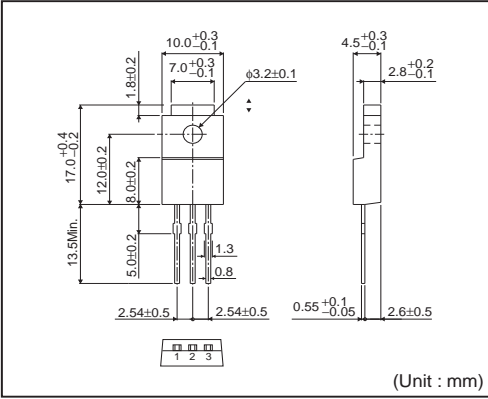


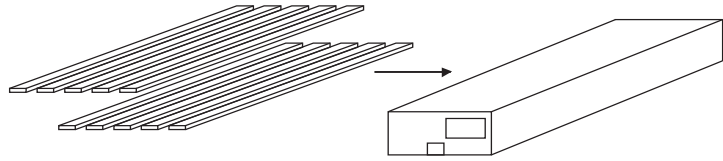
Fig.24 Example of Simple Bipolar IC Architecture

TO220FP-3



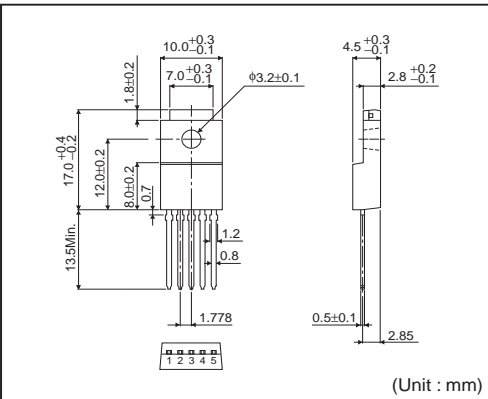
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Quantity	500pcs
Direction of feed	Direction of products is fixed in a container tube



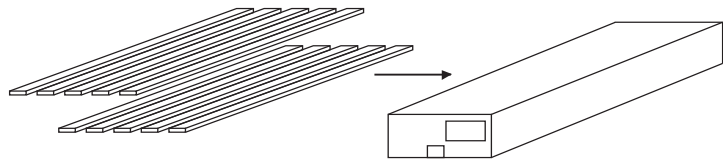
*Order quantity needs to be multiple of the minimum quantity.

TO220FP-5



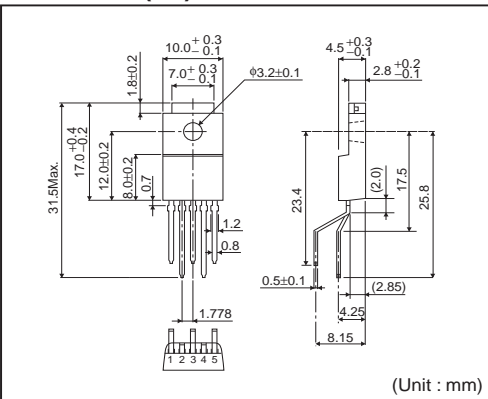
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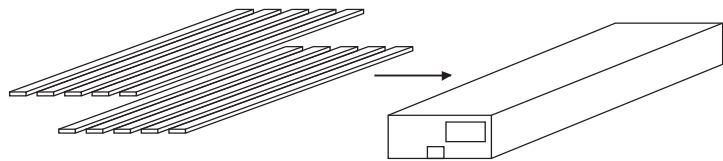
*Order quantity needs to be multiple of the minimum quantity.

TO220FP-5(V5)



<Tape and Reel information>

Container	Tube
Quantity	500pcs
Direction of feed	Direction of products is fixed in a container tube



*Order quantity needs to be multiple of the minimum quantity.

Notes

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