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KoHS

Comparator series

Automotive Ground Sense Comparators

BA2903Yxxx-C, BA2901Yxx-C

General Description

BA2903Yxxx-C/BA2901Yxx-C, integrate two or four independent high gain voltage comparator. Some features are the wide operating voltage that is 2V to 36V and low supply current. BA2903Yxxx-C, BA2901Yxx-C are manufactured for automotive requirements of engine control unit, electric power steering, antilock brake system, etc.

Features

- AEC-Q100 Qualified
- Single or dual supply operation
- Wide operating supply voltage
- Standard comparator Pin-assignments
- Common-mode Input Voltage Range includes ground level, allowing direct ground sensing
- Internal ESD protection circuit
- Wide temperature range

Application

- Engine Control Unit
- Electric Power Steering (EPS)
- Anti-Lock Braking System (ABS)
- Automotive electronics

Selection Guide

Key Specifications

Wide operating supply voltage	
single supply :	+2.0V to +36V
split supply :	±1.0V to ±18V
Very low supply current	
BA2903Yxxx-C	0.6mA(Typ.)
BA2901Yxx-C	0.8mA(Typ.)
Low input bias current :	50nA(Typ.)
Low input offset current :	5nA(Typ.)
Operating temperature range :	-40°C to +125°C

Packages
SOP8
SOP14
SSOP-B8
SSOP-B14

MSOP8

W(Typ.) x D(Typ.) x H(Max.) 5.00mm x 6.20mm x 1.71mm 8.70mm x 6.20mm x 1.71mm 3.00mm x 6.40mm x 1.35mm 5.00mm x 6.40mm x 1.35mm 2.90mm x 4.00mm x 0.90mm

Maximum Operating Temperature

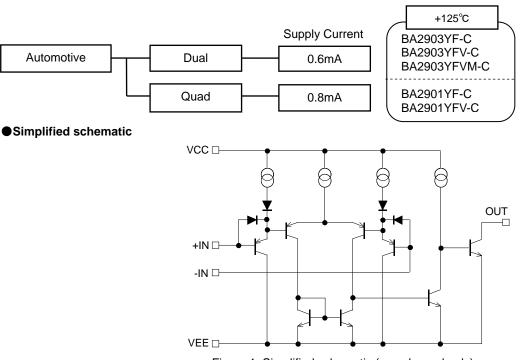
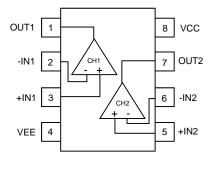


Figure 1. Simplified schematic (one channel only)

OProduct structure : Silicon monolithic integrated circuit OThis product is not designed protection against radioactive rays.

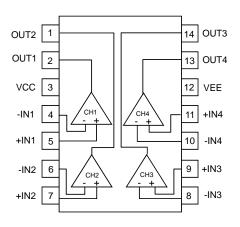
•Pin Configuration

BA2903YF-C : SOP8 BA2903YFV-C : SSOP-B8 BA2903YFVM-C : MSOP8



Pin No.	Pin name		
1	OUT1		
2	-IN1		
3	+IN1		
4	VEE		
5	+IN2		
6	-IN2		
7	OUT2		
8	VCC		

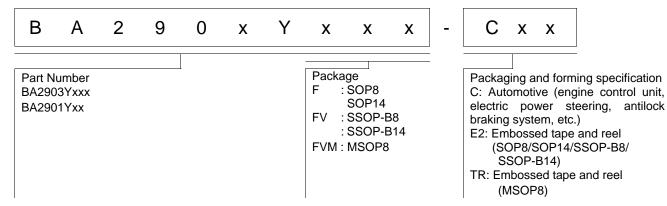
BA2901YF-C : SOP14 BA2901YFV-C : SSOP-B14



Pin name		
OUT2		
OUT1		
VCC		
-IN1		
+IN1		
-IN2		
+IN2		
-IN3		
+IN3		
-IN4		
+IN4		
VEE		
OUT4		
OUT3		

Package								
SOP8	SSOP-B8	MSOP8	SOP14	SSOP-B14				
BA2903YF-C	BA2903YFV-C	BA2903YFVM-C	BA2901YF-C	BA2901YFV-C				

Ordering Information



●Line-up

Topr	Operating Supply Voltage	Dual/Quad	Pa	ickage	Orderable Part Number
			SOP8	Reel of 2500	BA2903YF-CE2
	+2.0V to +36V	Dual	SSOP-B8	Reel of 2500	BA2903YFV-CE2
-40°C to +125°C			MSOP8	Reel of 3000	BA2903YFVM-CTR
		0	SOP14	Reel of 2500	BA2901YF-CE2
		Quad	SSOP-B14	Reel of 2500	BA2901YFV-CE2

● Absolute Maximum Ratings (Ta=25°C)

Parameter	Symbol		Ratings	Unit	
Supply Voltage		VCC-VEE	+36	V	
Power Dissipation		SOP8	77 0 ^{*1*6}		
		SSOP-B8	620 ^{*2*6}		
	Pd	MSOP8	580 ^{*3*6}	mW	
		SOP14	560 ^{*4*6}		
		SSOP-B14	870 ^{*5*6}		
Differential Input Voltage *7		Vid	+36	V	
Input Common-mode Voltage Range		Vicm	(VEE-0.3) to (VEE+36)	V	
Input Current		li	-10	mA	
Operating Supply Voltage		Vopr	+2.0 to +36 (±1.0 to ±18)	V	
Operating Temperature Range		Topr	-40 to +125	°C	
Storage Temperature Range		Tstg	-55 to +150	C°	
Maximum junction Temperature	Tjmax		Tjmax +150		

Note : Absolute maximum rating item indicates the condition which must not be exceeded.

Application if voltage in excess of absolute maximum rating or use out of absolute maximum rated temperature environment may cause deterioration of characteristics.

*1 To use at temperature above Ta=25°C reduce 6.2mW/°C.

*2 To use at temperature above Ta=25°C $\,$ reduce 5.5mW/°C.

*3 To use at temperature above Ta=25°C $\,$ reduce 4.7mW/°C.

*4 To use at temperature above Ta=25°C reduce 4.9mW/°C.

*5 To use at temperature above $Ta=25^{\circ}C$ reduce 7.0mW/°C.

*6 Mounted on a FR4 glass epoxy PCB(70mm×70mm×1.6mm).

*7 The voltage difference between inverting input and non-inverting input is the differential input voltage.

Then input pin voltage is set to more than VEE.

*8 An excessive input current will flow when input voltages of lesser than VEE-0.6V are applied. The input current can be set to less than the rated current by adding a limiting resistor.

•Electrical Characteristics

OBA2903Yxxx-C (Unless otherwise specified VCC=+5V, VEE=0V)

Parameter	Symbol	Temperature Limits		Unit	Conditions		
Falailletei	Symbol	range	Min.	Тур.	Max.	Unit	Conditions
Input Offset Voltage *9	Vio	25°C	-	2	4	mV	OUT=1.4V
input Onset voltage	VIO	Full range	-	-	5	mv	VCC=5 to 36V, OUT=1.4V
Input Offset Current *9	lio	25°C	-	5	40	5	OUT=1.4V
input Oliset Current	IIO	Full range	-	-	50	nA	001=1.4V
Input Bias Current *9	lb	25°C	-	50	250	5	OUT=1.4V
Input bias Current	ai	Full range	-	-	275	nA	001=1.4V
Input Common-mode	Vicm	25°C	0	-	VCC-1.5	v	
Voltage Range	VICITI	Full range	0	-	VCC-2.0	v	-
Lorgo Signal Valtago Cain	<u>^</u> ,,	25°C	88	100	-	dB	VCC=15V, OUT=1.4 to 11.4V
Large Signal Voltage Gain	Av	Full range	74	-	-	иБ	RL=15kΩ, VRL=15V
Quarte la Quarte at	ICC	25°C	-	0.6	1	mA	OUT=open
Supply Current		Full range	-	-	2.5	mA	OUT=open, VCC=36V
Output Sink Current *10	Isink	25°C	6	16	-	mΑ	+IN=0V, -IN=1V, VOL=1.5V
Output Saturation Voltage	VOL	25°C	-	150	400	mV	+IN=0V, -IN=1V,
(Low level output voltage)	VOL	Full range	-	-	700	mv	Isink=4mA
Output Leakage Current	lleak	25°C	-	0.1	-	μA	+IN=1V, -IN=0V, VOH=5V
(High level output current)	lieak	Full range	-	-	1	μΑ	+IN=1V, -IN=0V, VOH=36V
				1.3			RL=5.1kΩ, VRL=5V
Pooponoo Timo	Tre	25°C	-	1.5	-		IN=100mV _{P-P} , overdrive=5mV
Response Time	ne	250		0.4		μs	RL=5.1kΩ, VRL=5V, IN=TTL
			-	0.4	-		Logic Swing, VREF=1.4V
Operable Frequency	Foor	25°C	100			kHz	VCC=5V, RL=2kΩ, +IN=1.5V, -IN=5Vp-p
Operable Frequency	Fopr	250	100	-	-	КПΖ	(Duty 50% Rectangular Pulse)

*9 Absolute value

*10 Under high temperatures, please consider the power dissipation when selecting the output current.

When the output terminal is continuously shorted the output current reduces the internal temperature by flushing.

OBA2901Yxx-C (Unless otherwise specified VCC=+5V, VEE=0V)

Parameter	Symbol	Temperature Limits				Unit	Conditions	
Falailletei	Symbol	range	Min.	Тур.	Max.	Unit	Conditions	
Input Offset Voltage *11	Vio	25°C	-	2	4	mV	OUT=1.4V	
input Onset voltage	VIO	Full range	-	-	5	mv	VCC=5 to 36V, OUT=1.4V	
Input Offset Current *11	lio	25°C	-	5	40	nA	OUT=1.4V	
Input Onset Current	10	Full range	-	-	50	IIA	001=1.40	
Input Bias Current *11	lb	25°C	-	50	250	nA	OUT=1.4V	
Input bias Current	a	Full range	-	-	275	ΠA	001=1.4V	
Input Common-mode	Vicm	25°C	0	-	VCC-1.5	v		
Voltage Range	VICITI	Full range	0	-	VCC-2.0	V	-	
Lorgo Signal Valtage Cain	Av	25°C	88	100	-	dB	VCC=15V, OUT=1.4 to 11.4V	
Large Signal Voltage Gain	Av	Full range	74	-	-	иБ	RL=15kΩ, VRL=15V	
	ICC	25°C	-	0.8	2		OUT=open	
Supply Current		Full range	-	-	2.5	mA	OUT=open, VCC=36V	
Output Sink Current *12	Isink	25°C	6	16	-	mΑ	+IN=0V, -IN=1V, VOL=1.5V	
Output Saturation Voltage	VO	25°C	-	150	400	mV	+IN=0V, -IN=1V,	
(Low level output voltage)	VOL	Full range	-	-	700	mv	Isink=4mA	
Output Leakage Current	lleek	25°C	-	0.1	-		+IN=1V, -IN=0V, VOH=5V	
(High level output current)	lleak	Full range	-	-	1	μA	+IN=1V, -IN=0V, VOH=36V	
				1.3			RL=5.1kΩ, VRL=5V	
Deenenae Time	Tro	25°C	-	1.3	-		IN=100mV _{P-P} , overdrive=5mV	
Response Time	Tre	250		0.4		μs	RL=5.1kΩ, VRL=5V, IN=TTL	
			-	0.4	-		Logic Swing, VREF=1.4V	
Operable Frequency	Foor	25°C	100			kHz	VCC=5V, RL=2kΩ, +IN=1.5V, -IN=5Vp-p	
Operable Frequency	Fopr	250	100	-	-	KEIZ	(Duty 50% Rectangular Pulse)	

*11 Absolute value

*12 Under high temperatures, please consider the power dissipation when selecting the output current.

When the output terminal is continuously shorted the output current reduces the internal temperature by flushing.

Description of Electrical Characteristics

Described below are descriptions of the relevant electrical terms used in this datasheet. Items and symbols used are also shown. Note that item name and symbol and their meaning may differ from those on another manufacturer's document or general document.

1. Absolute maximum ratings

Absolute maximum rating items indicate the condition which must not be exceeded. Application of voltage in excess of absolute maximum rating or use out of absolute maximum rated temperature environment may cause deterioration of characteristics.

- 1.1 Supply Voltage (VCC-VEE) Indicates the maximum voltage that can be applied between the positive power supply terminal and negative power supply terminal without deterioration or destruction of characteristics of internal circuit.
- 1.2 Differential Input Voltage (Vid) Indicates the maximum voltage that can be applied between non-inverting and inverting terminals without damaging the IC.
- 1.3 Input Common-mode Voltage Range (Vicm) Indicates the maximum voltage that can be applied to the non-inverting and inverting terminals without deterioration or destruction of electrical characteristics. Input common-mode voltage range of the maximum ratings does not assure normal operation of IC. For normal operation, use the IC within the input common-mode voltage range characteristics.
- 1.4 Operating and Storage Temperature Ranges (Topr,Tstg) The operating temperature range indicates the temperature range within which the IC can operate. The higher the ambient temperature, the lower the power consumption of the IC. The storage temperature range denotes the range of temperatures the IC can be stored under without causing excessive deterioration of the electrical characteristics.
- 1.5 Power Dissipation (Pd)

Indicates the power that can be consumed by the IC when mounted on a specific board at the ambient temperature 25°C (normal temperature). As for package product, Pd is determined by the temperature that can be permitted by the IC in the package (maximum junction temperature) and the thermal resistance of the package.

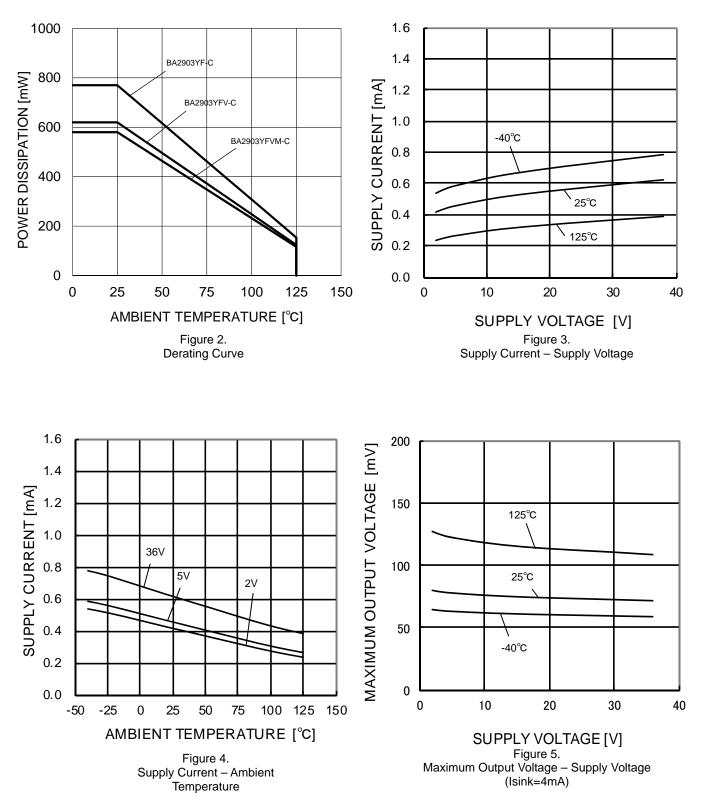
2.Electrical characteristics

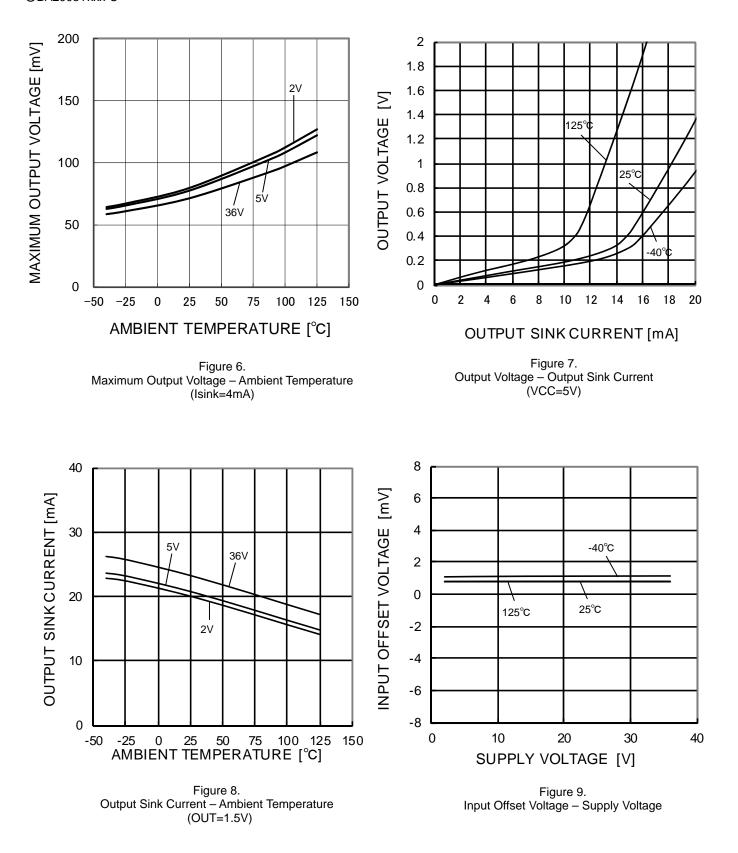
- 2.1 Input Offset Voltage (Vio) Indicates the voltage difference between non-inverting terminal and inverting terminals. It can be translated into the input voltage difference required for setting the output voltage at 0 V.
- 2.2 Input Offset Current (lio)

Indicates the difference of input bias current between the non-inverting and inverting terminals.

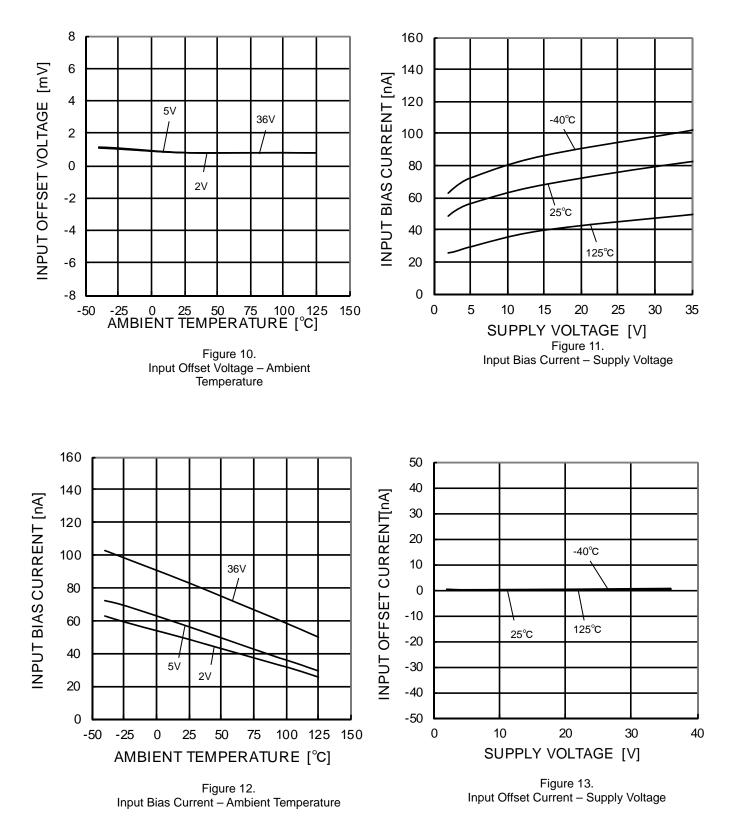
- 2.3 Input Bias Current (Ib) Indicates the current that flows into or out of the input terminal. It is defined by the average of input bias currents at the non-inverting and inverting terminals.
- 2.4 Input Common-mode Voltage Range (Vicm) Indicates the input voltage range where IC normally operates.
- 2.5 Large Signal Voltage Gain (Av) Indicates the amplifying rate (gain) of output voltage against the voltage difference between non-inverting terminal and inverting terminal. It is normally the amplifying rate (gain) with reference to DC voltage. Av = (Output voltage) / (Differential input voltage)
- 2.6 Supply current (ICC) Indicates the current that flows within the IC under specified no-load conditions.
- 2.7 Output Sink Current (Isink) Indicates the current flowing into the IC under specific output conditions.
- 2.8 Output Saturation Voltage (Low level output voltage) (VOL) Indicates the lower limit of output voltage under specific input and output conditions.
- 2.9 Output Leakage Current(High level output current) (Ileak) Indicates the current that flows into the IC under specific input and output conditions.
- 2.11 Response Time (Tre) Indicates the time interval between the application of input and output conditions.
- 2.10 Operable Frequency (Fopr) Indicates minimum frequency that IC moves under specific conditions..

OBA2903Yxxx-C

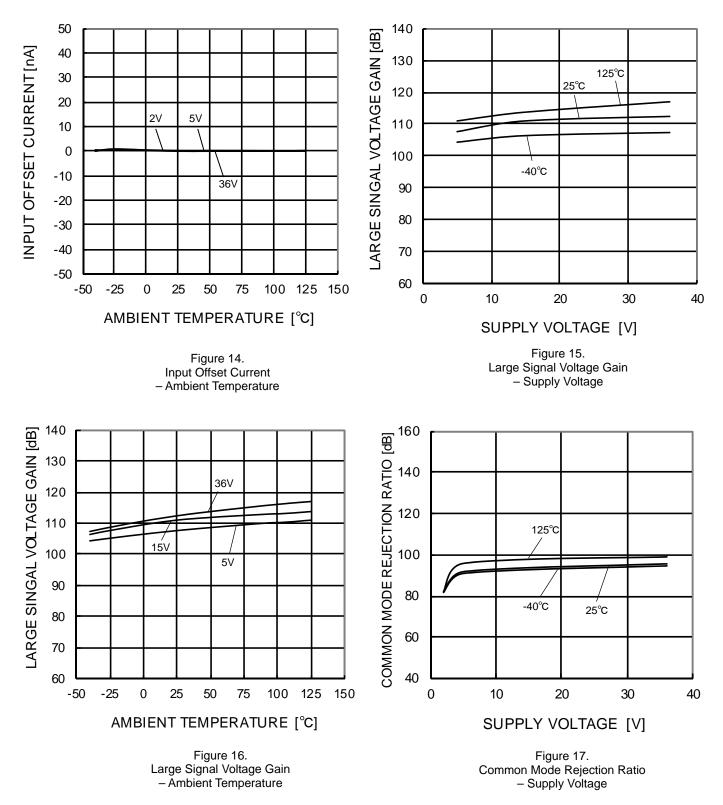




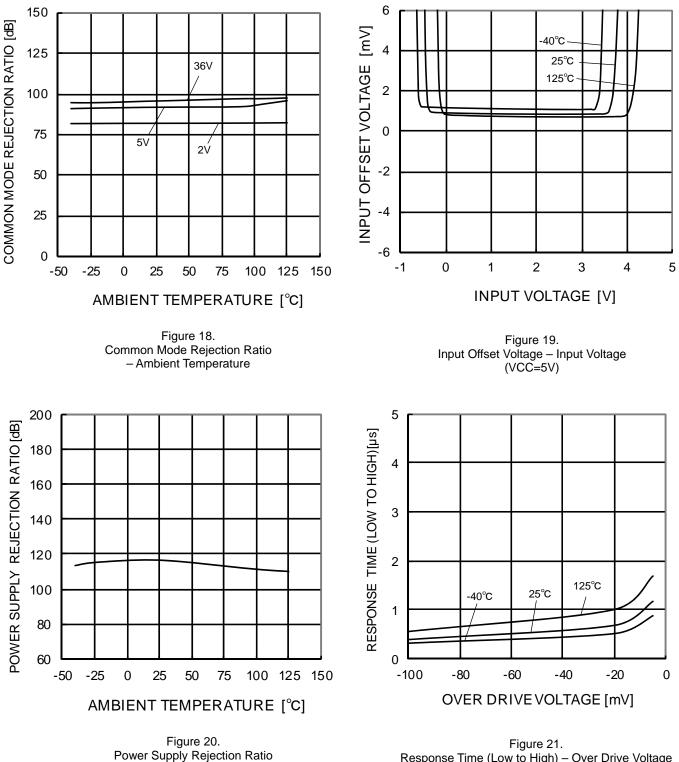
● Typical Performance Curves - Continued OBA2903Yxxx-C



Typical Performance Curves -Continued OBA2903Yxxx-C

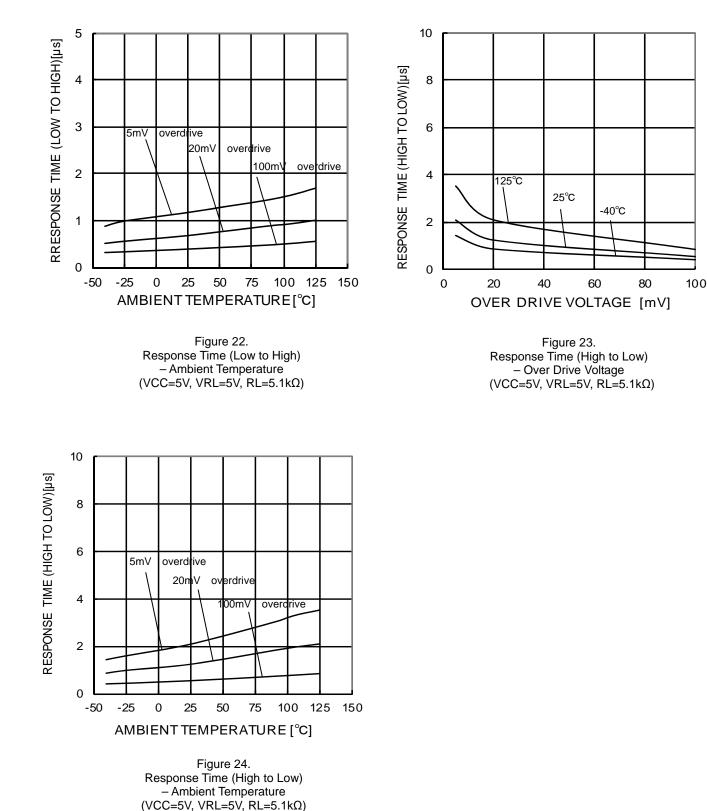


Typical Performance Curves -Continued OBA2903Yxxx-C

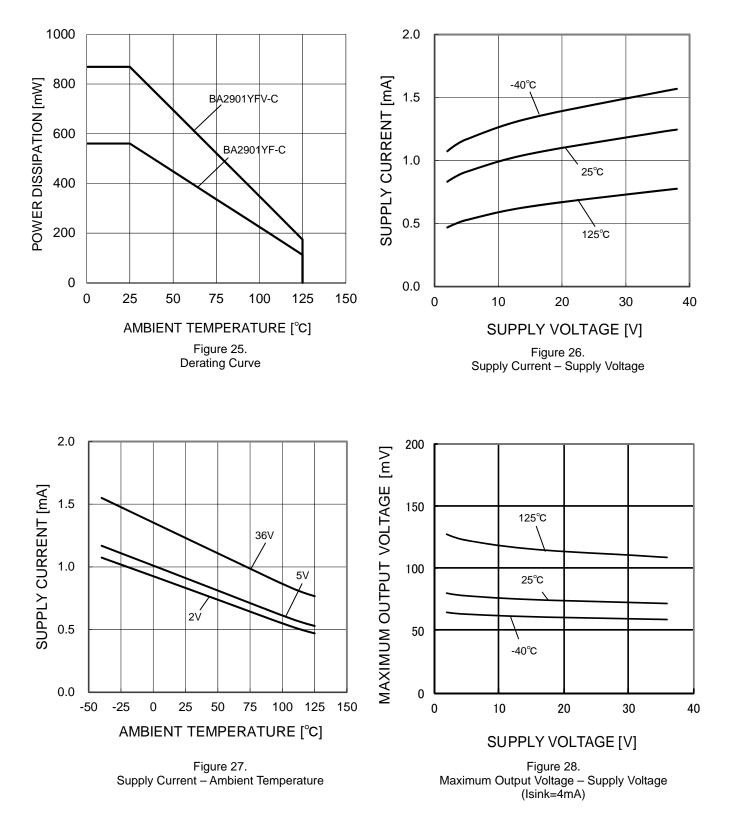


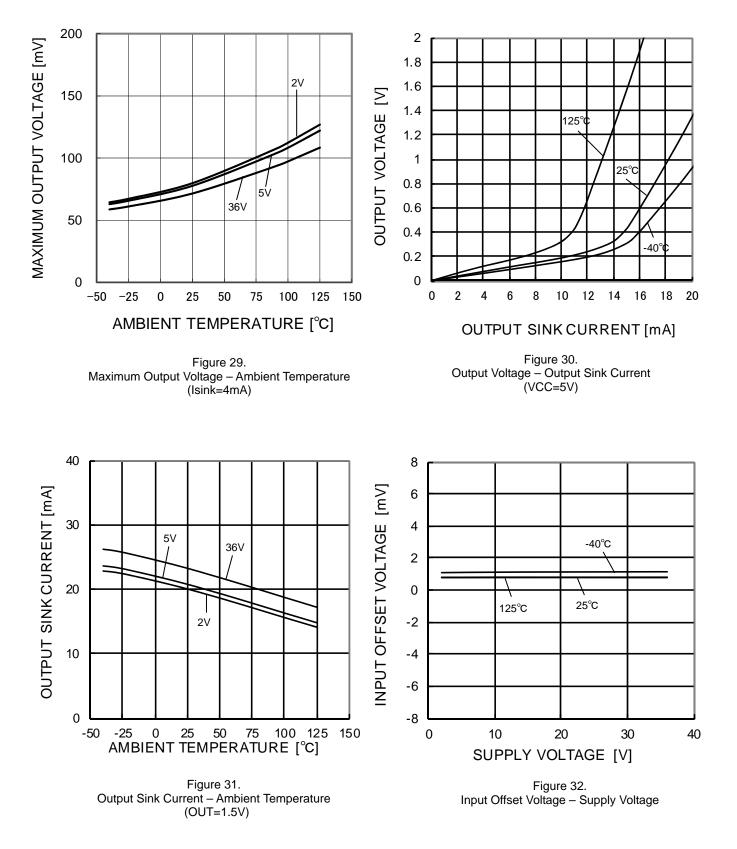
- Ambient Temperature

Response Time (Low to High) - Over Drive Voltage (VCC=5V, VRL=5V, RL=5.1kΩ)

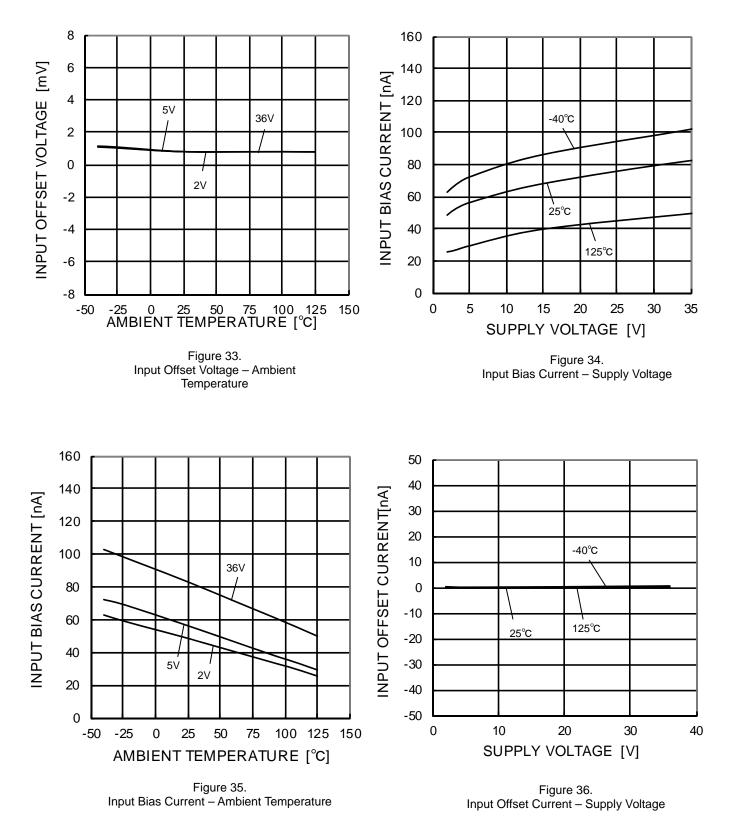


OBA2901Yxx-C

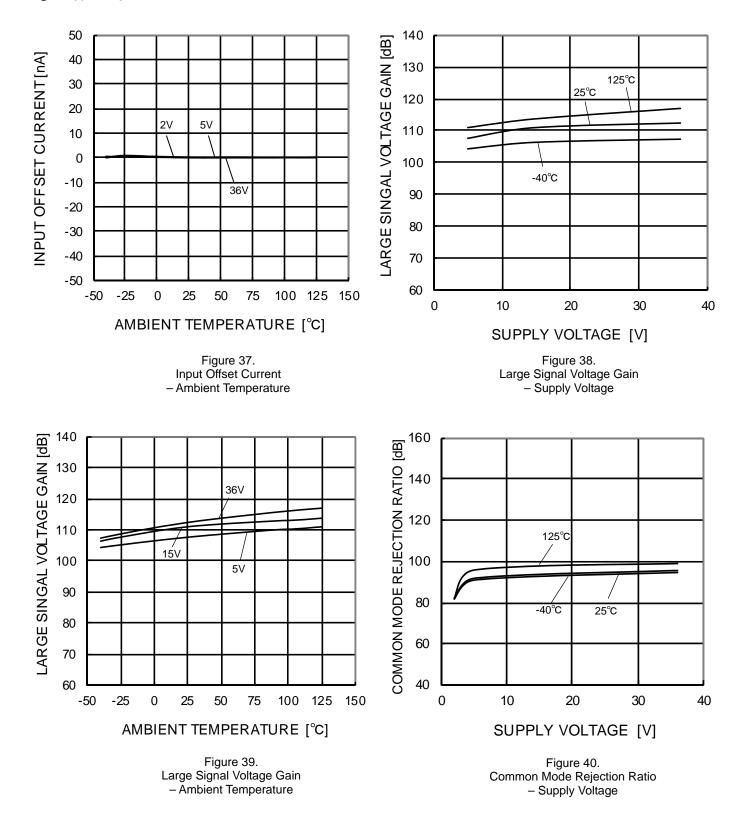




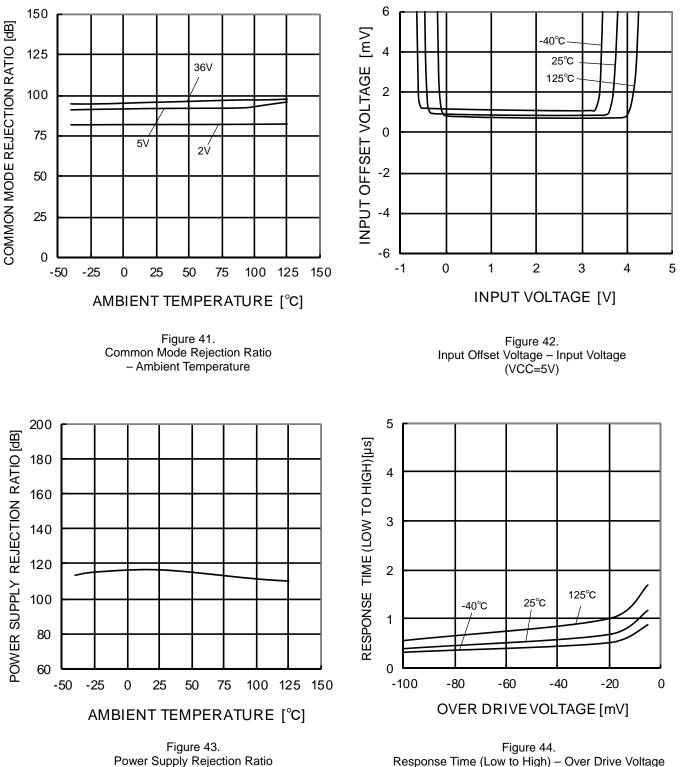
● Typical Performance Curves - Continued OBA2901Yxx-C



●Typical Performance Curves -Continued OBA2901Yxx-C



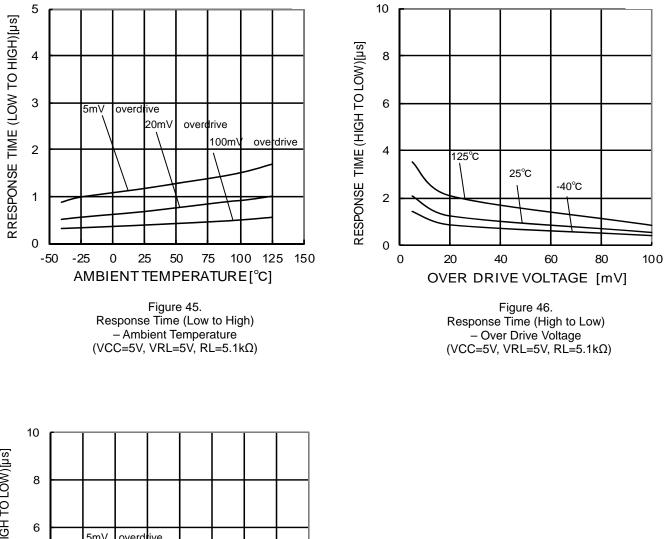
Typical Performance Curves -Continued OBA2901Yxx-C



⁻ Ambient Temperature

Response Time (Low to High) - Over Drive Voltage $(VCC=5V, VRL=5V, RL=5.1k\Omega)$

● Typical Performance Curves - Continued OBA2901Yxx-C



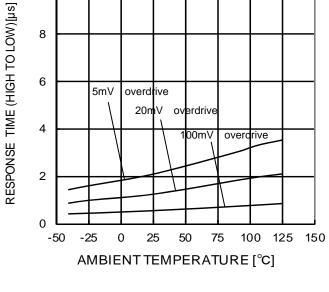


Figure 47. Response Time (High to Low) – Ambient Temperature (VCC=5V, VRL=5V, RL=5.1kΩ)

Power Dissipation

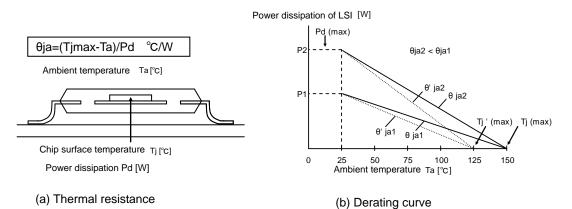
Power dissipation (total loss) indicates the power that the IC can consume at Ta=25°C (normal temperature). As the IC consumes power, it heats up, causing its temperature to be higher than the ambient temperature. The allowable temperature that the IC can accept is limited. This depends on the circuit configuration, manufacturing process, and consumable power.

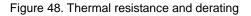
Power dissipation is determined by the allowable temperature within the IC (maximum junction temperature) and the thermal resistance of the package used (heat dissipation capability). Maximum junction temperature is typically equal to the maximum storage temperature. The heat generated through the consumption of power by the IC radiates from the mold resin or lead frame of the package. Thermal resistance, represented by the symbol $\theta ja^{\circ}C/W$, indicates this heat dissipation capability. Similarly, the temperature of an IC inside its package can be estimated by thermal resistance.

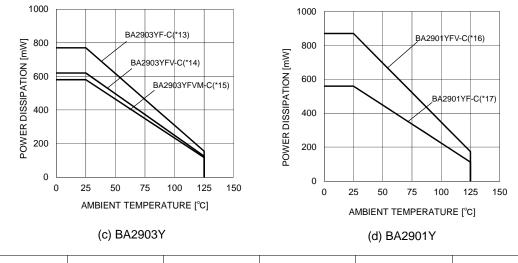
Figure 50. (a) shows the model of the thermal resistance of the package. The equation below shows how to compute for the Thermal resistance (θ ja), given the ambient temperature (Ta), junction temperature (Tj), and power dissipation (Pd).

 $\theta_{ja} = (T_{jmax}-T_a) / Pd$ °C/W · · · · · (I)

The Derating curve in Figure 48. (b) indicates the power that the IC can consume with reference to ambient temperature. Power consumption of the IC begins to attenuate at certain temperatures. This gradient is determined by Thermal resistance (θ ja), which depends on the chip size, power consumption, package, ambient temperature, package condition, wind velocity, etc. This may also vary even when the same of package is used. Thermal reduction curve indicates a reference value measured at a specified condition. Figure 49. (c),(d) shows an example of the derating curve for BA2903Yxxx-C, BA2901Yxx-C.







(*13)	(*14)	(*15)	(*16)	(*17)	UNIT
6.2	5.0	4.7	7.0	4.5	mW/°C

When using the unit above Ta=25°C, subtract the value above per degree°C.

Permissible dissipation is the value when FR4 glass epoxy board 70mm×70mm×1.6mm(cooper foil area below 3%) is mounted.

Figure 49. Derating curve

Application Information

NULL method condition for Test circuit 1

VCC,VEE,EK,Vicm Unit : V

Parameter	VF	S1	S2	S3	Vcc	VEE	EK	Vicm	Calculation
Input Offset Voltage	VF1	ON	ON	ON	5~36	0	-1.4	0	1
Input Offset Current	VF2	OFF	OFF	ON	5	0	-1.4	0	2
Innut Ding Current	VF3	OFF	ON		5	0	-1.4	0	2
Input Bias Current	VF4	ON	OFF	ON	5	0	-1.4	0	3
Lorgo Signal Valtago Coin	VF5	ON	ON	ON	15	0	-1.4	0	4
Large Signal Voltage Gain	VF6		ON	UN	15	0	-11.4	0	4

- Calculation -

1. Input Offset Voltage (Vio)

$$Vio = \frac{|VF1|}{1+RF/RS} [V]$$

2. Input Offset Current (lio)

$$lio = \frac{|VF2 - VF1|}{Ri \times (1 + RF / RS)}$$
 [A]

3. Input Bias Current (Ib)

$$Ib = \frac{|VF4 - VF3|}{2 \times Ri \times (1 + RF/RS)}$$
 [A]

4. Large Signal Voltage Gain (Av)

 $Av = 20 \times Log \frac{\Delta EK \times (1+RF/RS)}{\mid VF5 - VF6 \mid} \quad [dB]$

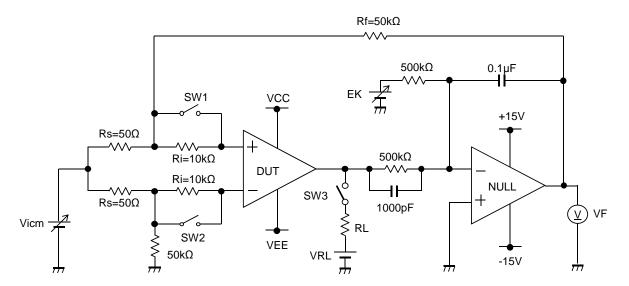


Figure 50. Test circuit 1 (one channel only)

BA2903Yxxx-C, BA2901Yxx-C

Switch Condition for Test Circuit 2

SW No.			SW 2	SW 3	SW 4	SW 5	SW 6	SW 7
Supply Current		OFF	OFF	OFF	OFF	OFF	OFF	OFF
Output Sink Current	VOL=1.5V	OFF	ON	ON	OFF	OFF	OFF	ON
Output Saturation Voltage	lsink=4mA	OFF	ON	ON	OFF	ON	ON	OFF
Output Leakage Current	VOH=36V	OFF	ON	ON	OFF	OFF	OFF	ON
Response Time	RL=5.1kΩ, VRL=5V	ON	OFF	ON	ON	OFF	OFF	OFF

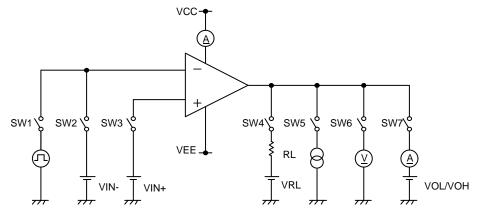
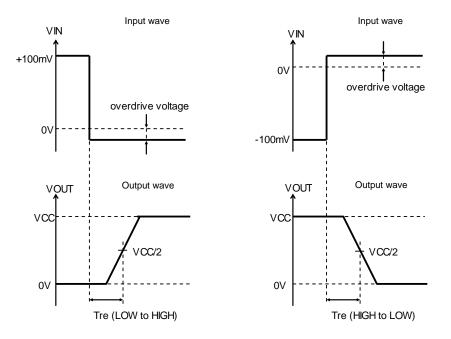
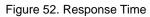


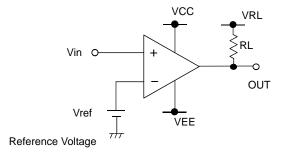
Figure 51. Test Circuit 2 (one channel only)



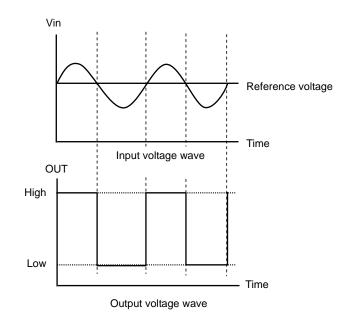


Application example

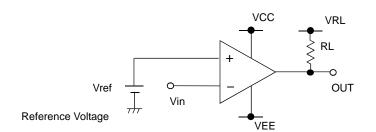
OReference voltage is -IN



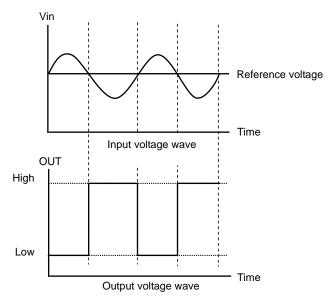
While the input voltage(Vin) is higher that the reference voltage, the output voltage remains high. In case the input voltage becomes lower than the reference voltage, the output voltage will turn low.



OReference voltage is +IN



While the input voltage(Vin) is smaller that the reference voltage, the output voltage remains high. In case the input voltage becomes higher than the reference voltage, the output voltage will turn low.



Operational Notes

1) Unused circuits

When there are unused comparators, it is recommended that they are connected as in Figure 53. ,setting the non-inverting input terminal to a potential within the in-phase input voltage range (Vicm).

Vicm VCC OPEN

Figure 53. Example of application

circuit for unused comparator

Please keep this potential in Vicm VCC-1.5V>Vicm>VEE

2) Input voltage

Applying VEE +36V to the input terminal is possible without causing deterioration of the electrical characteristics or destruction, regardless of the supply voltage. However, this does not ensure normal circuit operation. Please note that the circuit operates normally only when the input voltage is within the common mode input voltage range of the electric characteristics.

3) Power supply (single / dual)

The comparator operates when the voltage supplied is between VCC and VEE. Therefore, the signal supply comparator can be used as a dual supply comparator as well.

4) Power dissipation Pd

Using the unit in excess of the rated power dissipation may cause deterioration in electrical characteristics including reduced current capability due to the rise of chip temperature. Therefore, please take into consideration the power dissipation (Pd) under actual operating conditions and apply a sufficient margin in thermal design. Refer to the thermal derating curves for more information.

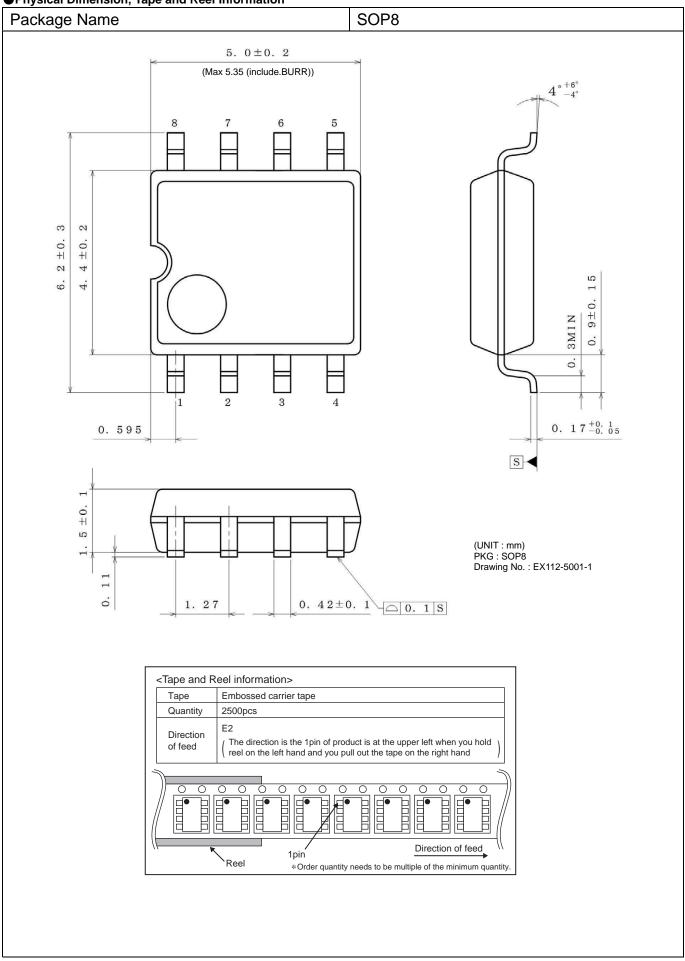
- 5) Short-circuit between pins and erroneous mounting Be careful when mounting the IC on printed circuit boards. The IC may be damaged if it is mounted in a wrong orientation or if pins are shorted together. Short circuit may be caused by conductive particles caught between the pins.
- 6) Terminal short-circuits When the output and VCC terminals are shorted, excessive output current may flow, resulting in undue heat generation and, subsequently, destruction.
- Operation in a strong electromagnetic field Operating the IC in the presence of a strong electromagnetic field may cause the IC to malfunction.
- Radioactive rays This IC is not designed protection against radioactive rays.
- 9) IC handling

Applying mechanical stress to the IC by deflecting or bending the board may cause fluctuations in the electrical characteristics due to piezo resistance effects.

10) Board inspection

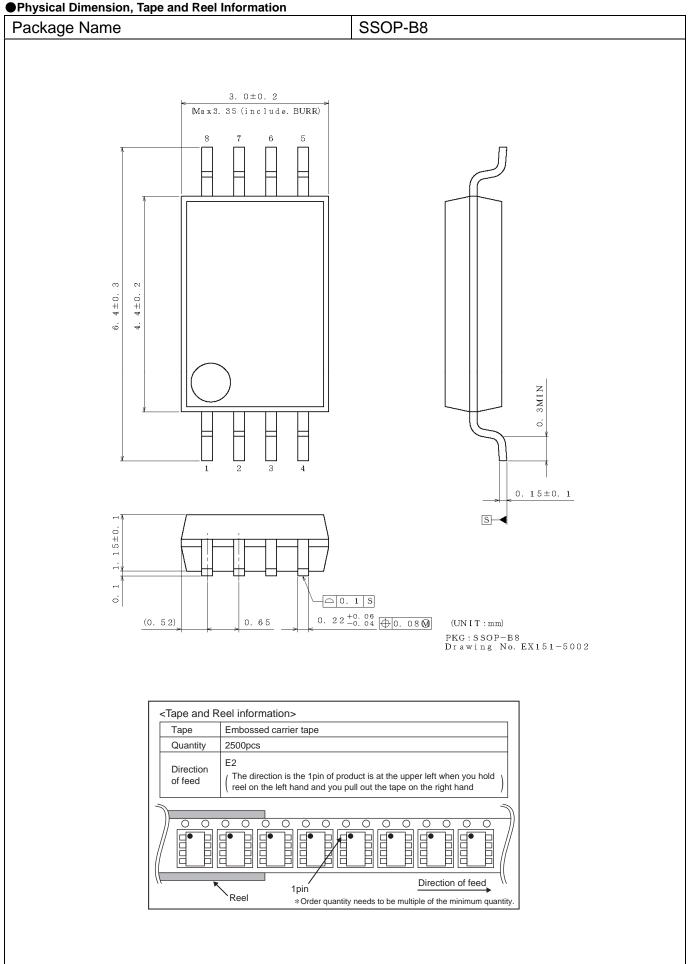
Connecting a capacitor to a pin with low impedance may stress the IC. Therefore, discharging the capacitor after every process is recommended. In addition, when attaching and detaching the jig during the inspection phase, ensure that the power is turned OFF before inspection and removal. Furthermore, please take measures against ESD in the assembly process as well as during transportation and storage.

Physical Dimension, Tape and Reel Information

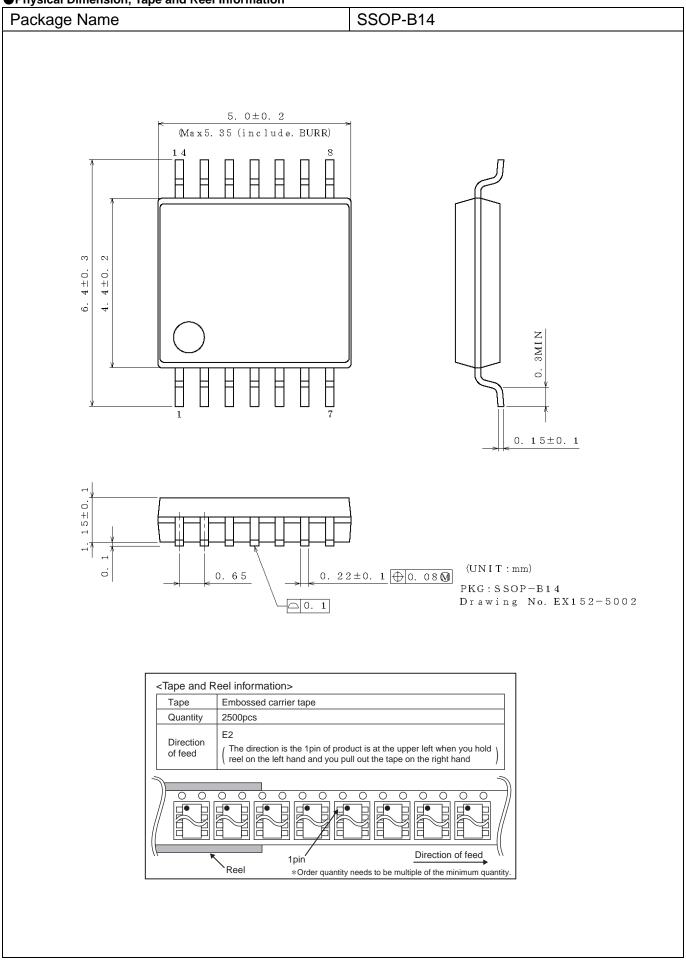


Physical Dimension, Tape and Reel Information SOP14 Package Name $8.\ 7\ \pm 0.\ 2$ (Max 9.05 (include.BURR)) 8 14 ŝ 2 2 ± 0 . 4 ± 0 . .9 4 3 M I N 0. 1 $0.\ 15\pm0.\ 1$ - 5 ± 0 . (UNIT : mm) PKG : SOP14 Drawing No. : EX113-5001 Ļ, 11 0. 1.27 $0. \ 4\pm0. \ 1$ \bigcirc 0. 1 <Tape and Reel information> Таре Embossed carrier tape Quantity 2500pcs E2 Direction The direction is the 1pin of product is at the upper left when you hold of feed reel on the left hand and you pull out the tape on the right hand Direction of feed 1pin Reel *Order quantity needs to be multiple of the minimum quantity



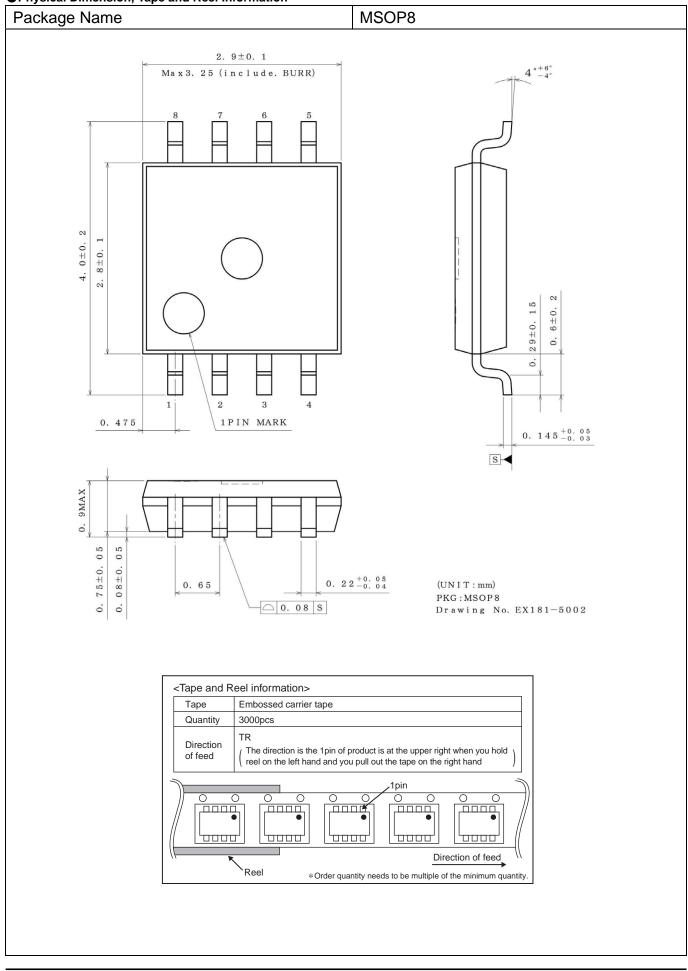


Physical Dimension, Tape and Reel Information

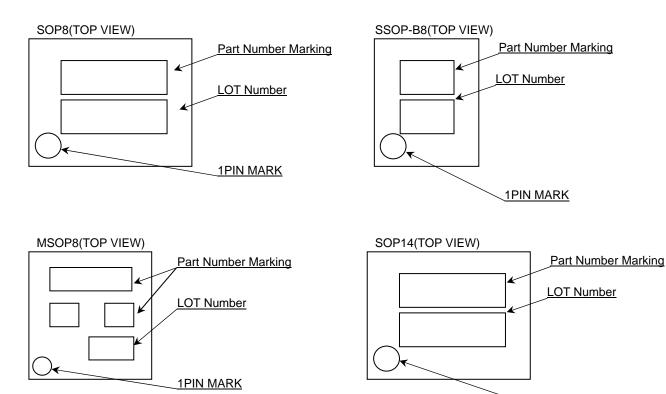


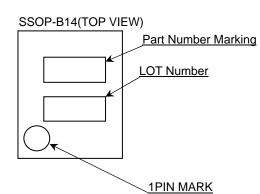
Datasheet

Physical Dimension, Tape and Reel Information



Marking Diagrams

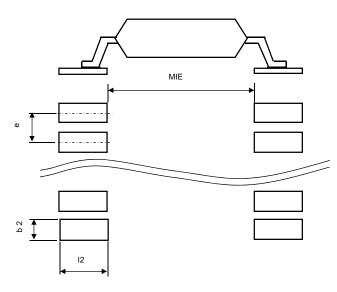




Product N	Name	Package Type	Marking
	F-C	SOP8	2903Y
BA2903Y	FV-C	SSOP-B8	03Y
	FVM-C	MSOP8	2903Y
BA2901Y	F-C	SOP14	BA2901YF
BA29011	FV-C	SSOP-B14	2901Y

1PIN MARK

Land pattern data



SOP8, SSOP-B8, MSOP8 SOP14, SSOP-B14

All	dime	ensior	ns in	mm

Package	Land pitch e	Land space MIE	Land length ≧ℓ 2	Land width b2
SOP8 SOP14	1.27	4.60	1.10	0.76
SSOP-B8 SSOP-B14	0.65	4.60	1.20	0.35
MSOP8	0.65	2.62	0.99	0.35

Revision History

Date	Revision	Changes
11.APR.2012	001	New Release
21.JAN.2013	002	Land pattern data inserted.
11.MAR.2013	003	Input offset voltage, Input offset current limit (Temp=25°C) changed. Description of Physical Dimension, Tape and Reel Information changed.
8.MAY.2013	004	SOP8, SSOP-B8, MSOP8 Power dissipation corrected. SSOP-B8, SSOP-B14 corrected.

Notice

Precaution on using ROHM Products

1. If you intend to use our Products in devices requiring extremely high reliability (such as medical equipment ^(Note 1), aircraft/spacecraft, nuclear power controllers, etc.) and whose malfunction or failure may cause loss of human life, bodily injury or serious damage to property ("Specific Applications"), please consult with the ROHM sales representative in advance. Unless otherwise agreed in writing by ROHM in advance, ROHM shall not be in any way responsible or liable for any damages, expenses or losses incurred by you or third parties arising from the use of any ROHM's Products for Specific Applications.

(Note1) Medical Equipment Classification of the Specific Ap	plications
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JAPAN	USA	EU	CHINA
CLASSI	CLASSⅢ	CLASS II b	CLASSII
CLASSⅣ	CLASSI	CLASSⅢ	CLASSII

2. ROHM designs and manufactures its Products subject to strict quality control system. However, semiconductor products can fail or malfunction at a certain rate. Please be sure to implement, at your own responsibilities, adequate safety measures including but not limited to fail-safe design against the physical injury, damage to any property, which a failure or malfunction of our Products may cause. The following are examples of safety measures:

[a] Installation of protection circuits or other protective devices to improve system safety

[b] Installation of redundant circuits to reduce the impact of single or multiple circuit failure

- 3. Our Products are not designed under any special or extraordinary environments or conditions, as exemplified below. Accordingly, ROHM shall not be in any way responsible or liable for any damages, expenses or losses arising from the use of any ROHM's Products under any special or extraordinary environments or conditions. If you intend to use our Products under any special or extraordinary environments or conditions (as exemplified below), your independent verification and confirmation of product performance, reliability, etc, prior to use, must be necessary:
 - [a] Use of our Products in any types of liquid, including water, oils, chemicals, and organic solvents
 - [b] Use of our Products outdoors or in places where the Products are exposed to direct sunlight or dust
 - [c] Use of our Products in places where the Products are exposed to sea wind or corrosive gases, including Cl₂, H₂S, NH₃, SO₂, and NO₂
 - [d] Use of our Products in places where the Products are exposed to static electricity or electromagnetic waves
 - [e] Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
 - [f] Sealing or coating our Products with resin or other coating materials
 - [g] Use of our Products without cleaning residue of flux (even if you use no-clean type fluxes, cleaning residue of flux is recommended); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
 - [h] Use of the Products in places subject to dew condensation
- 4. The Products are not subject to radiation-proof design.
- 5. Please verify and confirm characteristics of the final or mounted products in using the Products.
- 6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse. is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- 7. De-rate Power Dissipation (Pd) depending on Ambient temperature (Ta). When used in sealed area, confirm the actual ambient temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
- 9. ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

Precaution for Mounting / Circuit board design

- 1. When a highly active halogenous (chlorine, bromine, etc.) flux is used, the residue of flux may negatively affect product performance and reliability.
- 2. In principle, the reflow soldering method must be used; if flow soldering method is preferred, please consult with the ROHM representative in advance.

For details, please refer to ROHM Mounting specification

Precautions Regarding Application Examples and External Circuits

- 1. If change is made to the constant of an external circuit, please allow a sufficient margin considering variations of the characteristics of the Products and external components, including transient characteristics, as well as static characteristics.
- 2. You agree that application notes, reference designs, and associated data and information contained in this document are presented only as guidance for Products use. Therefore, in case you use such information, you are solely responsible for it and you must exercise your own independent verification and judgment in the use of such information contained in this document. ROHM shall not be in any way responsible or liable for any damages, expenses or losses incurred by you or third parties arising from the use of such information.

Precaution for Electrostatic

This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of lonizer, friction prevention and temperature / humidity control).

Precaution for Storage / Transportation

- 1. Product performance and soldered connections may deteriorate if the Products are stored in the places where:
 - [a] the Products are exposed to sea winds or corrosive gases, including Cl2, H2S, NH3, SO2, and NO2
 - [b] the temperature or humidity exceeds those recommended by ROHM
 - [c] the Products are exposed to direct sunshine or condensation
 - [d] the Products are exposed to high Electrostatic
- 2. Even under ROHM recommended storage condition, solderability of products out of recommended storage time period may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is exceeding the recommended storage time period.
- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- 4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

Precaution for Product Label

QR code printed on ROHM Products label is for ROHM's internal use only.

Precaution for Disposition

When disposing Products please dispose them properly using an authorized industry waste company.

Precaution for Foreign Exchange and Foreign Trade act

Since our Products might fall under controlled goods prescribed by the applicable foreign exchange and foreign trade act, please consult with ROHM representative in case of export.

Precaution Regarding Intellectual Property Rights

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- 4. The proper names of companies or products described in this document are trademarks or registered trademarks of ROHM, its affiliated companies or third parties.

General Precaution

- 1. Before you use our Products, you are requested to care fully read this document and fully understand its contents. ROHM shall not be in an y way responsible or liable for failure, malfunction or accident arising from the use of a ny ROHM's Products against warning, caution or note contained in this document.
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