

BA6220

LINEAR INTEGRATED CIRCUIT

GENERAL USE ELECTRONIC GOVERNOR

DESCRIPTION

The UTC **BA6220** is a monolithic integrated circuit, developed for speed control of general use DC motors.

FEATURES

- * Wide range of working power supply voltage range (V_{CC}= 3.5V 16V).
- * Very large starting torque at the low voltage.
- * Large permissible loss due to effective utilization of substrate radiation.
- * Usable for various DC motors by means of changing constants of the external components.

* Radio cassette tape recorders

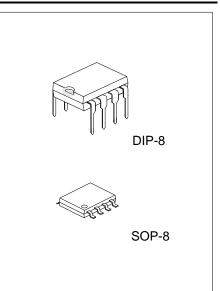
ORDERING INFORMATION

Ordering Number		Dookogo	Dooking	
Lead Free	Halogen Free	Package	Packing	
BA6220L-D08-T	BA6220G-D08-T	DIP-8	Tube	
BA6220L-S08-R	BA6220G-S08-R	SOP-8	Tape Reel	

BA6220G-D08-T	(1)Packing Type (2)Package Type (3)Green Package	(1) T: Tube, R: Tape Reel(2) D08: DIP-8, S08: SOP-8(3) G: Halogen Free and Lead Free, L: Lead Free
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MARKING

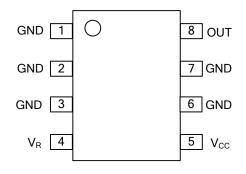
DIP-8	SOP-8
8 7 6 5 UTC □□□□ BA6220□ C: Lead Free G: Halogen Free Lot Code	8 7 6 5 UTC □□□□ ► Date Code BA6220 → G: Halogen Free ● □□→ Lot Code 1 2 3 4



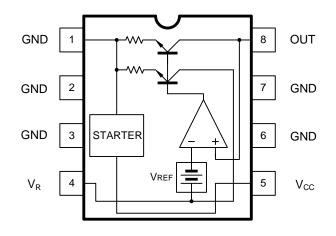
BA6220

LINEAR INTEGRATED CIRCUIT

■ PIN CONFIGURATION



BLOCK DIAGRAM





■ ABSOLUTE MAXIMUM RATINGS (T_A=25°C, unless otherwise specified.)

PARAMETER		SYMBOL	RATINGS	UNIT
Supply Voltage		V _{cc}	18	V
Power Dissipation (Note 2)	DIP-8		1.4	W
	SOP-8		0.8	W
Operating Temperature		T _{OPR}	-25 ~ +75	°C
Storage Temperature		T _{STG}	-55 ~ +125	°C

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. PCB (Copper-surfaced) 9cm², T 1.0mm.

■ **RECOMMENDED OPERATING CONDITIONS** (T_A=25°C, unless otherwise specified.)

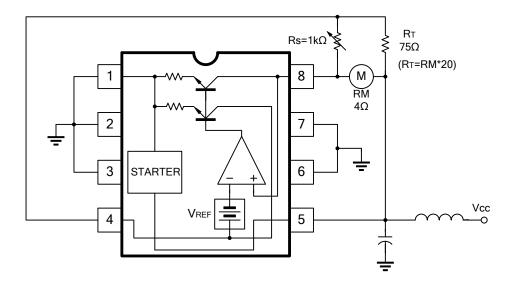
PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Operating Supply Voltage	V _{cc}	Loader: 8g-cm	3.5		16	V

■ ELECTRICAL CHARACTERISTICS (T_A=25°C, V_{CC}=12V, unless otherwise specified.)

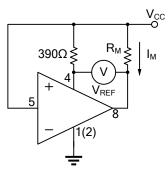
PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Output Saturate Voltage	V _{SAT}	V _{CC} =4.2V, R _M =4.4Ω (Fig.3)		1.5	2.3	V
Reference Voltage	V _{REF}	I _M =10Ma (Fig.1)	1.10	1.27	1.40	V
Current Ratio	К	R _M =33 - 44Ω (Fig.2)	18	20	22	
Voltage Feature of Reference Voltage	$\Delta V_{REF}/V_{REF}/\Delta V_{CC}$	I _M =100mA, V _{CC} =6.3 - 16V (Fig.1)		0.06		%/V
Voltage Feature of Current Ratio	Δ K/K/ Δ V _{CC}	I _M =100mA, V _{CC} =6.3 - 16V (Fig.2)		0.4		%/V
Bias Current	IBIAS	R _M =180Ω (Fig.4)	0.5	0.8	1.2	mA
Current Feature of Reference Voltage	$\Delta V_{\text{REF}}/V_{\text{REF}}/\Delta I_{\text{M}}$	I _M =30 - 200mA (Fig.1)		-0.02		%/mA
Current Feature of Current Ratio	$\Delta K/K/\Delta I_M$	I _M =30 - 200mA (Fig.2)		-0.02		%/mA
Temperature Feature of Reference Voltage	$\Delta V_{REF}/V_{REF}/\Delta T_A$	I _M =100mA, T _A =-25 ~ 75℃ (Fig.1)		0.01		%/°C
Temperature Feature of Current ratio	Δ K/K/ Δ T _A	I _M =100mA, T _A =-25 ~ 75℃ (Fig.2)		0.01		%/°C

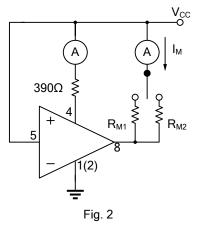


APPLICATION CIRCUIT

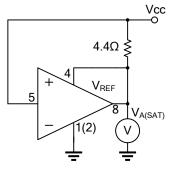


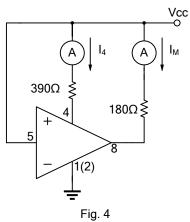
TEST CIRCUIT















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