

## **Technical Note**

## Reversible Motor Drivers for Brush Motors 1.0A or More Reversible Motor Drivers (2 Motors)



No.11008EBT04

## BA6247FP-Y, BA6238A

#### Description

The reversible motor driver for output 1.0A or more for two motors drives a brush motor and incorporates one and a half circuits of reversible motor driver. In addition, since the output section can control voltage applied to motors by output high voltage setting pin, the torque at the time of driving motors can be varied.

#### Features

- 1) Built-in one and a half circuits of a reversible motor driver
- 2) Minimal external components
- 3) Output voltage can be optionally set by reference voltage setting pin
- 4) Built-in thermal shutdown circuit

#### Applications

Audio-visual equipment; PC peripherals; Car audios; Car navigation systems; OA equipments

Parameter	Currente e l	Rat	Linit		
Parameter	Symbol	BA6247FP-Y	BA6238A	Unit	
Supply voltage	VCC1, VCC2	20	20	V	
Output current	I <sub>OMAX</sub>	1* <sup>1</sup>	1.6* <sup>1</sup>	А	
Control input pins	V <sub>IN</sub>	-0.2 ~ 6.0	-0.3 ~ 5.0	V	
Operating temperature	T <sub>OPR</sub>	-25 ~ 75	-25 ~ 75	°C	
Storage temperature	T <sub>STG</sub>	-55 ~ 150	-55 ~ 125	°C	
Power dissipation	Pd	1.45* <sup>4</sup>	2.0* <sup>2</sup> / 0.95* <sup>3</sup>	W	
Junction temperature	T <sub>jmax</sub>	150	125	°C	

#### ●Absolute maximum ratings (Ta=25°C, All voltages are with respect to ground)

\*1 Do not, exceed Pd or ASO (Pulse at 1/50 duty: 50ms).

\*2 HSIP10 package. Derated at 20mW/°C above 25°C.

\*3 SIP10 package. Derated at 9.5mW/°C above 25°C.

\*4 HSOP25 package. Mounted on a 70mm x 70mm x 1.6mm FR4 glass-epoxy board with less than 3% copper foil. Derated at 11.6mW/°C above 25°C.

#### Operating conditions (Ta=25°C)

Parameter	Symbol	Rat	Unit	
Farameter	Symbol	BA6247FP-Y	BA6238A	Offic
Supply voltage	VCC1,	8 ~ 18	8 ~ 18	V
Supply voltage	VCC2	8 ~ 18	8 ~ 18	V
VREF voltage	VR	0 ~ 18	0 ~ 18	V

## ●Electrical characteristics (BA6247FP-Y, unless otherwise specified, Ta=25°C and VCC1=VCC2=12V)

Parameter	Symbol		Limits		Unit	Conditions	
Falameter	Symbol	Min.	Тур.	Max.	Unit	Conditions	
Supply current	I <sub>CC</sub>	-	10	20	mA	IN1, IN2, IN3=L	
Input threshold voltage H	V <sub>IH</sub>	3.5	-	-	V		
Input threshold voltage L	VIL	-	-	1.0	V		
Output voltage H	V <sub>OH</sub>	10	10.5	-	V	I <sub>O</sub> =0.5A, VR=OPEN	
Output voltage L	V <sub>OL</sub>	-	0.9	1.5	V	I <sub>0</sub> =0.5A	
Output leak current	I <sub>OL</sub>	-	-	1	mA	IN1, IN2, IN3=L, VCC2 current	
Output offset voltage	V <sub>OFS</sub>	-0.5	0	0.5	V	VR=6V, I <sub>0</sub> =0.5A, V <sub>OH</sub> -VR	
VR bias current	I <sub>8</sub>	0.5	0.8	1.6	mA	VR=6V, I <sub>0</sub> =0.5A	

## ● Electrical characteristics (BA6238A, unless otherwise specified, Ta=25°C and VCC1=VCC2=12V)

Parameter	Symbol		Limits		Unit	Conditions	
Falameter	Symbol	Min.	Тур.	Max.	Offic	Conditions	
Supply current	I <sub>CC</sub>	-	12	24	mA	IN1, IN2, IN3=L, $R_L = \infty$	
Input threshold voltage H	V <sub>IH</sub>	4.0	-	-	V		
Input threshold voltage L	V <sub>IL</sub>	-	-	1.0	V		
Output voltage H	V <sub>OH</sub>	10.0	10.5	-	V	I <sub>O</sub> =0.5A, VR=OPEN	
Output voltage L	V <sub>OL</sub>	-	0.8	1.5	V	I <sub>O</sub> =0.5A, VR=OPEN	
Output leak current	I <sub>OL</sub>	-	-	1	mA	IN1, IN2, IN3=L, R <sub>L</sub> =∞, VCC2 current	
Output offset voltage	V <sub>OFS</sub>	-0.5	0	0.5	V	VR=6V, I <sub>0</sub> =0.5A, V <sub>OH</sub> -VR	
VR bias current	I <sub>8</sub>	0.2	0.6	1.5	mA	VR=6V, I <sub>0</sub> =0.5A	

75°C

25°C

-25°C

75°C

25°C

-25°C

1.6

-25°C

25℃ 75°C

1.6

1.2

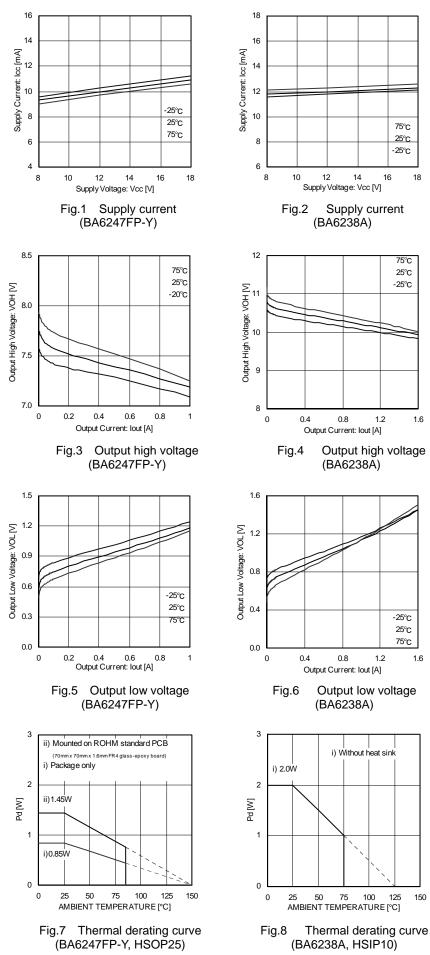
125 150

1.2

18

16

## Electrical characteristic curves (Reference data)



## Block diagram and pin configuration

## BA6247FP-Y

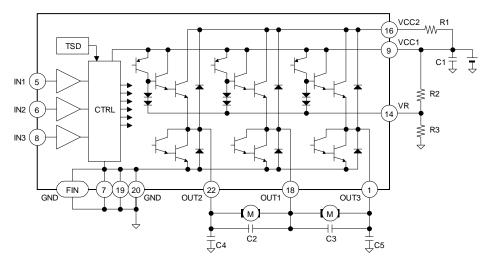


Fig.9 BA6247FP-Y

Table	DA0247FF-1
Name	Function
OUT3	Driver output
NC	NC
NC	NC
NC	NC
IN1	Control input
IN2	Control input
GND	GND
IN3	Control input
VCC1	Power supply (small signal)
NC	NC
VR	Reference voltage setting pin
NC	NC
VCC2	Power supply (driver stage)
NC	NC
OUT1	Driver output
GND	GND
GND	GND
NC	NC
OUT2	Driver output
NC	NC
NC	NC
NC	NC
GND	GND
	Name           OUT3           NC           NC           NC           IN1           IN2           GND           IN3           VCC1           NC           NC           NC           NC           NC           NC           VR           VC2           NC           VC2           NC           OUT1           GND           GND           NC           OUT1           GND           NC           NC           OUT1           GND           NC           NC

Table 1 BA6247FP-Y

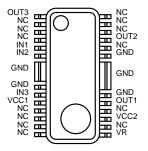


Fig.10 BA6247FP-Y (HSOP25)

## Block diagram and pin configuration

## BA6238A

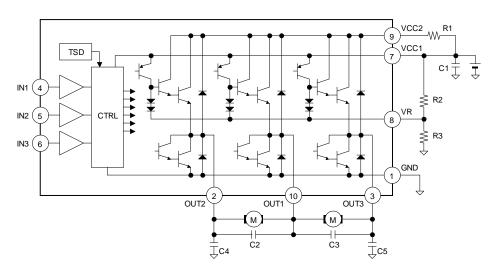
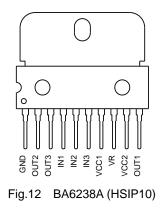


Fig.11 BA6238A

Table 2 BA6238A							
Pin	Name	Function					
1	GND	GND					
2	OUT2	Driver output					
3	OUT3	Driver output					
4	IN1	Control input					
5	IN2	Control input					
6	IN3	Control input					
7	VCC1	Power supply (small signal)					
8	VR	Reference voltage setting pin					
9	VCC2	Power supply (driver stage)					
10	OUT1	Driver output					

Table 2 BA6238A



#### •External application components

1) Resistor for the current limitation, R1

This is a current limiting resistor for collector loss reduction and at the time of short-circuited output. It depends on the power supply voltage used, etc., but choose resistance of about 5 to  $10\Omega$ . In addition, set resistance with utmost care to voltage drop caused by inrush current that flows when the motor is started.

- 2) Resistors and zener diode for the output high voltage setting, R2, R3 and ZD These are the resistors and zener diode used when output high voltage is set. Zener diode ZD is recommended to be used instead of resistor R3 when the power supply voltage is unstable.
- Stabilization capacitor for the power supply line, C1
   Please connect the capacitor of 1µF to 100µF for the stabilization of the power supply line, and confirm the motor
   operation.
- Phase compensating capacitor, C2, C3, C4, C5 Noise is generated in output pins or oscillation results in accord with the set mounting state such as power supply circuit, motor characteristics, PCB pattern artwork, etc. As noise oscillation measures, connect 0.01μF to 0.1μF capacitors.

#### Functional descriptions

1) Operation modes

Table 3 Logic table, DA024711-17 DA0230A								
IN1	IN2	IN3	OUT1	OUT2	OUT3	OPERATION		
L	L	L	L	L L		L L Brake (stop)		Brake (stop)
-		н						
н		L	Н	L OPE	OPEN*	Motor 1, forward (OUT1 > OUT2)		
	L	н	L	Н	OPEN*	Motor 1, reverse (OUT2 > OUT1)		
	Н	L	Н	OPEN*	L	Motor 2, forward (OUT1 > OUT3)		
	п	Н	L OPEI		Н	Motor 2, reverse (OUT3 > OUT1)		
Н	ц	L,,		Proko (stop)				
	H	Н	L	L	L	Brake (stop)		

Table 3 Logic table, BA6247FP-Y / BA6238A

\* OPEN is the off state of all output transistors. Please note that this is the state of the connected diodes, which differs from that of the mechanical relay.

⊖ vcc2

Ο Ουτ

03

Q6

Fig.13 BA6247FP-Y

01

VR

#### 2) Output high voltage setting

This function optionally sets output voltage by the VR pin and controls the motor rotating speed. However, when the output high voltage is set to a low level, consumption at IC increases. Carry out thermal design with sufficient margin incorporated with the power dissipation (Pd) under the actual application condition taken into account. Please do not to exceed the VCC1 and VCC2 voltage forced to the VR pin voltage.

a) BA6247FP-Y

The circuit diagram associated with the output high voltage setting VR pin is as per shown on the right. The output high and low voltages  $V_{OH}$  and  $V_{OL}$  are expressed by:

$$V_{OH} = VR + (V_{F(Q5)} + V_{F(Q4)}) - (V_{F(Q2)} + V_{F(Q3)}) \\ \approx VR$$

 $V_{OL} = V_{SAT(Q7)} + V_{F(Q6)}$ 

(Reference values;  $V_{SAT} \approx 0.2V$ ,  $V_F \approx 0.7V$ )

In addition, the relation of VREF voltage to output voltage is expressed by:

 $\begin{array}{l} VR < VCC1 \ - \ V_{SAT(Q1)} \ - \ V_{F(Q4)} \ - \ V_{F(Q5)} \\ VR < VCC2 \ - \ V_{SAT(Q3)} \ + \ (V_{F(Q2)} \ + V_{F(Q3)}) \ - \ ( \ V_{F(Q4)} \ + \ V_{F(Q5)} \ ) \end{array}$ 



$$\begin{split} & \mathsf{VR} > \mathsf{VCC1} - \mathsf{V}_{\mathsf{SAT}(Q1)} - \mathsf{V}_{\mathsf{F}(Q4)} - \mathsf{V}_{\mathsf{F}(Q5)} \\ & \mathsf{VR} > \mathsf{VCC2} - \mathsf{V}_{\mathsf{SAT}(Q3)} + \left(\mathsf{V}_{\mathsf{F}(Q2)} + \mathsf{V}_{\mathsf{F}(Q3)}\right) - \left(\mathsf{V}_{\mathsf{F}(Q4)} + \mathsf{V}_{\mathsf{F}(Q5)}\right) \\ & \mathsf{V}_{\mathsf{OH}} = \mathsf{VCC1} - \mathsf{V}_{\mathsf{SAT}(Q1)} - \mathsf{V}_{\mathsf{F}(Q2)} - \mathsf{V}_{\mathsf{F}(Q3)} \end{split}$$

 $V_{OH} = VCC2 - V_{SAT(Q3)}$ 

#### b) BA6238A

The circuit diagram associated with the output high voltage setting VR pin is as per shown on the right. The output high and low voltages  $V_{OH}$  and  $V_{OL}$  are expressed by:

$$V_{OH} = VR + (V_{F(Q5)} + V_{F(Q4)}) - (V_{F(Q2)} + V_{F(Q3)})$$

$$\approx VR$$

 $V_{OL} = V_{SAT(Q7)} + V_{F(Q6)}$ 

(Reference values; V<sub>SAT</sub> ≈ 0.1V, V<sub>F</sub> ≈ 0.7V)

In addition, the relation of VREF voltage to output voltage is expressed by:

 $\begin{array}{l} VR < VCC1 \ - \ V_{SAT(Q1)} \ - \ V_{F(Q4)} \ - \ V_{F(Q5)} \\ VR < VCC2 \ - \ ( \ V_{SAT(Q2)} \ + \ V_{F(Q3)} \ ) \ + \ (V_{F(Q2)} \ + \ V_{F(Q3)}) \ - \ ( \ V_{F(Q4)} \ + \ V_{F(Q5)} \ ) \end{array}$ 

Therefore, when the VREF voltage condition is as follows, the output high voltage is restricted.

 $\begin{array}{l} VR > VCC1 \ - \ V_{SAT(Q1)} \ - \ V_{F(Q4)} \ - \ V_{F(Q5)} \\ VR > VCC2 \ - \ ( \ V_{SAT(Q2)} \ + \ V_{F(Q3)} \ ) \ + \ (V_{F(Q2)} \ + \ V_{F(Q3)}) \ - \ ( \ V_{F(Q4)} \ + \ V_{F(Q5)} \ ) \end{array}$ 

$$\label{eq:VOH} \begin{split} V_{OH} &= VCC1 \mbox{ - } V_{SAT(Q1)} \mbox{ - } V_{F(Q2)} \mbox{ - } V_{F(Q3)} \\ V_{OH} &= VCC2 \mbox{ - } V_{SAT(Q2)} \mbox{ - } V_{F(Q3)} \end{split}$$

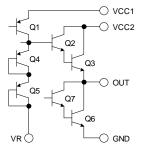


Fig.14 BA6238A

3) Control input conditions

The input threshold voltage has a positive temperature coefficient and is expressed by:

$$\frac{\Delta \text{VIH}}{\Delta \text{T}} = +2.8 \text{mV} / \text{°C} \qquad \frac{\Delta \text{VIL}}{\Delta \text{T}} = +1.6 \text{mV} / \text{°C}$$

The input pin is pulled up at about  $15k\Omega$ .

Set input voltage with care not to exceed the maximum input voltage (internal voltage regulator).

BA6247FP-Y --- 6V BA6238A --- 5V

4) Switching of rotating direction (FWD/REV)

When the rotating direction is changed over by the motor rotating condition, switch the direction after the motor is temporarily brought to the BRAKE condition or OPEN condition. It is recommended to keep the relevant conditions as follows:

via BRAKE: Longer than braking time\*.

(\* the time required for the output L terminal to achieve potential below GND when brake is activated.)

The motor in no drive might be influenced momentarily because the all driver outputs low at the brake.

Interfaces

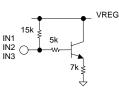


Fig. 15 IN1, IN2, IN3

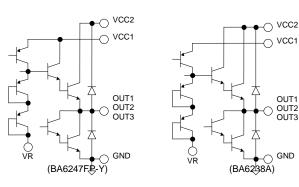


Fig.16 VCC1, VCC2, OUT1, OUT2, OUT3, VR, GND

#### Notes for use

#### 1) Absolute maximum ratings

Devices may be destroyed when supply voltage or operating temperature exceeds the absolute maximum rating. Because the cause of this damage cannot be identified as, for example, a short circuit or an open circuit, it is important to consider circuit protection measures – such as adding fuses – if any value in excess of absolute maximum ratings is to be implemented.

#### 2) Connecting the power supply connector backward

Connecting the power supply in reverse polarity can damage the IC. Take precautions against reverse polarity when connecting the power supply lines, such as adding an external direction diode.

#### 3) Power supply lines

Return current generated by the motor's Back-EMF requires countermeasures, such as providing a return current path by inserting capacitors across the power supply and GND (10µF, ceramic capacitor is recommended). In this case, it is important to conclusively confirm that none of the negative effects sometimes seen with electrolytic capacitors – including a capacitance drop at low temperatures - occurs. Also, the connected power supply must have sufficient current absorbing capability. Otherwise, the regenerated current will increase voltage on the power supply line, which may in turn cause problems with the product, including peripheral circuits exceeding the absolute maximum rating. To help protect against damage or degradation, physical safety measures should be taken, such as providing a voltage clamping diode across the power supply and GND.

#### 4) Electrical potential at GND

Keep the GND terminal potential to the minimum potential under any operating condition. In addition, check to determine whether there is any terminal that provides voltage below GND, including the voltage during transient phenomena. When both a small signal GND and high current GND are present, single-point grounding (at the set's reference point) is recommended, in order to separate the small signal and high current GND, and to ensure that voltage changes due to the wiring resistance and high current do not affect the voltage at the small signal GND. In the same way, care must be taken to avoid changes in the GND wire pattern in any external connected component.

#### 5) Thermal design

Use a thermal design that allows for a sufficient margin in light of the power dissipation (Pd) under actual operating conditions.

#### 6) Inter-pin shorts and mounting errors

Use caution when positioning the IC for mounting on printed circuit boards. The IC may be damaged if there is any connection error, or if pins are shorted together.

#### 7) Operation in strong electromagnetic fields

Using this product in strong electromagnetic fields may cause IC malfunctions. Use extreme caution with electromagnetic fields.

#### 8) ASO - Area of Safety Operation

When using the IC, set the output transistor so that it does not exceed absolute maximum ratings or ASO.

#### 9) Built-in thermal shutdown (TSD) circuit

The TSD circuit is designed only to shut the IC off - when BA6238A driver outputs low - to prevent thermal runaway. It is not designed to protect the IC or guarantee its operation in the presence of extreme heat. Do not continue to use the IC after the TSD circuit is activated, and do not operate the IC in an environment where activation of the circuit is assumed.

	BA6247FP-Y	BA6238A	
Т <sub>ОN</sub> [°С]	170	150	
T <sub>HYS</sub> [°C]	30	50	

\*All temperature values are typical.

#### 10) Capacitor between output and GND

In the event a large capacitor is connected between the output and GND, if VCC and VIN are short-circuited with 0V or GND for any reason, the current charged in the capacitor flows into the output and may destroy the IC. Use a capacitor smaller than 1µF between output and GND.

#### 11) Testing on application boards

When testing the IC on an application board, connecting a capacitor to a low impedance pin subjects the IC to stress. Therefore, always discharge capacitors after each process or step. Always turn the IC's power supply off before connecting it to or removing it from the test setup during the inspection process. Ground the IC during assembly steps as an antistatic measure. Use similar precaution when transporting or storing the IC.

#### 12) Switching of rotating direction (FWD/REV)

When the rotating direction is changed over by the motor rotating condition, switch the direction after the motor is temporarily brought to the BRAKE condition or OPEN condition. It is recommended to keep the relevant conditions as follows:

#### via BRAKE: Longer than braking time\*.

(\* the time required for the output L terminal to achieve potential below GND when brake is activated.)

#### 13) Regarding the input pin of the IC

This monolithic IC contains P+ isolation and P substrate layers between adjacent elements, in order to keep them isolated. P-N junctions are formed at the intersection of these P layers with the N layers of other elements, creating a parasitic diode or transistor. For example, the relation between each potential is as follows:

When GND > Pin A and GND > Pin B, the P-N junction operates as a parasitic diode.

When GND > Pin B, the P-N junction operates as a parasitic transistor.

Parasitic diodes inevitably occur in the structure of the IC. The operation of parasitic diodes can result in mutual interference among circuits, as well as operating malfunctions and physical damage. Therefore, do not use methods by which parasitic diodes operate, such as applying a voltage lower than the GND (P substrate) voltage to an input pin.

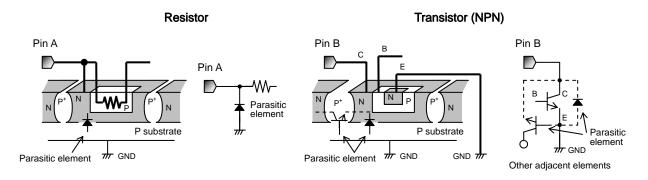
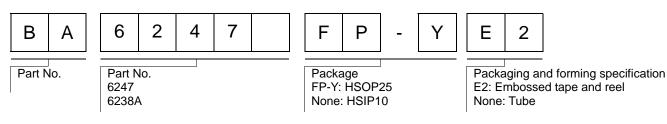
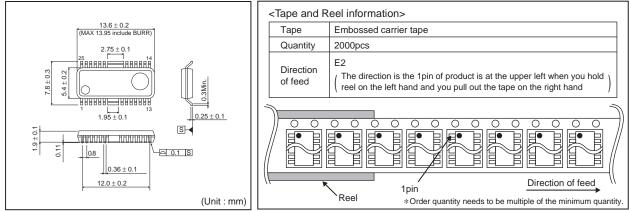


Fig.17 Example of monolithic IC structure

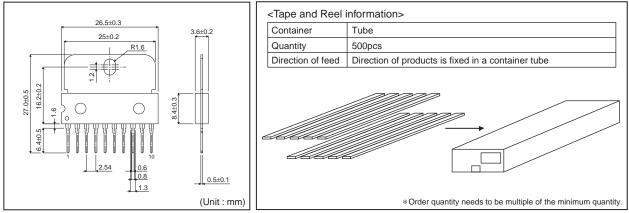
## Ordering part number



## HSOP25



HSIP10



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CLASSⅣ	CLASSⅢ	CLASSⅢ	CLASSII

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  - [e] Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
  - [f] Sealing or coating our Products with resin or other coating materials
  - [g] Use of our Products without cleaning residue of flux (even if you use no-clean type fluxes, cleaning residue of flux is recommended); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
  - [h] Use of the Products in places subject to dew condensation
- 4. The Products are not subject to radiation-proof design.
- 5. Please verify and confirm characteristics of the final or mounted products in using the Products.
- 6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse. is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- 7. De-rate Power Dissipation (Pd) depending on Ambient temperature (Ta). When used in sealed area, confirm the actual ambient temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
- 9. ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

#### Precaution for Mounting / Circuit board design

- 1. When a highly active halogenous (chlorine, bromine, etc.) flux is used, the residue of flux may negatively affect product performance and reliability.
- 2. In principle, the reflow soldering method must be used; if flow soldering method is preferred, please consult with the ROHM representative in advance.

For details, please refer to ROHM Mounting specification

#### **Precautions Regarding Application Examples and External Circuits**

- 1. If change is made to the constant of an external circuit, please allow a sufficient margin considering variations of the characteristics of the Products and external components, including transient characteristics, as well as static characteristics.
- 2. You agree that application notes, reference designs, and associated data and information contained in this document are presented only as guidance for Products use. Therefore, in case you use such information, you are solely responsible for it and you must exercise your own independent verification and judgment in the use of such information contained in this document. ROHM shall not be in any way responsible or liable for any damages, expenses or losses incurred by you or third parties arising from the use of such information.

#### **Precaution for Electrostatic**

This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of lonizer, friction prevention and temperature / humidity control).

#### **Precaution for Storage / Transportation**

- 1. Product performance and soldered connections may deteriorate if the Products are stored in the places where:
  - [a] the Products are exposed to sea winds or corrosive gases, including Cl2, H2S, NH3, SO2, and NO2
  - [b] the temperature or humidity exceeds those recommended by ROHM
  - [c] the Products are exposed to direct sunshine or condensation
  - [d] the Products are exposed to high Electrostatic
- 2. Even under ROHM recommended storage condition, solderability of products out of recommended storage time period may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is exceeding the recommended storage time period.
- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- 4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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