

# 4-channel BTL driver for CD

## BA6998FP

The BA6998FP ICs contain a 4-channel BTL driver, 5V regulator (which requires an externally connected PNP transistor), multi-purpose operational amplifier, and reset output for use with CD players. Also equipped with an input pin for gain adjustment for all channels of the driver block so that the gain can be adjusted to the desired value for your application. Furthermore, a built-in level shift circuit exists to further reduce the amount of external components required.

### ●Features

- 1) 4ch Driver for BTL.
- 2) Small surface mounting power package (HSOP 28pin).
- 3) Adjustable gain by the external resistors.
- 4) Incorporates a thermal shut down circuit.
- 5) Incorporates a voltage regulator (5V) (PNP Tr is needed outside).
- 6) Incorporates an operational amplifier.
- 7) Incorporates a reset-output terminal.

### ●Applications

CD players, CD-ROM, and other optical disc equipment

### ●Absolute maximum ratings (Ta=25°C)

Parameter	Symbol	Limits	Unit
Supply voltage	V <sub>CC</sub>	13.5	V
Power dissipation	P <sub>D</sub>	1.7 *	W
Operating temperature	T <sub>opr</sub>	-35 to +85	°C
Storage temperature	T <sub>stg</sub>	-55 to +150	°C

\* Derating is done at 13.6mW/°C for operation above Ta=25°C.  
On less than 3% (percentage occupied by copper foil), 70 x 70mm<sup>2</sup>, t =1.6mm, glass epoxy mounting.

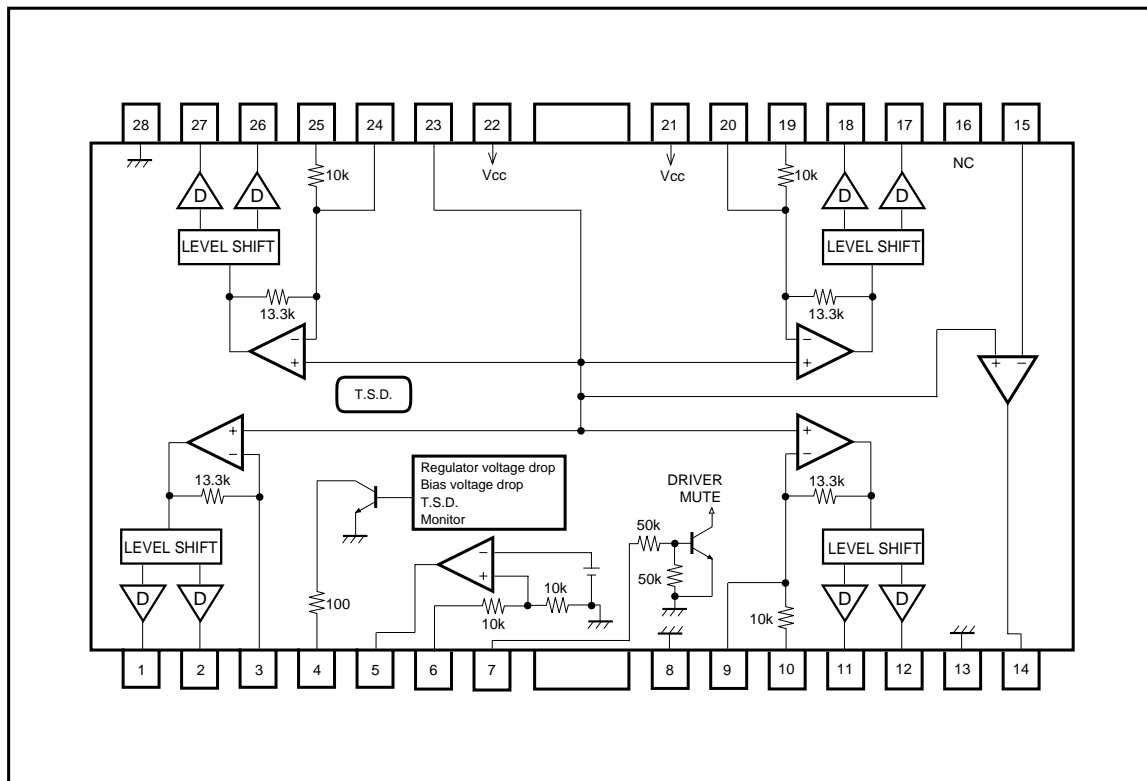
### ●Recommended operating conditions (Ta=25°C)

Parameter	Symbol	Limits	Unit
Supply voltage	V <sub>CC</sub>	6 to 9 *	V

\* The driver blocks operate on condition that supply voltage is over 5.5V.

## Optical disc ICs

## ●Block diagram



T.S.D : Thermal shut down  
D.BUF : Driver buffer

## ●Pin description

No	Symbol	Function	No	Symbol	Function
1	OUT1-B	Inverted output of CH1	15	OPIN-B	Inverting input for OP-amp.
2	OUT1-A	Noninverted output of CH1	16	NC	NC
3	IN1	Gain adjustment of CH1	17	OUT3-B	Inverted output of CH3
4	RESET	Output for reset	18	OUT3-A	Noninverted output of CH3
5	REG-B	Connection with BASE of PNP Tr	19	IN3	Input for CH3
6	REGOUT	Output for regulator *1	20	IN3'	Gain adjustment of CH3
7	MUTE	Mute control	21	VCC	Vcc
8	GND	Ground	22	VCC	Vcc
9	IN2'	Gain adjustment of CH2	23	VREFIN	Input for reference voltage (bias)
10	IN2	Input for CH2	24	IN4'	Gain adjustment of CH4
11	OUT2-A	Noninverted output of CH2	25	IN4	Input for CH4
12	OUT2-B	Inverted output of CH2	26	OUT4-A	Noninverted output of CH4
13	GND	Substrate ground	27	OUT4-B	Inverted output of CH4
14	OPOUT	Output for OP-amp.	28	GND	Substrate ground

\*1 Connect COLLECTOR of external PNP Tr.

## Optical disc ICs

## ●Electrical characteristics (Unless otherwise noted, Ta=25°C, Vcc=8V, f=1kHz, RL=8Ω)

Parameter	Symbol	Limit			Unit	Conditions
		Min.	Typ.	Max.		
Quiescent current	I <sub>Q</sub>	6.0	10.0	14.0	mA	R <sub>L</sub> =∞
Output offset voltage	V <sub>OO</sub>	-40	0	+40	mV	
High level output voltage	V <sub>OHD</sub>	5.60	6.0	-	V	
Low level output voltage	V <sub>OLD</sub>	-	1.20	1.45	V	
Closed loop voltage gain	G <sub>VC</sub>	7.40	8.40	9.40	dB	V <sub>IN</sub> =0.1V (except for CH1)
Ripple rejection	RR	-	60	-	dB	V <sub>IN</sub> =0.1Vrms, 100Hz
Slew rate	SR	-	1.0	-	V/μs	100kHz square wave output 3V <sub>P-P</sub>
Voltage for mute-off	V <sub>moff</sub>	2.0	-	-	V	
(REGULATOR)						
Output voltage	V <sub>REG</sub>	4.75	5.00	5.25	V	I <sub>L</sub> =100mA
Load regulation	V <sub>RL</sub>	-70	0	+10	mV	I <sub>L</sub> =0 to 200mA
Line regulation	V <sub>VCC</sub>	-10	0	+35	mV	I <sub>L</sub> =100mA, V <sub>CC</sub> =6V to 9V
(OPERATIONAL AMPLIFIER)						
Offset voltage	V <sub>OFOF</sub>	-5	0	+5	mV	
Input bias current	I <sub>BIAS</sub>	-	-	300	nA	
High level output voltage	V <sub>OHO</sub>	7.0	-	-	V	
Low level output voltage	V <sub>OLO</sub>	-	-	1.1	V	
Output source current	I <sub>SO</sub>	10	40	-	mA	Output short to GND by 50Ω
Output sink current	I <sub>SIN</sub>	10	50	-	mA	Output short to V <sub>CC</sub> by 50Ω
Open loop voltage gain	G <sub>VO</sub>	-	78	-	dB	V <sub>IN</sub> = -75dBV, 1kHz
Slew rate	SR <sub>OP</sub>	-	1	-	V/μs	100kHz square wave output 4V <sub>P-P</sub>
Ripple rejection	RR <sub>OP</sub>	50	65	-	dB	V <sub>IN</sub> = -20dBV, 100Hz
(RESET)						
Threshold (RESET ON)	V <sub>THR</sub>	-	4.0	-	V	Sense of regulator output
Output voltage (RESET ON)	V <sub>RON</sub>	-	-	0.5	V	Output short to 5V by 10kΩ

\* This product is not designed for protection against radioactive rays.

Optical disc ICs

●Application circuit

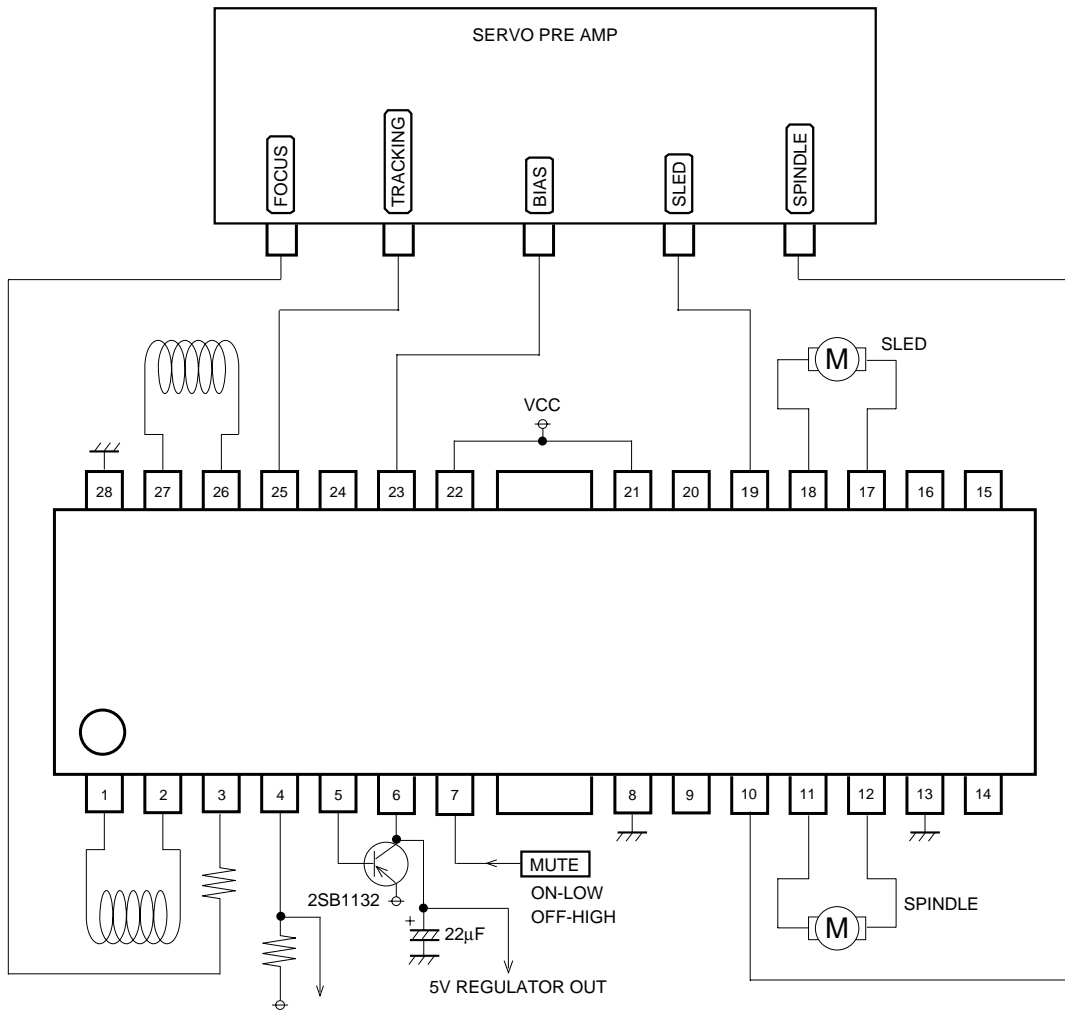


Fig.1

Optical disc ICs

●Operation notes

(1) Relation between mute and reset.

Functions	MUTE	RESET
Regulator drop	ON	'H' to 'L'
Bias drop	ON	'H' to 'L'
Thermal shut down	ON	'H' to 'L'
Mute (Pin.15)	ON	constant

Regulator drop . . . In case regulator voltage below 4.0V(Typ.), output current is muted and reset output is 'L'.

Next time regulator voltage rises to 4.2V(Typ.), output current is active and reset output is 'H'.

Bias drop . . . In case bias-pin (Pin.23) voltage falls below 0.7V(Typ.), output current is muted and reset output is 'L'. Make sure that under normal operating conditions, this pin is at 1.2V or above.

T.S.D. . . . In case IC chip temperature rise to 175°C (Typ.), thermal shut down circuit operates and output current is muted and reset output is 'L'.

Next time IC chip temperature falls below 150°C (Typ.),the driver blocks start..

Mute (Pin.7) . . . In case mute-pin (Pin.7) voltage is under 0.5V or opened, output current is muted.

(2) The output current is muted in the event of a T.S.D., mute-on, a bias voltage drop, or a regulator voltage drop.

Only the drivers are muted. When muted, the internal bias voltage of the output pin becomes roughly (V<sub>CC</sub>-VF/2).

(3) Insert the by-pass capacitor between V<sub>CC</sub> and GND as possible as near (about 0.1μF).

(4) The capacitor between regulator output (Pin.6) and GND combines with prevention of oscillation. The capacitor with small temperature coefficient should be recommendable.

(5) When it is not to use regulator, output of regulator (Pin.6) is connected with V<sub>CC</sub> and base terminal of PNP(Pin.5) is opened.

(6) Fin should be connected to outside ground.

(7) Please lay out pattern with care, as Pin.8 GND electric potential is not below SUBGND electric potential include transitional condition.Because only Pin.8 GND of three GND do not connect with IC's substrate.

●Electrical characteristics curves

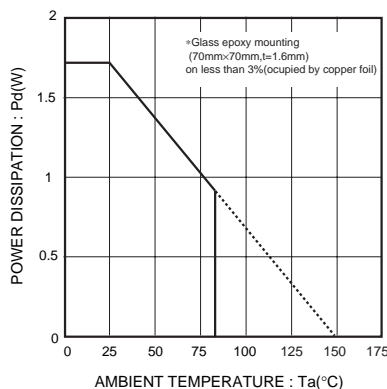


Fig.2 Thermal dissipation curve

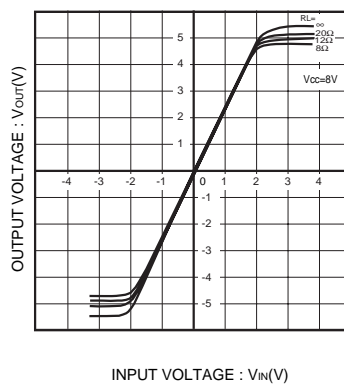


Fig.3 Driver input/output characteristics (When load changed)

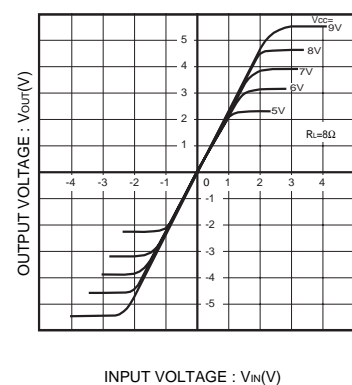


Fig.4 Driver input/output characteristics (When supply voltage changed)

Optical disc ICs

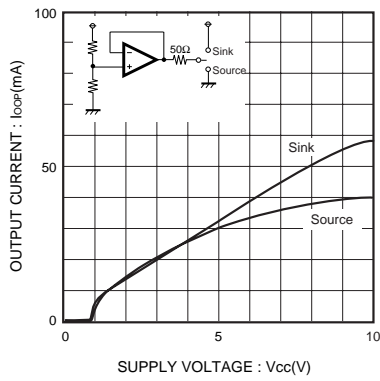


Fig.5 Power supply voltage vs. op-amplifier operating current

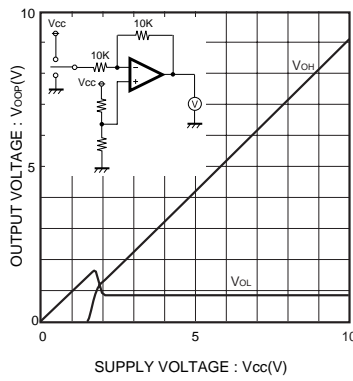


Fig.6 Power supply voltage vs. op-amplifier "H" and "L" level output voltage

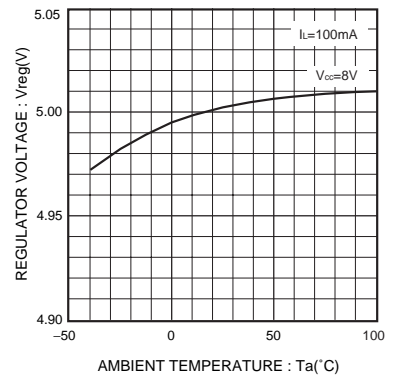


Fig.7 Regulator voltage vs. temperature characteristics

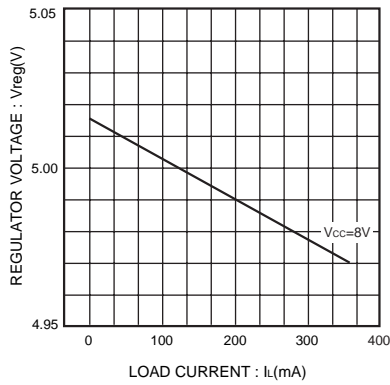
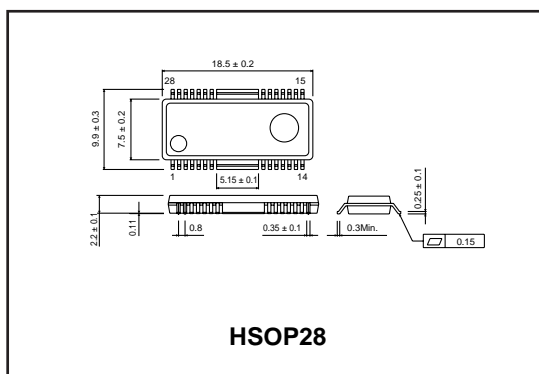


Fig.8 Load current vs. regulator voltage

●External dimensions (Unit : mm)



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