

Structure	: Silicon Monolithic Integrated Circuit
Product name	: Single Circuit Video Signal Switchers
Туре	BA7653AF
Features	 1) Clamp capacitor can be made low capacitance (Min. 3000 pF Recommended 0.01 μ F (FZ) ceramic capacitor) 2) Low voltage operation is possible (Operating range 4.0V~7.0V) 3) Can be used superimposed since switching speed of SW is fast and there is little switching noise (Typ. 70ns) 4) Low power consumption (Typ. 25mW when Vcc=5V)

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 5) Large dynamic range (Typ. 3.1V_{P-P} when Vcc=5V)
- 6) Good frequency characteristics (Typ. 10 MHz 0 dB)
- 7) Low interchannel crosstalk (Typ. -70 dB)
- 8) Even when Vcc is not being applied, a voltage can be applied to the CTL pin.

OAbsolute Maximum Ratings (Ta=25°C)

Parameter	Symbol	Limits	Unit
Supply voltage	Vcc	9.0	V
Power dissipation	Pd	500 *	mW
Operating temperature	Topr	-25~+75	°C
Storage temperature	Tstg	-55~+125	°C

* Deratings is done at 5.0mW/℃ above Ta=25℃

(When mounted on a $50 \text{ mm} \times 50 \text{ mm}$ PCB board)

OOperating Range (Ta=25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit
Power supply voltage	Vcc	4.0	5.0	7.0	V
* This product is not dea	signed for	arotectio	n against	radioact	ivo rovo

This product is not designed for protection against radioactive rays.

Application example

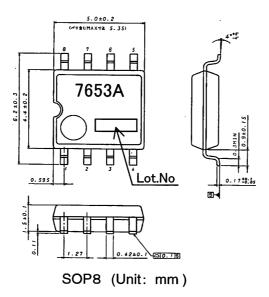
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Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Circuit current	lcc	2.7	5.0	8.2	mA	
Maximum output level	Vom	2.6	3.1	-	Vp-p	f=1kHz,THD=1.0%
Voltage gain	GV	-0.5	0	0.5	dB	f=1MHz,VIN=1.0Vp-p
Interchannel crosstalk	Cta	-	-70	-	dB	f=4.43MHz,VIN=1.0Vp-p
Frequency characteristic	Gf	-3.0	0	1.0	dB	f=10MHz/1MHz,VIN=1.0Vp-p
CTL switching voltage	Vth L	-	-	1.0		Vcc-CTL Threshold Level
	Vth H	2.5	-	-	V	$Vth = \frac{Vcc - V_F}{45} \times 20 (V)$
Differential phase	DG	-	0	0.5	%	Vin=1.0Vp-p, standard staircase signal
Differential gain	DP	-	0.5	1.0	deg	Vin=1.0Vp-p, standard staircase signal

OElectrical characteristics (Unless otherwise noted, Ta= 25°C, Vcc=5.0V)

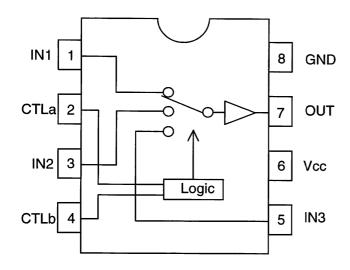
OOuter dimensions





OBlock diagram

OPin number and pin name



Pin No.	Pin name
1	IN1
2	CTLa
3	IN2
4	CTLb
5	IN3
6	Vcc
7	OUT
8	GND

OCautions on use

1) Absolute maximum ratings

If applied voltage, operating temperature range, or other absolute maximum ratings are exceeded, the LSI may be damaged. Do not apply voltages or temperatures that exceed the absolute maximum ratings. If you think of a case in which absolute maximum ratings are exceeded, enforce fuses or other physical safety measures and investigate how not to apply the conditions under which absolute maximum ratings are exceeded to the LSI.

2) GND potential

Make the GND pin voltage such that it is the lowest voltage even when operating below it. Actually confirm that the voltage of each pin does not become a lower voltage than the GND pin, including transient phenomena.

3) Thermal design

Perform thermal design in which there are adequate margins by taking into account the allowable power dissipation in actual states of use.

4) Shorts between pins and miss-installation

When mounting the LSI on a board, pay adequate attention to orientation and placement discrepancies of the LSI. If it is miss-installed and the power is turned on, the LSI may be damaged. It also may be damaged if it is shorted by a foreign substance coming between pins of the LSI or between a pin and a power supply or a pin and a GND.

5) Operation in strong magnetic fields

Adequately evaluate use in a strong magnetic field, since there is a possibility of malfunction.

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