

BALF-CC25-02D3

Datasheet – production data

50 ohm, conjugate match to CC2541 transformer balun

Flip-Chip package 4 bumps

Features

- 2.45 GHz balun with integrated matching network
- Matching optimized for following CC2541
- Low insertion loss
- Low amplitude imbalance
- Low phase imbalance
- Coated Flip-Chip on glass
- Small footprint: < 0.88 mm²

Benefits

- Very low profile
- High RF performance
- PCB space saving versus discrete solution
- BOM count reduction
- Efficient manufacturability

Description

STMicroelectronics BAL-CC25-02D3 is an ultra miniature balun which integrates a matching network in a monolithic glass substrate. This has been customized for the CC2541 RF transceivers.

It's a design using STMicroelectronics IPD (integrated passive device) technology on nonconductive glass substrate to optimize RF performance.

Figure 1. Pin configuration (top view)

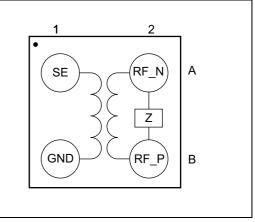
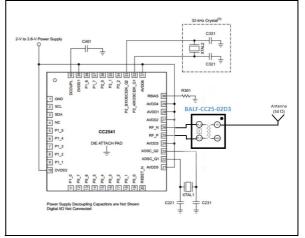


Figure 2. Application schematic (top view)



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This is information on a product in full production.

1 Characteristics

Symbol	Parameter	Value	Unit
P _{IN}	Input power RF _{IN}	20	dBm
	ESD ratings MIL STD883C (HBM: C = 100 pF, R = 1.5Ω , air discharge)	2000	
V _{ESD}	ESD ratings machine model (MM: C = 200 pF, R = 25 Ω , L = 500 nH)	500	V
	ESD ratings charged device model (CDM, JESD22-C101D)	500	
T _{OP}	Operating temperature	-40 to + 105	°C

Symbol	Parameter		Value		
	i urumotor	Min.	Тур.	Max.	Unit
Z _{OUT}	Nominal differential output impedance	Conjuga	Conjugate match to CC2541		0
Z _{IN}	Nominal input impedance 50			Ω	
F	Frequency range (bandwidth)	2379		2507	
١ _L	Insertion loss in bandwidth		1.6	1.8	dB
R_{L_SE}	Single ended return loss in bandwidth	9	10		dB
$R_{L_{DIFF}}$	Differential ended return loss in bandwidth	9	17		dB
Φ_{imb}	Phase imbalance		7		0
A _{imb}	Amplitude imbalance		0.6		dB



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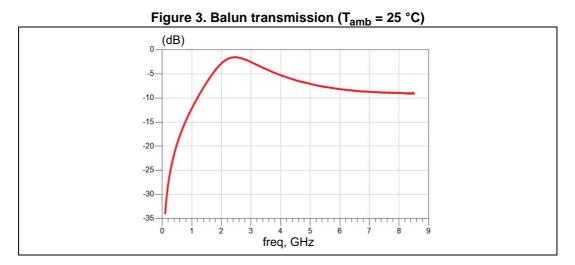


Figure 4. Insertion loss (T_{amb} = 25 °C)

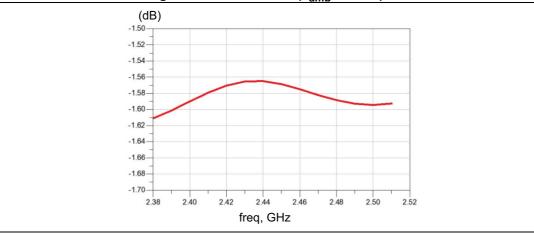
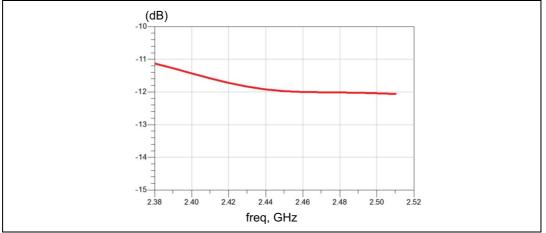


Figure 5. Return loss on SE port (T_{amb} = 25 °C)



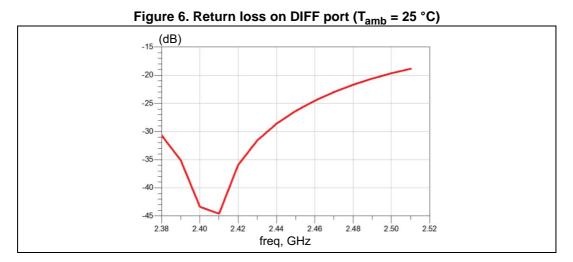
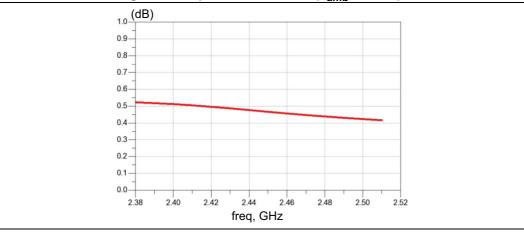
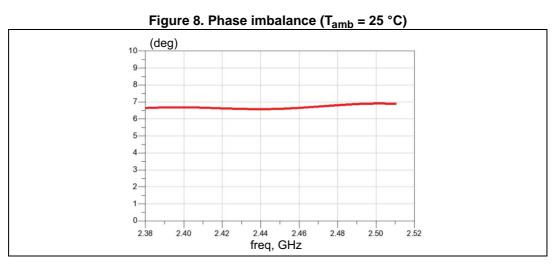


Figure 7. Amplitude imbalance ($T_{amb} = 25 \text{ °C}$)





2 Package information

- Epoxy meets UL94, V0
- Lead-free package

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK[®] packages, depending on their level of environmental compliance. ECOPACK[®] specifications, grade definitions and product status are available at: *www.st.com*. ECOPACK[®] is an ST trademark.

2.1 Flip-Chip package information

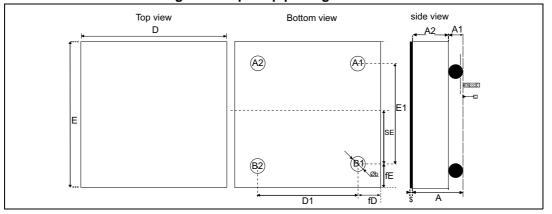
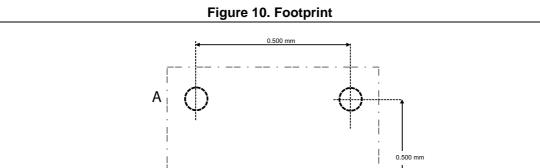


Figure 9. Flip-Chip package outline

Table 3. Flip-Chip package mechanical data					
Description	Min	Turn			

Parameter	Description	Min.	Тур.	Max.	Unit
А	Bump height + substrate thickness	0.570	0.630	0.690	mm
A1	Bump height	0.155	0.205	0.255	mm
A2	Substrate thickness		0.400		mm
b	Bump diameter	0.215	0.255	0.295	mm
D	Y dimension of the die	0.890	0.940	0.990	mm
D1	Y pitch		0.500		mm
E	X dimension of the die	0.890	0.940	0.990	mm
E1	X pitch		0.500		mm
SE			0.250		mm
fD	Distance from bump to edge of die on Y axis		0.220		mm
fE	Distance from bump to edge of die on X axis		0.220		mm
ссс				0.05	mm
\$			0.025		mm



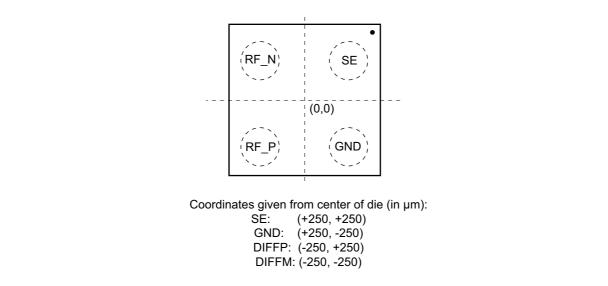




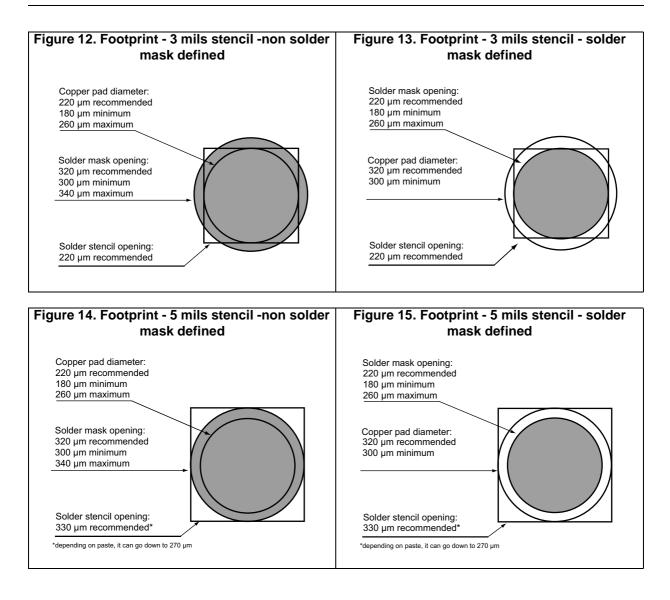
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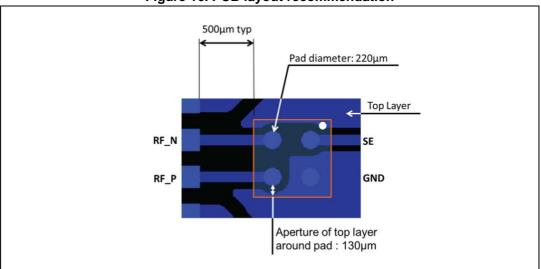
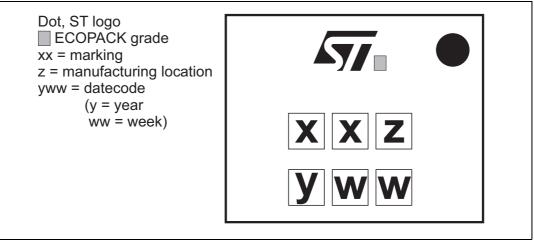


Figure 16. PCB layout recommendation

Figure 17. Marking



Note:

More information is available in the STMicroelectronics Application note: AN2348 Flip-Chip: "Package description and recommendations for use"



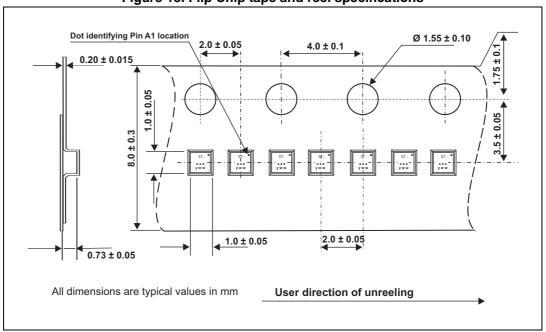
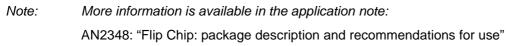


Figure 18. Flip Chip tape and reel specifications





3 Ordering information

Order code	Marking	Package	Weight	Base qty	Delivery mode	
BAL-CC25-02D3	TE	Flip Chip	1.07 mg	5000	Tape and reel (7")	

4 Revision history

Table 5. Document revision histor

Date	Revision	Changes
17-Nov-2015	1	Initial release



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